

General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® series is integrated with fast recovery diode (FRD) to minimize reverse recovery time. It is suitable for resonant switching topologies to reach higher efficiency, higher reliability and smaller form factor.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity



Applications

- LED lighting
- Telecom
- Adapter
- Sever
- Solar/UPS

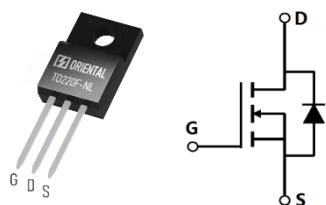
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	700	V
I_D , pulse	72	A
$R_{DS(ON)}$, max @ $V_{GS}=10V$	150	mΩ
Q_g	30.6	nC
PD	34	W

Marking Information

Product Name	Package	Marking
OSG70R150FT3F_NB	TO220F-NL	OSG70R150FT3

Package & Pin Information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	700	V
Gate-source voltage (static)	V_{GS}	± 20	V
Gate-source voltage (dynamic)		± 30	V
Continuous drain current ¹⁾ , $T_C=25^\circ\text{C}$	I_D	24	A
Continuous drain current ¹⁾ , $T_C=100^\circ\text{C}$		15	
Pulsed drain current ²⁾ , $T_C=25^\circ\text{C}$	$I_{D, \text{pulse}}$	72	A
Continuous diode forward current ¹⁾ , $T_C=25^\circ\text{C}$	I_S	24	A
Diode pulsed current ²⁾ , $T_C=25^\circ\text{C}$	$I_{S, \text{pulse}}$	72	A
Power dissipation ³⁾ , $T_C=25^\circ\text{C}$	P_D	34	W
Single pulsed avalanche energy ⁴⁾	E_{AS}	192	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\dots 400\text{ V}$	dv/dt	100	V/ns
Reverse diode dv/dt, $V_{DS}=0\dots 400\text{ V}$, $I_{SD} \leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	3.67	$^\circ\text{C/W}$
Thermal resistance, junction-ambient	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$

Electrical Characteristics at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	700			V	$V_{GS}=0\text{ V}$, $I_D=1\text{ mA}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.9		3.9	V	$V_{DS}=V_{GS}$, $I_D=1\text{ mA}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		104	150	$\text{m}\Omega$	$V_{GS}=10\text{ V}$, $I_D=12\text{ A}$
			264			$V_{GS}=10\text{ V}$, $I_D=12\text{ A}$, $T_j=150^\circ\text{C}$
Gate-source leakage current	I_{GS}			100	nA	$V_{GS}=20\text{ V}$
				-100		$V_{GS}=-20\text{ V}$
Drain-source leakage current	I_{DS}			1	μA	$V_{DS}=700\text{ V}$, $V_{GS}=0\text{ V}$
Gate resistance	R_G		14		Ω	$f=1\text{ MHz}$, Open drain

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{iss}		1447		pF	V _{GS} =0 V, V _{DS} =50 V, f=100 kHz
Output capacitance	C _{oss}		96.8		pF	
Reverse transfer capacitance	C _{rss}		2		pF	
Effective output capacitance, energy related	C _{o(er)}		65		pF	
Effective output capacitance, time related	C _{o(tr)}		394		pF	V _{GS} =0 V, V _{DS} =0 V-400 V
Turn-on delay time	t _{d(on)}		16.4		ns	V _{GS} =10 V, V _{DS} =400 V, R _G =2 Ω, I _D =12 A
Rise time	t _r		20.4		ns	
Turn-off delay time	t _{d(off)}		70.6		ns	
Fall time	t _f		18		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		30.6		nC	V _{GS} =10 V, V _{DS} =400 V, I _D =12 A
Gate-source charge	Q _{gs}		6		nC	
Gate-drain charge	Q _{gd}		11		nC	
Gate plateau voltage	V _{plateau}		5.2		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V _{SD}			1.2	V	I _S =24 A, V _{GS} =0 V
Reverse recovery time	t _{rr}		284.7		ns	
Reverse recovery charge	Q _{rr}		3.16		μC	
Peak reverse recovery current	I _{rrm}		19.8		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) V_{DD}=100 V, V_{GS}=10 V, L=75 mH, starting T_j=25 °C.

Electrical Characteristics Diagrams

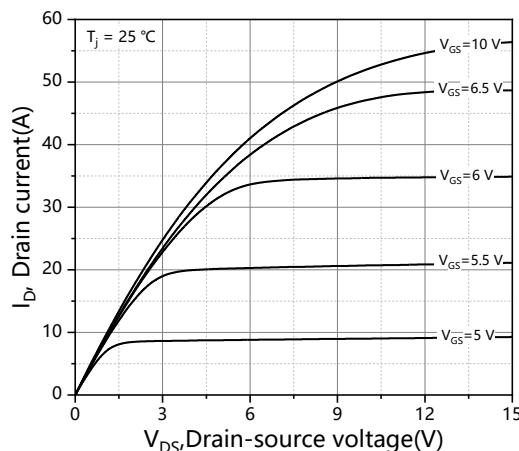


Figure 1. Typ. output characteristics $T_j=25^\circ\text{C}$

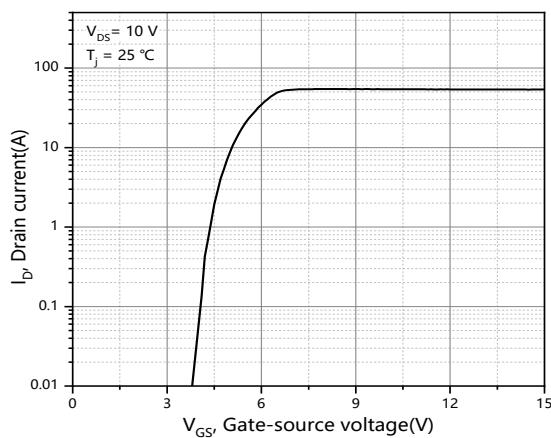


Figure 2. Typ. transfer characteristics

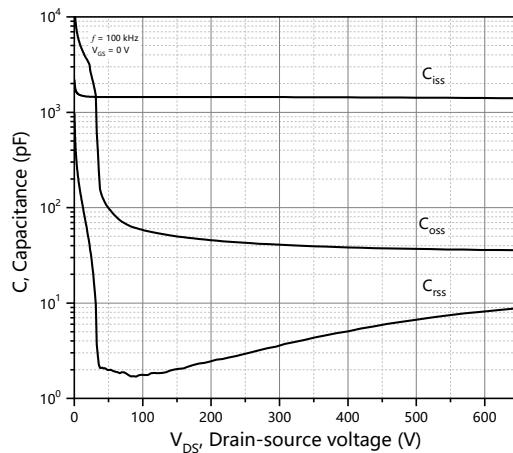


Figure 3. Typ. capacitances

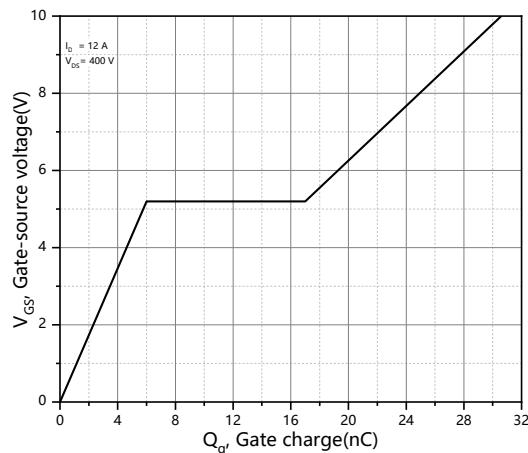


Figure 4. Typ. gate charge

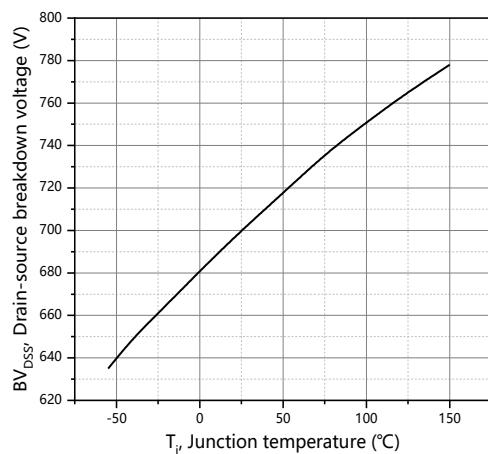


Figure 5. Drain-source breakdown voltage

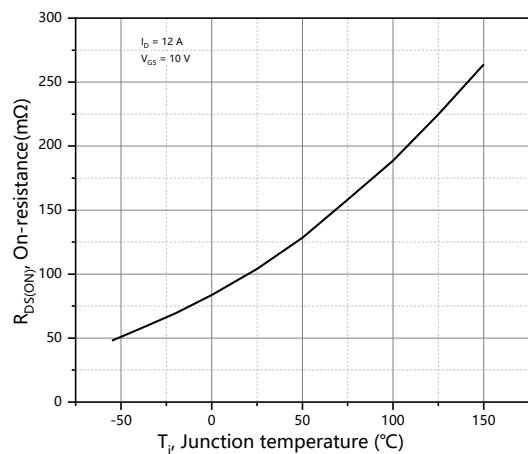


Figure 6. Drain-source on-state resistance

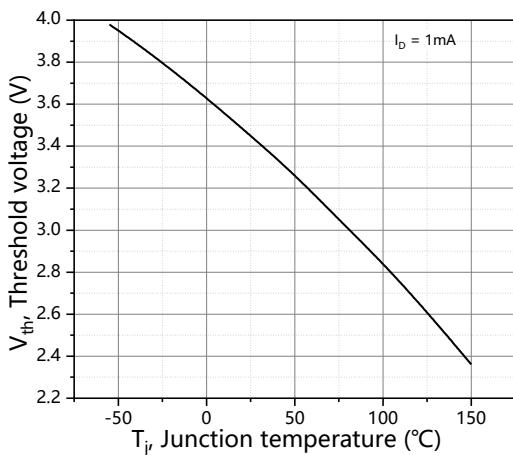


Figure 7. Threshold voltage

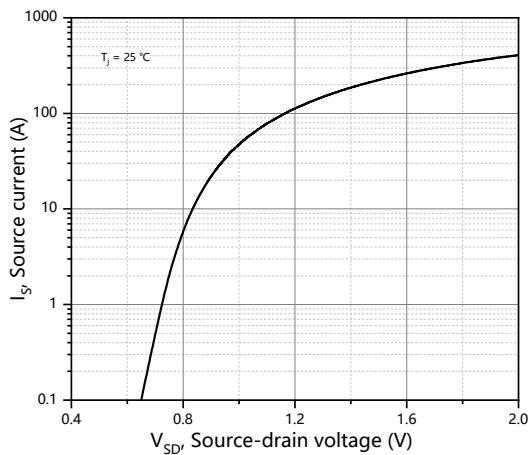


Figure 8. Forward characteristic of body diode

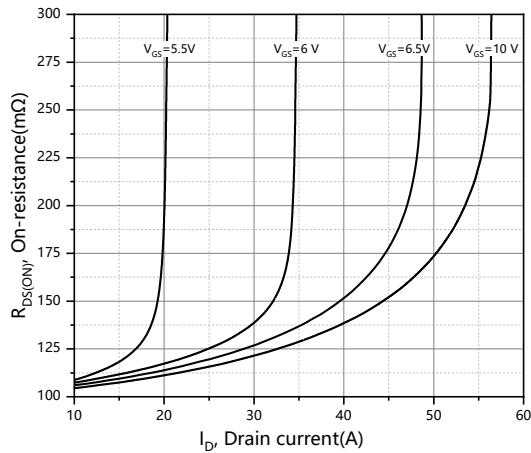


Figure 9. Drain-source on-state resistance

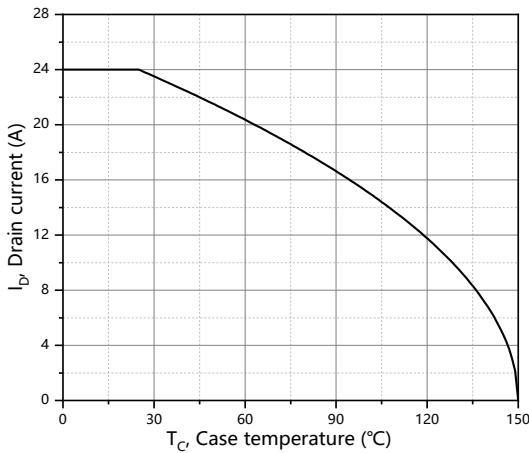


Figure 10. Drain current

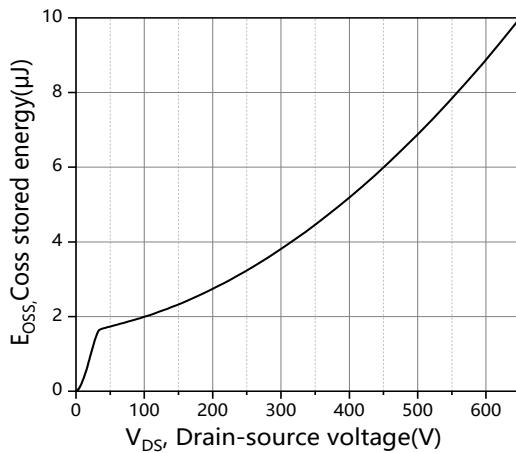


Figure 11. Typ. Coss stored energy

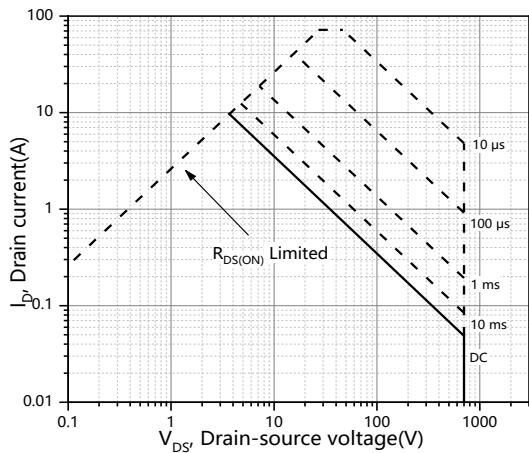


Figure 12. Safe operation area Tc=25°C

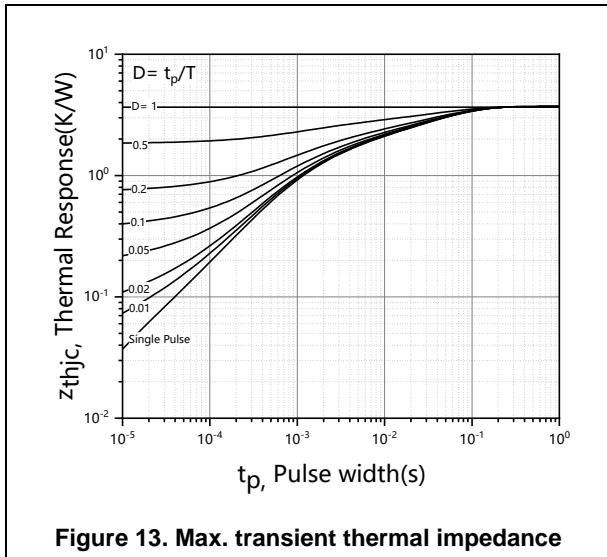


Figure 13. Max. transient thermal impedance

Test circuits and waveforms

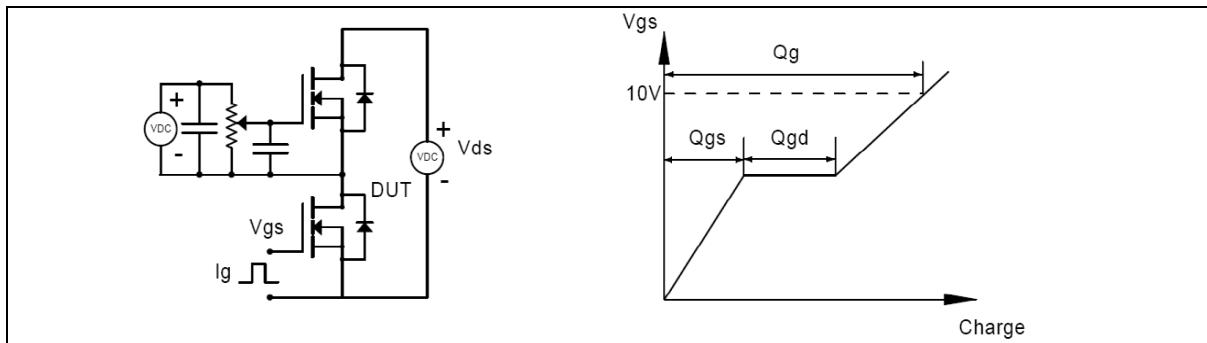


Figure 1. Gate charge test circuit & waveform

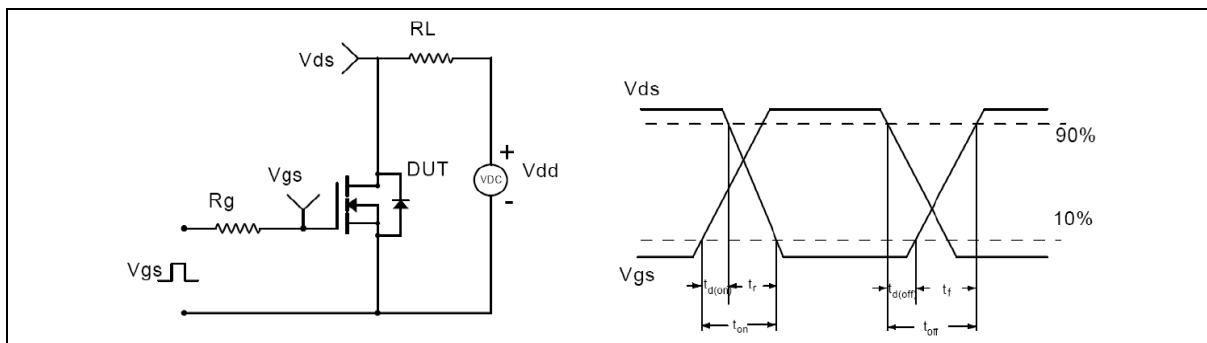


Figure 2. Switching time test circuit & waveforms

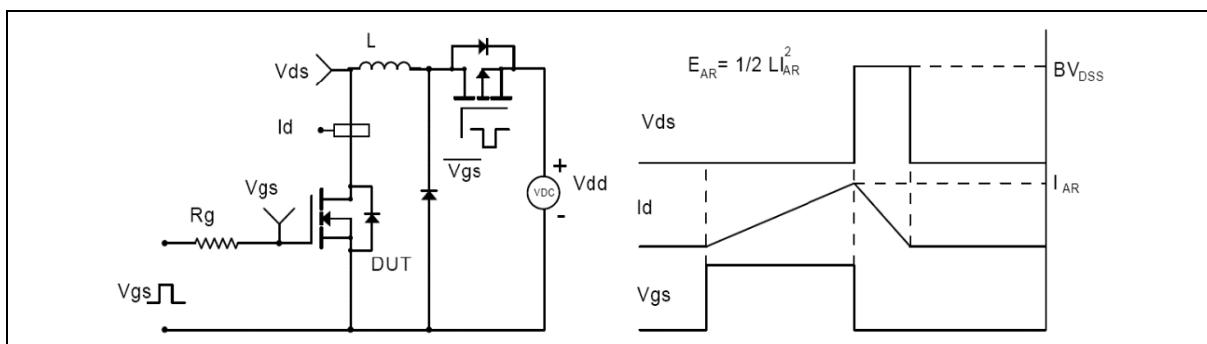


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

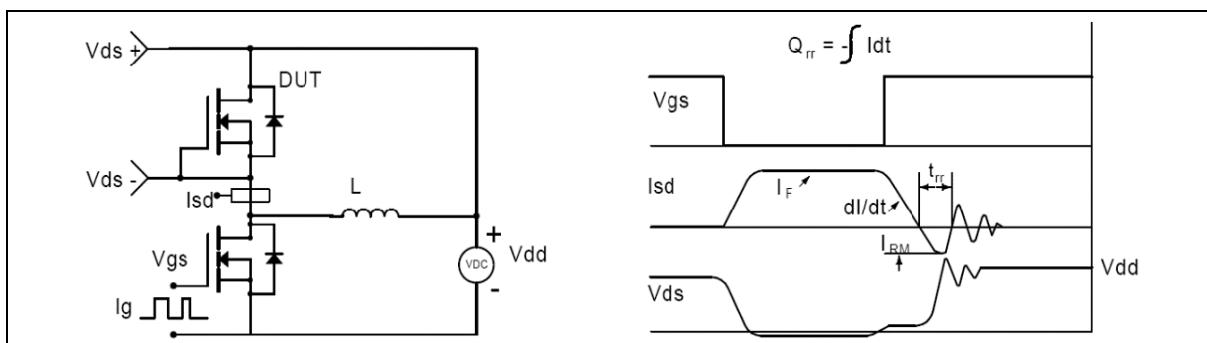
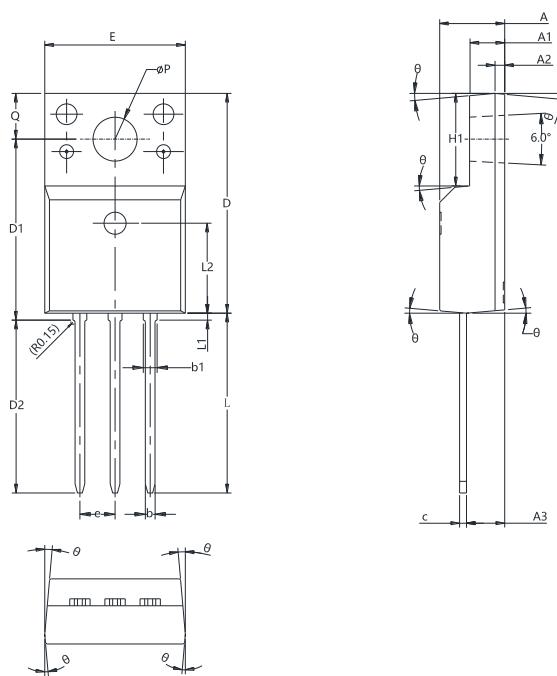


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.5	4.7	4.83
A1	2.34	2.54	2.74
A2	0.70 REF		
A3	2.56	2.76	2.93
b	0.6	-	0.8
b1	0.9	-	1.1
c	0.45	0.5	0.6
D	15.67	15.87	16.07
D1	12.87	13.07	13.27
D2	12.28	12.48	12.68
E	9.96	10.16	10.36
e	2.54 BSC		
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	-	-	0.85
L2	6.50 REF		
ΦP	3.08	3.18	3.28
Q	3.20	-	3.40
θ1	1°	3°	5°

Version: TO220F-NL-J package outline dimension

Ordering Information

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO220F-NL-J	50	20	1000	5	5000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG70R150FT3F_NB	TO220F-NL	yes	yes	yes

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