

General Description

The GreenMOS[®] high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS[®] Z series is integrated with fast recovery diode (FRD) to minimize reverse recovery time. It is suitable for resonant switching topologies to reach higher efficiency, higher reliability and smaller form factor.

Features

- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Ultra-fast and robust body diode

GreenMOS[®]



Applications

- PC power
- Telecom power
- Server power
- EV Charger
- Motor driver

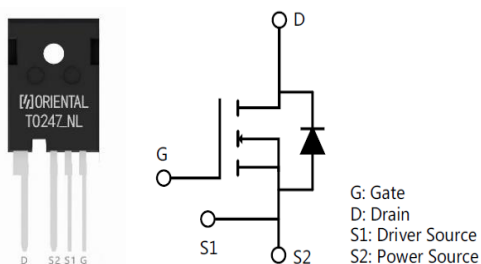
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	650	V
$I_{D, pulse}$	240	A
$R_{DS(ON), max @ V_{GS}=10V}$	38	m Ω
Q_g	175	nC

Marking Information

Product Name	Package	Marking
OSG65R038H4ZF_NB	TO247_NL	OSG65R038H4Z

Package & Pin Information



Absolute Maximum Ratings at $T_j=25^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Gate-source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_C=25^{\circ}\text{C}$	I_D	80	A
Continuous drain current ¹⁾ , $T_C=100^{\circ}\text{C}$		50	
Pulsed drain current ²⁾ , $T_C=25^{\circ}\text{C}$	$I_{D, \text{pulse}}$	240	A
Continuous diode forward current ¹⁾ , $T_C=25^{\circ}\text{C}$	I_S	80	A
Diode pulsed current ²⁾ , $T_C=25^{\circ}\text{C}$	$I_{S, \text{pulse}}$	240	A
Power dissipation ³⁾ , $T_C=25^{\circ}\text{C}$	P_D	500	W
Single pulsed avalanche energy ⁴⁾	E_{AS}	2900	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\ldots 480\text{ V}$	dv/dt	100	V/ns
Reverse diode dv/dt, $V_{DS}=0\ldots 480\text{ V}$, $I_{SD}\leq I_D$	dv/dt	50	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	$^{\circ}\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	0.25	$^{\circ}\text{C/W}$
Thermal resistance, junction-ambient	$R_{\theta JA}$	62	$^{\circ}\text{C/W}$

Electrical Characteristics at $T_j=25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	650			V	$V_{GS}=0\text{ V}$, $I_D=2\text{ mA}$
Gate threshold voltage	$V_{GS(th)}$	3.0		4.5	V	$V_{DS}=V_{GS}$, $I_D=2\text{ mA}$
Drain-source on-state resistance	$R_{DS(on)}$		0.032	0.038	Ω	$V_{GS}=10\text{ V}$, $I_D=40\text{ A}$
			0.083			$V_{GS}=10\text{ V}$, $I_D=40\text{ A}$, $T_j=150^{\circ}\text{C}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	I_{DSS}			10	μA	$V_{DS}=650\text{ V}$, $V_{GS}=0\text{ V}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		8537.9		pF	$V_{GS}=0\text{ V}$, $V_{DS}=50\text{ V}$, $f=100\text{ kHz}$
Output capacitance	C_{oss}		516.1		pF	
Reverse transfer capacitance	C_{rss}		13.7		pF	
Turn-on delay time	$t_{d(on)}$		55.9		ns	$V_{GS}=10\text{ V}$, $V_{DS}=400\text{ V}$, $R_G=2\ \Omega$, $I_D=40\text{ A}$
Rise time	t_r		121.2		ns	
Turn-off delay time	$t_{d(off)}$		114.2		ns	
Fall time	t_f		8.75		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		175.0		nC	$V_{GS}=10\text{ V}$, $V_{DS}=400\text{ V}$, $I_D=40\text{ A}$
Gate-source charge	Q_{gs}		40.1		nC	
Gate-drain charge	Q_{gd}		76.1		nC	
Gate plateau voltage	$V_{plateau}$		6.4		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	$I_S=80\text{ A}$, $V_{GS}=0\text{ V}$
Reverse recovery time	t_{rr}		180		ns	$I_S=30\text{ A}$, $di/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge	Q_{rr}		1.5		μC	
Peak reverse recovery current	I_{rrm}		15.2		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) $V_{DD}=100\text{ V}$, $V_{GS}=10\text{ V}$, $L=80\text{ mH}$, starting $T_j=25\text{ }^\circ\text{C}$.

Electrical Characteristics Diagrams

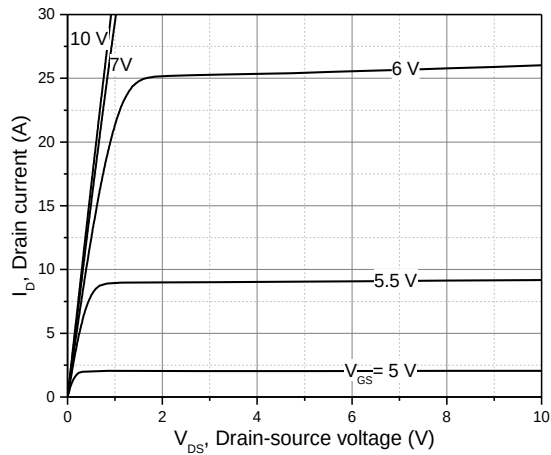


Figure 1. Typ. output characteristics

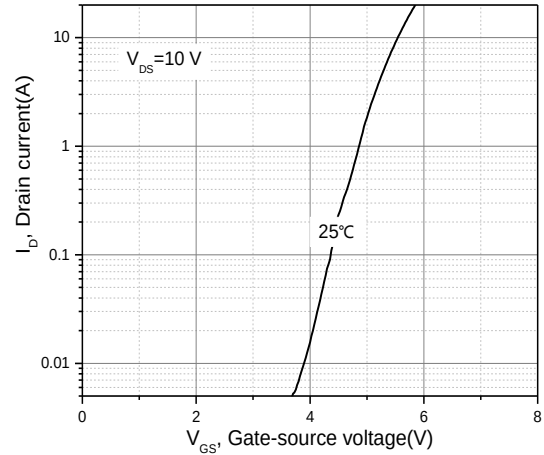


Figure 2. Typ. transfer characteristics

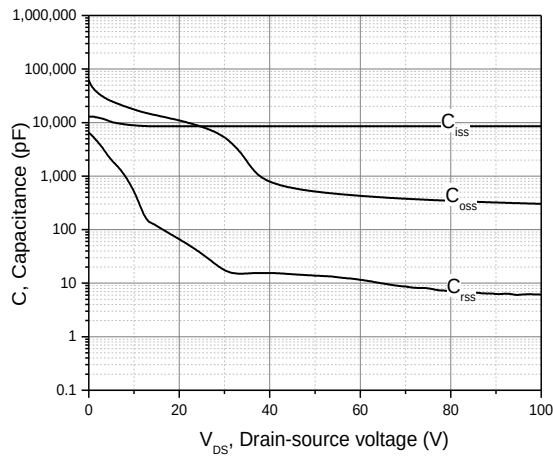


Figure 3. Typ. capacitances

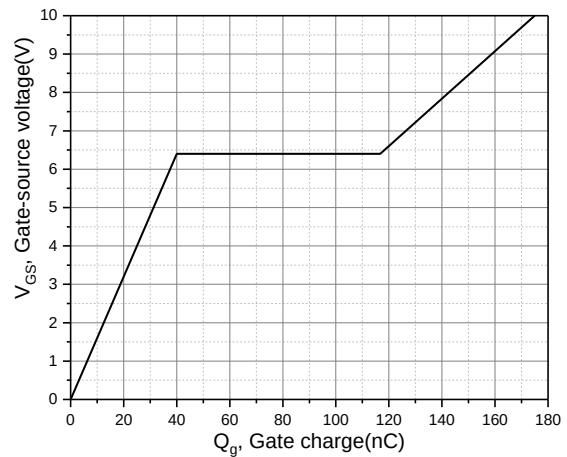


Figure 4. Typ. gate charge

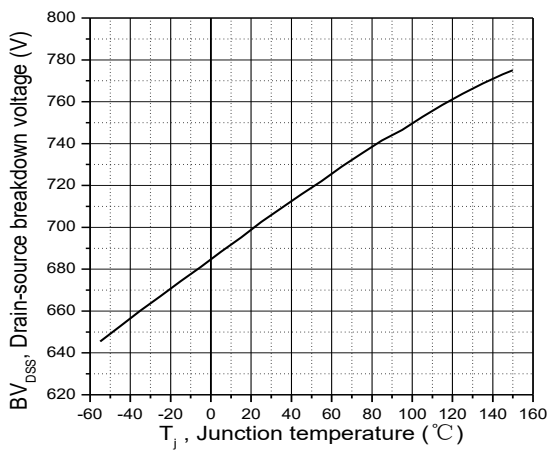


Figure 5. Drain-source breakdown voltage

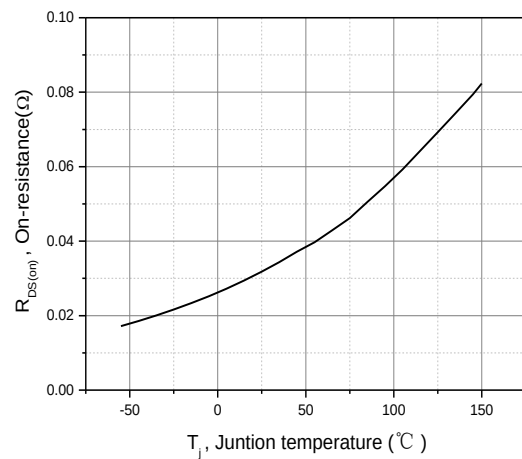


Figure 6. Drain-source on-state resistance

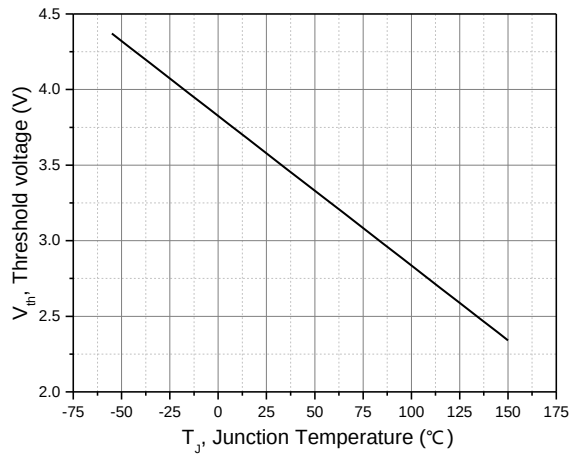


Figure 7. Threshold voltage

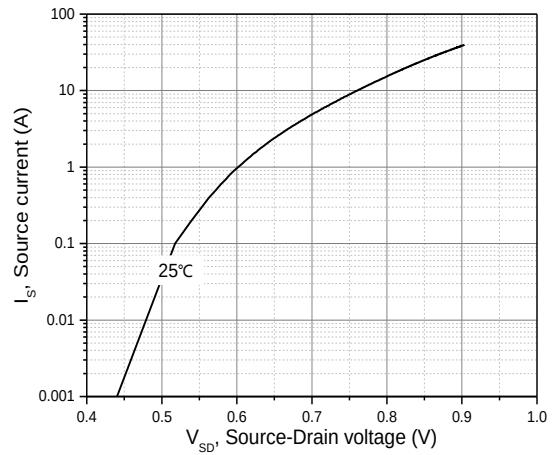


Figure 8. Forward characteristic of body diode

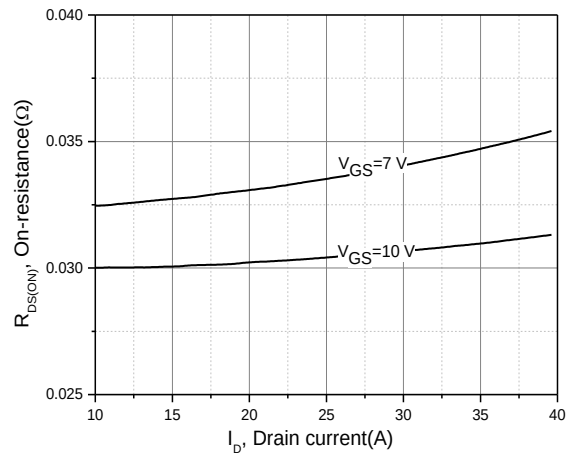


Figure 9. Drain-source on-state resistance

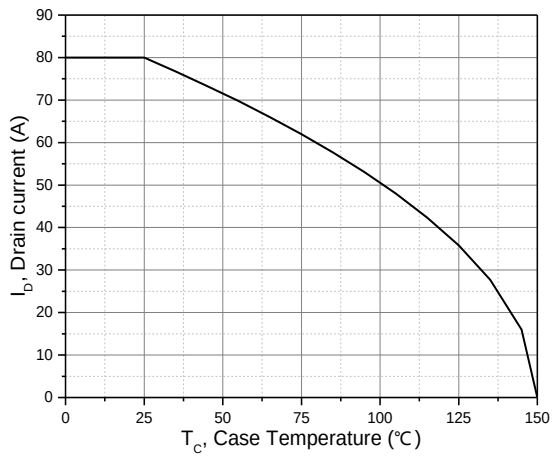


Figure 10. Drain current

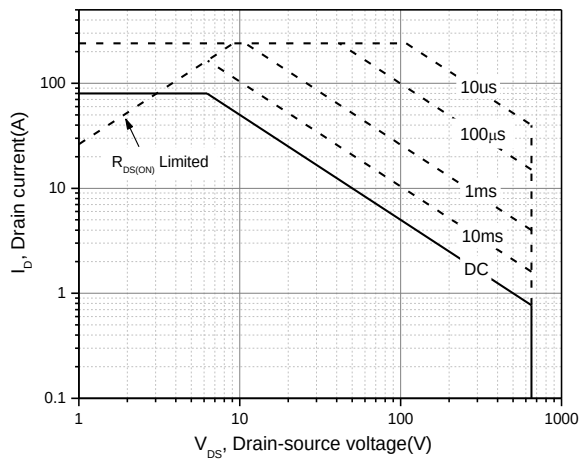


Figure 7. Safe operation area $T_C=25^\circ\text{C}$

Test circuits and waveforms

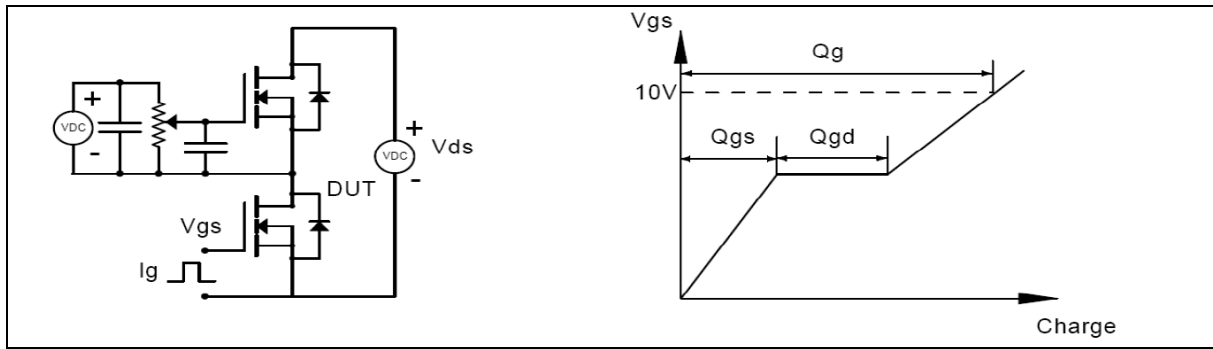


Figure 1. Gate charge test circuit & waveform

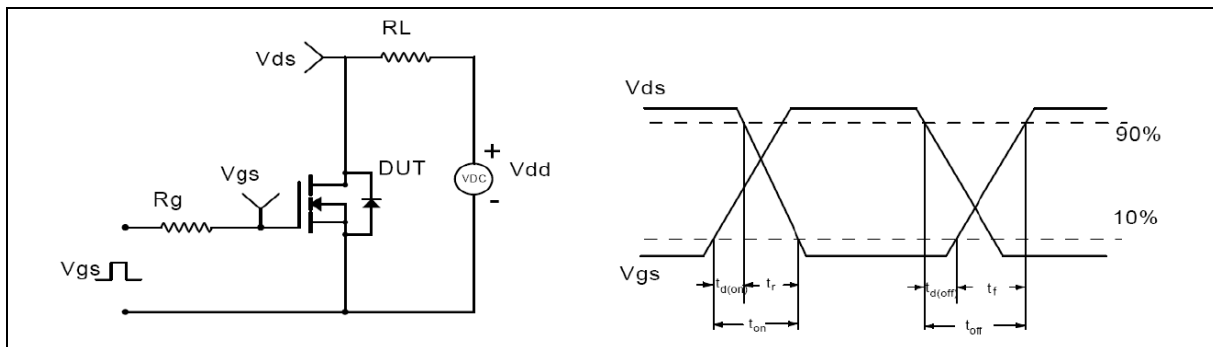


Figure 2. Switching time test circuit & waveforms

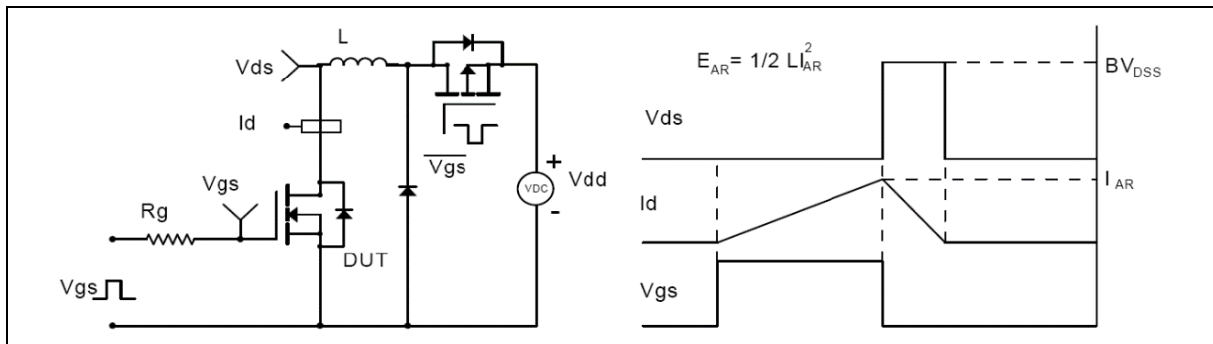


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

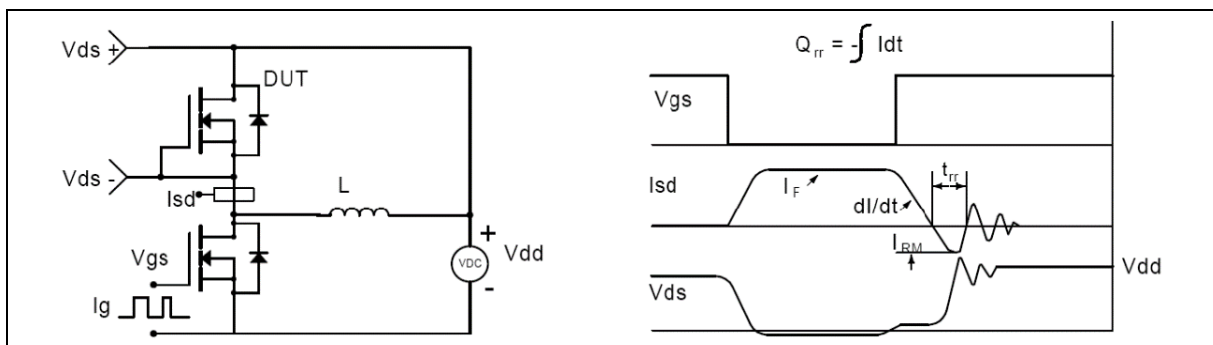
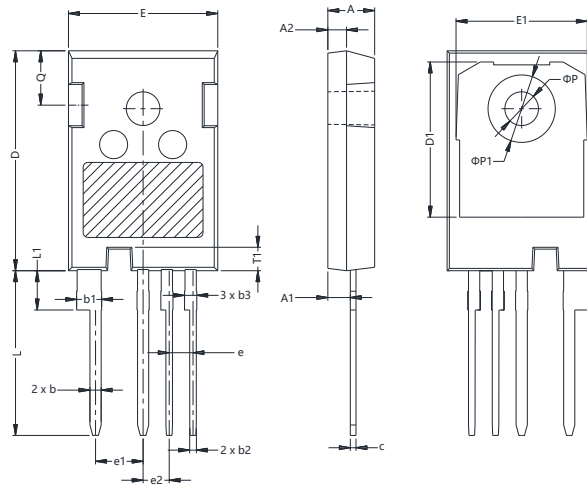


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.80	5.00	5.20
A1	2.21	2.41	2.61
A2	1.80	2.00	2.20
b	1.06	1.21	1.36
b1	2.33	2.63	2.93
b2	0.60	0.70	0.80
b3	1.07	1.30	1.60
c	0.51	0.61	0.75
D	23.30	23.45	23.60
D1	16.25	16.55	16.85
E	15.74	15.94	16.14
E1	13.72	14.02	14.32
T1	2.35	2.50	2.65
e	2.54BSC		
e1	5.08BSC		
e2	2.79BSC		
Q	5.49	5.79	6.09
L	17.27	17.57	17.87
L1	3.99	4.19	4.39
ΦP	3.40	3.60	3.80
ΦP1	7.19REP		

Version: TO247_NL-P package outline dimension

Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO247_NL-P	30	11	330	6	1980

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R038H4ZF_NB	TO247_NL	yes	yes	yes

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