

General Description

OSC80N65HF uses advanced Oriental-Semi's patented Trident-Gate Bipolar Transistor (TGBT™) technology to provide extremely low $V_{CE(sat)}$, low gate charge, and excellent switching performance. This device is suitable for mid to high range switching frequency converters.

Features

- Advanced TGBT™ technology
- Excellent conduction and switching loss
- Excellent stability and uniformity



Applications

- Induction converters
- Uninterruptible power supplies

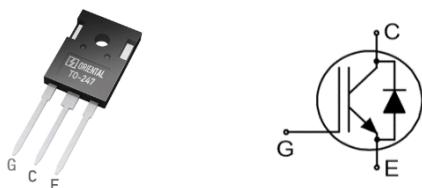
Key Performance Parameters

Parameter	Value	Unit
$V_{CES, min}$ @ 25°C	650	V
Maximum junction temperature	175	°C
I_C , pulse	200	A
$V_{CE(sat), typ}$ @ $V_{GE}=15V$	1.85	V
Q_g	179	nC

Marking Information

Product Name	Package	Marking
OSC80N65HF	TO247	OSC80N65H

Package & Pin Information



Absolute Maximum Ratings at $T_{vj}=25^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Collector emitter voltage	V_{CES}	650	V
Gate emitter voltage	V_{GES}	± 20	V
Transient gate emitter voltage, $T_P \leq 10\mu\text{s}$, $D < 0.01$		± 30	V
Continuous collector current ¹⁾ , $T_c=25^{\circ}\text{C}$	I_c	114	A
Continuous collector current ¹⁾ , $T_c=100^{\circ}\text{C}$		80	A
Pulsed collector current ²⁾ , $T_c=25^{\circ}\text{C}$	$I_{C, \text{pulse}}$	200	A
Diode forward current ¹⁾ , $T_c=25^{\circ}\text{C}$	I_F	114	A
Diode forward current ¹⁾ , $T_c=100^{\circ}\text{C}$		80	A
Diode pulsed current ²⁾ , $T_c=25^{\circ}\text{C}$	$I_{F, \text{pulse}}$	200	A
Power dissipation ³⁾ , $T_c=25^{\circ}\text{C}$	P_D	250	W
Power dissipation ³⁾ , $T_c=100^{\circ}\text{C}$		125	W
Operation and storage temperature	T_{stg}, T_{vj}	-55 to 175	$^{\circ}\text{C}$
Short circuit withstand time $V_{GE}=15\text{ V}$, $V_{CC} \leq 400\text{ V}$ Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{ S}$ $T_{vj}=150\text{ }^{\circ}\text{C}$	t_{sc}	10	μs

Thermal Characteristics

Parameter	Symbol	Value	Unit
IGBT thermal resistance, junction-case	$R_{\theta JC}$	0.6	$^{\circ}\text{C/W}$
Diode thermal resistance, junction-case	$R_{\theta JC}$	0.33	$^{\circ}\text{C/W}$
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	40	$^{\circ}\text{C/W}$

Electrical Characteristics at $T_{vj}=25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Collector-emitter breakdown voltage	$V_{(\text{BR})\text{CES}}$	650			V	$V_{GE}=0 \text{ V}$, $I_c=1 \text{ mA}$
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$		1.85	2.1	V	$V_{GE}=15 \text{ V}$, $I_c=80 \text{ A}$, $T_{vj}=25^{\circ}\text{C}$
			2.45		V	$V_{GE}=15 \text{ V}$, $I_c=80 \text{ A}$, $T_{vj}=125^{\circ}\text{C}$
			2.8			$V_{GE}=15 \text{ V}$, $I_c=80 \text{ A}$, $T_{vj}=175^{\circ}\text{C}$
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	3.0	4.0	5.0	V	$V_{CE}=V_{GE}$, $I_D=2 \text{ mA}$
Diode forward voltage	V_F		1.2	1.5	V	$V_{GE}=0 \text{ V}$, $I_F=80 \text{ A}$, $T_{vj}=25^{\circ}\text{C}$
			1.15			$V_{GE}=0 \text{ V}$, $I_F=80 \text{ A}$, $T_{vj}=125^{\circ}\text{C}$
			1.1			$V_{GE}=0 \text{ V}$, $I_F=80 \text{ A}$, $T_{vj}=175^{\circ}\text{C}$
Gate-emitter leakage current	I_{GES}			100	nA	$V_{CE}=0 \text{ V}$, $V_{GE}=20 \text{ V}$
Zero gate voltage collector current	I_{CES}			10	μA	$V_{CE}=650 \text{ V}$, $V_{GE}=0 \text{ V}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{ies}		7709		pF	V _{GE} =0 V, V _{CE} =25 V, f=100 kHz
Output capacitance	C _{oes}		2978		pF	
Reverse transfer capacitance	C _{res}		3		pF	
Turn-on delay time	t _{d(on)}		72		ns	
Rise time	t _r		112		ns	
Turn-off delay time	t _{d(off)}		255		ns	
Fall time	t _f		112		ns	
Turn-on energy	E _{on}		5.16		mJ	
Turn-off energy	E _{off}		1.22		mJ	
Turn-on delay time	t _{d(on)}		67		ns	
Rise time	t _r		47		ns	V _{GE} =15 V, V _{CC} =400 V, R _G =10 Ω, I _C =80 A
Turn-off delay time	t _{d(off)}		296		ns	
Fall time	t _f		25		ns	
Turn-on energy	E _{on}		2.01		mJ	
Turn-off energy	E _{off}		0.35		mJ	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		179		nC	V _{GE} =15 V, V _{CC} =520 V, I _C =80 A
Gate-emitter charge	Q _{ge}		55		nC	
Gate-collector charge	Q _{gc}		32		nC	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode reverse recovery time	t _{rr}		116		ns	V _R =400 V, I _F =80 A, dI _F /dt=500 A/μs T _{vj} = 25°C
Diode reverse recovery charge	Q _{rr}		2.8		μC	
Diode peak reverse recovery current	I _{rrm}		47.5		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.

Electrical Characteristics Diagrams

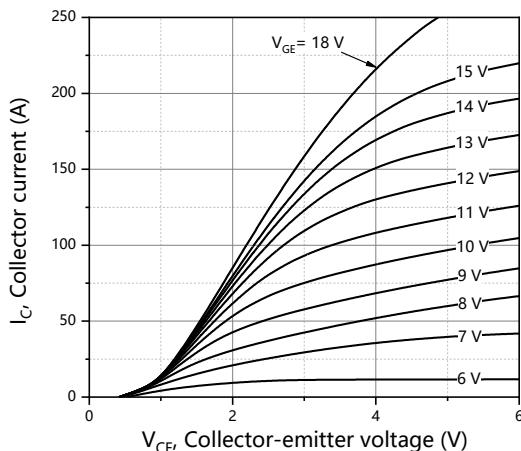


Figure 1. Typical output characteristics
($T_{vj}=25^{\circ}\text{C}$)

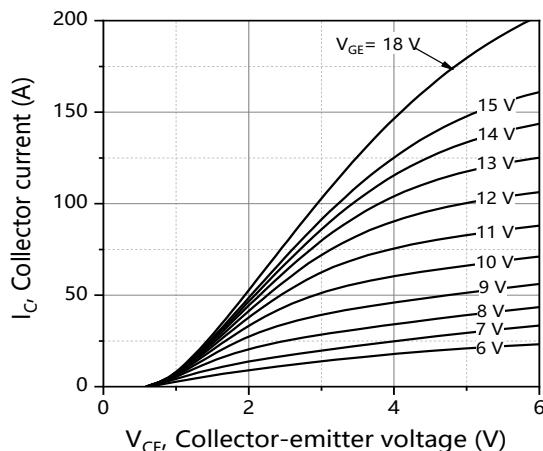


Figure 2. Typical output characteristics
($T_{vj}=150\ ^{\circ}\text{C}$)

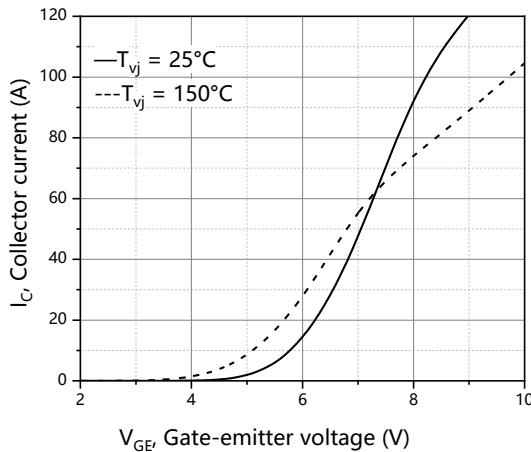


Figure 3. Typical transfer characteristics
($V_{CE}=20\text{ V}$)

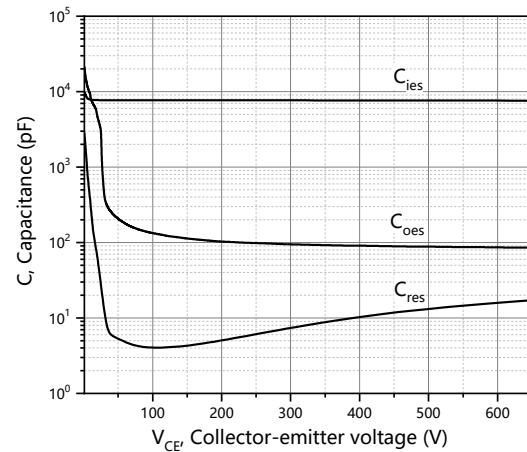


Figure 4. Typical capacitance
($V_{GE}=0\text{V}, f=100\text{ kHz}$)

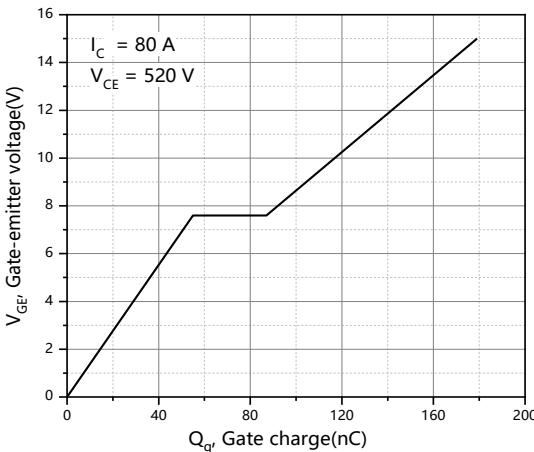


Figure 5. Typical gate charge

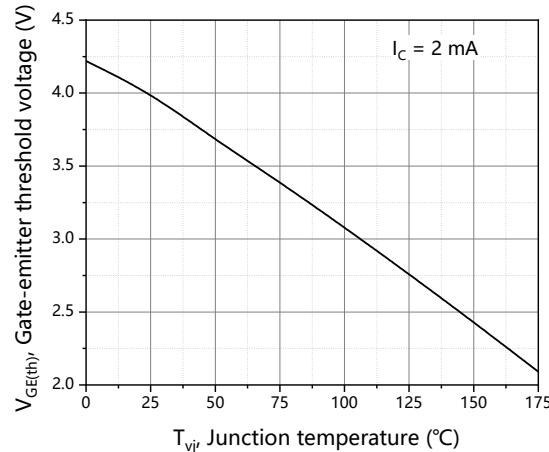
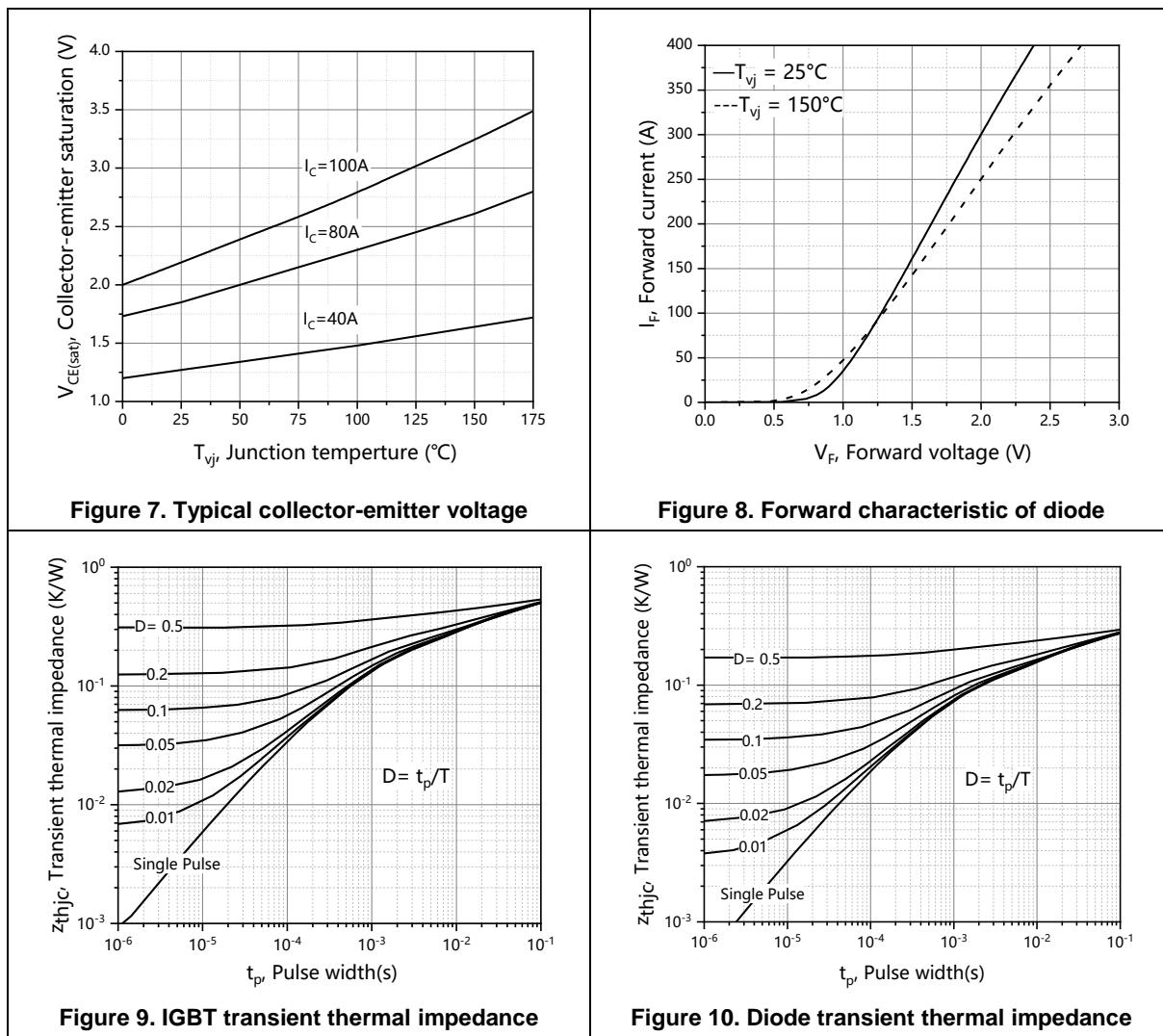
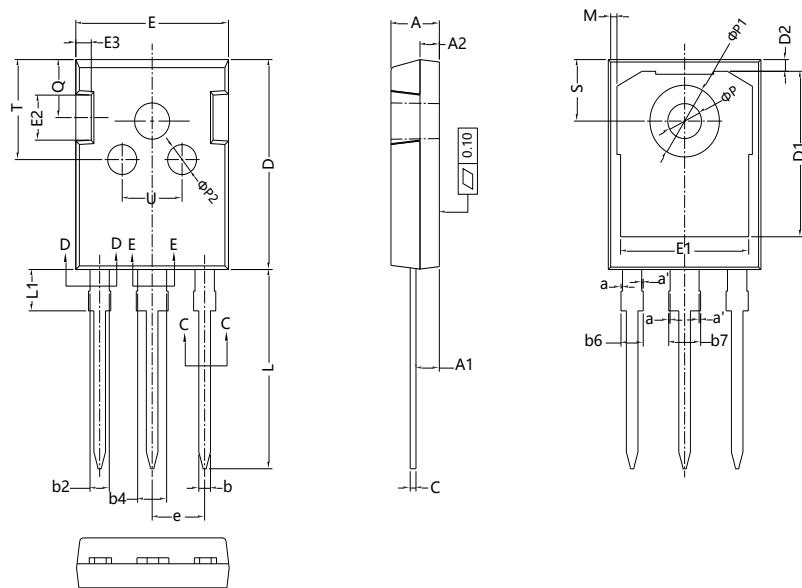


Figure 6. Gate-emitter threshold voltage



Package Information



Symbol	mm		
	Min	Nom	Max
A	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
a	0.00	-	0.15
a'	0.00	-	0.15
b	1.16	-	1.26
b2	1.96	-	2.06
b4	2.96	-	3.06
b6	-	-	2.25
b7	-	-	3.25
c	0.59	-	0.66
c1	0.58	0.60	0.62
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.40	4.50	4.60
E3	1.50	1.60	1.70
e	5.436 BSC		
L	19.80	19.92	20.10
L1	-	-	4.30
M	0.35	-	0.95
P	3.40	3.50	3.60
P1	7.00	-	7.40
P2	2.40	2.5	2.6
Q	5.60	-	6.0
S	6.05	6.15	6.25
T	9.8	-	10.20
U	6.00	-	6.40

Version 1: TO247-J package outline dimension

Ordering Information

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO247-J	30	20	600	4	2400

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSC80N65HF	TO247	yes	yes	yes

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