

General Description

This MOSFET uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a wide variety of applications.

Features

- Low gate charge
- 100% UIS tested, 100% DVDS tested
- High power and current handling capability
- Lead free product is acquired

Applications

- PWM applications
- Load switch
- Power management



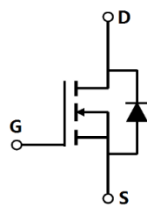
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	80	V
$R_{DS(ON), max} @ V_{GS}=10V$	8.7	mΩ

Marking Information

Product Name	Package	Marking
OSH08N087DNF	TO-252	OSH08N087DN

Package & Pin information



Absolute Maximum Ratings at $T_j=25^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	80	V
Gate-source voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	81	A
Pulsed Drain Current ¹⁾	$I_{D,pulse}$	324	A
Power Dissipation	P_D	125	W
Single pulsed avalanche energy ²⁾	E_{AS}	361	mJ
Operation and storage temperature	T_{stg}, T_j	-55 to 150	$^{\circ}\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-to case	$R_{\theta JC}$	1.00	$^{\circ}\text{C/W}$

Electrical Characteristics at $T_j=25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	80			V	$V_{GS}=0\text{ V}, I_D=250\text{ }\mu\text{A}$
Gate threshold voltage	$V_{GS(th)}$	2.0		4.0	V	$V_{DS}=V_{GS}, I_D=250\text{ }\mu\text{A}$
Drain-source on-state resistance	$R_{DS(ON)}$		7.3	8.7	$\text{m}\Omega$	$V_{GS}=10\text{ V}, I_D=40\text{ A}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=20\text{ V}, V_{DS}=0\text{ V}$
				-100		$V_{GS}=-20\text{ V}, V_{DS}=0\text{ V}$
Drain-source leakage current	I_{DSS}			1	μA	$V_{DS}=80\text{ V}, V_{GS}=0\text{ V}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		4653		pF	$V_{GS}=0\text{ V}$, $V_{DS}=40\text{ V}$, $f=1.0\text{ MHz}$
Output capacitance	C_{oss}		197		pF	
Reverse transfer capacitance	C_{rss}		171		pF	
Gate resistance	R_g		0.65		Ω	$V_{GS}=0\text{ V}$, $V_{DS}=0\text{ V}$, $f=1.0\text{ MHz}$
Turn-on Delay Time	$t_{d(on)}$		30		nS	$V_{GS}=10\text{ V}$, $V_{DS}=40\text{ V}$, $R_L=2\text{ }\Omega$, $R_{GEN}=6\text{ }\Omega$
Turn-on Rise Time	t_r		18		nS	
Turn-Off Delay Time	$t_{d(off)}$		65		nS	
Turn-Off Fall Time	t_f		30		nS	

Gate Charge Characteristics

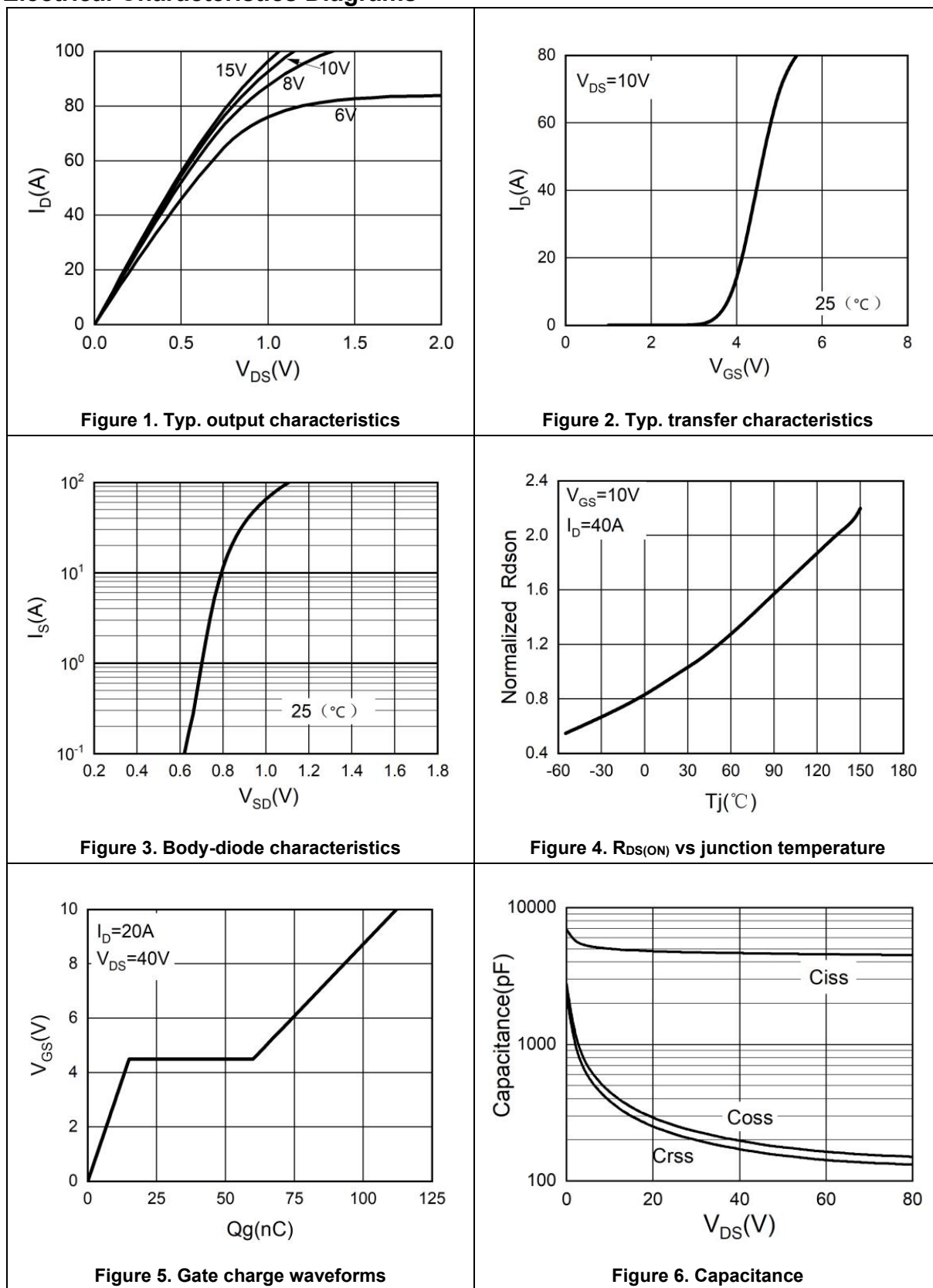
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total Gate Charge	Q_g		112		nC	$V_{GS}=10\text{ V}$, $V_{DS}=40\text{ V}$, $I_D=20\text{ A}$
Gate-Source Charge	Q_{gs}		15		nC	
Gate-Drain Charge	Q_{gd}		45		nC	

Body Diode Characteristics

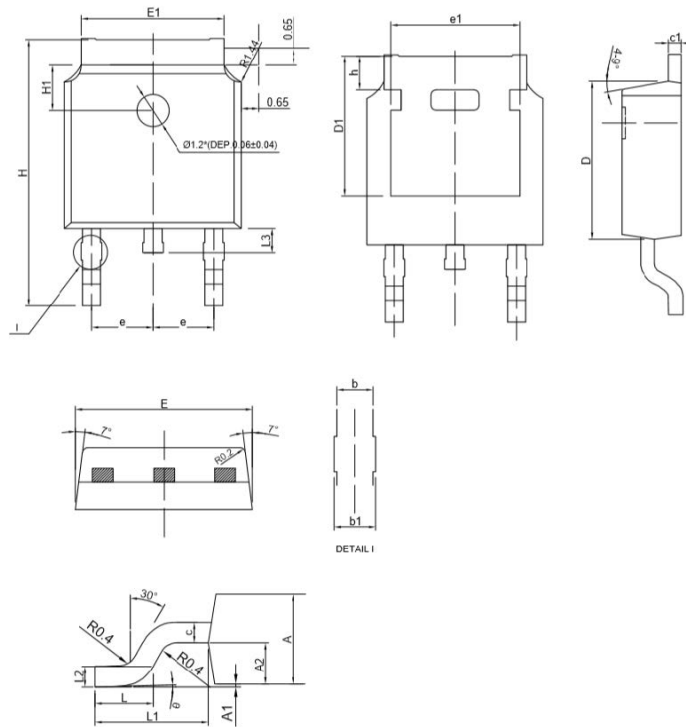
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Source drain current (Body Diode)	I_{SD}			81	A	$T_A=25^\circ\text{C}$
Diode forward voltage ³⁾	V_{SD}			1.2	V	$I_S=20\text{ A}$, $V_{GS}=0\text{ V}$
Reverse Recovery Time	t_{rr}		7		ns	$I_F=20\text{ A}$, $di/dt=100\text{ A/us}$
Reverse Recovery Charge	Q_{rr}		40		nC	

Note: 1) Pulse width limited by maximum allowable junction temperature.
 2) EAS condition: $T_J=25^\circ\text{C}$, $V_{DD}=40\text{ V}$, $V_G=10\text{ V}$, $R_g=25\text{ }\Omega$, $L=0.3\text{ mH}$.
 3) Repetitive Rating: Pulse width limited by maximum junction temperature.

Electrical Characteristics Diagrams



Package Information



Symbol	mm		
	Min.	Typ.	Max.
A	2.200	2.300	2.400
A1	0.000	0.075	0.15
A2	0.97	1.02	1.07
b	0.60	0.67	0.74
b1	0.65	-	1.15
c	0.508	0.528	0.548
c1	0.478	0.508	0.538
D	6.0	6.1	6.2
D1	5.15	5.25	5.35
E	6.5	6.6	6.7
E1	5.184	5.334	5.484
e	2.286BSC		
e1	4.806	4.826	4.846
H	9.8	10.0	10.2
H1	1.5	1.6	1.7
h	1.15	1.25	1.35
L	1.4	1.5	1.6
L1	2.888REF		
L2	0.51BSC		
L3	0.8	0.9	1.0
θ	0°	-	10°

Version1: TO-252-G package outline dimension

Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO-252-G	2500	1	2500	6	15000

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