General Description

The silicon carbide power MOSFET utilizes Oriental-Semi's advanced technology to achieve low on-resistance, low gate charge, low Qrr and excellent thermal performance by exploiting innovative properties of wide bandgap materials. It is engineered to minimize conduction loss and provide superior switching performance almost independent of temperature.

The silicon carbide power MOSFET provides high reliability and extremely high efficiency. It is targeted to improve application performance in frequency, energy efficiency, reliability, system size and weight reduction.

Features

- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- IGBT-compatible driving voltage (15V for turn-on)
- Fast intrinsic diode with low reverse recovery (Qrr)
- Temperature independent turn-off switching losses
- Halogen free, RoHS compliant

Applications

- Solar inverters
- On-board charger/PFC
- EV motor drive
- High voltage DC/DC converters
- Switch mode power supplies

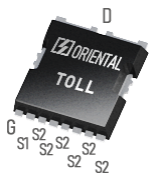
Key Performance Parameters

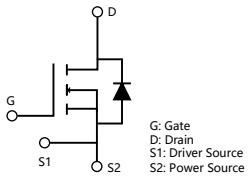
Parameter	Value	Unit
V_{DS}	650	V
$I_{D,\ pulse}$	208	A
$R_{DS(ON),\ typ}$ @ V_{GS}=18V	30	mΩ
Q_g	80	nC

Marking Information

Product Name	Package	Marking
OSQ65R030TT4NF	TOLL	OSQ65R030TT4N

Package & Pin Information





Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Gate-source voltage (max static)	V_{GS}	-8/+23	V
Gate-source voltage (recommended operating values)	V_{GS}	0/+18	V
Continuous drain current[1], T_C=25 °C	I_D	81	A
Continuous drain current[1], T_C=100 °C		57	
Pulsed drain current[2]	$I_{D,\ pulse}$	208	A
Power dissipation[3], T_C=25 °C	P_D	230	W
Operation and storage temperature	T_{stg}, T_j	-55 to 175	°C

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction-case	$R_{\theta JC}$		0.5	0.65	°C/W
Thermal resistance, junction-ambient	$R_{\theta JA}$			62	°C/W

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	650			V	V_{GS}=0 V, I_D=100 μA
Gate threshold voltage	$V_{GS(th)}$	2.8	3.2	4.2	V	V_{DS}=V_{GS}, I_D=10 mA
Drain-source on-state resistance	$R_{DS(ON)}$		30	39	mΩ	V_{GS}=18 V, I_D=55 A
			35			V_{GS}=18 V, I_D=55 A, T_j=175 °C
			39	51	mΩ	V_{GS}=15 V, I_D=55 A
			39			V_{GS}=15 V, I_D=55 A, T_j=175 °C
Transconductance	g_{fs}		42		S	V_{DS} = 20 V, I_D =55 A
Gate-source leakage current	I_{GSS}			±100	nA	V_{GS}=-8~23 V
Drain-source leakage current	I_{DSS}			100	μA	V_{DS}=650 V, V_{GS}=0 V
				500	μA	V_{DS}=650 V, V_{GS}=0 V, T_j=175 °C
Gate resistance	R_G		2.7		Ω	f=1 MHz, Open drain, V_{AC}=25 mV

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		2550		pF	V_{GS}=0 V, V_{DS}=400 V, f=100 kHz, V_{AC}=25 mV
Output capacitance	C_{oss}		159		pF	
Reverse transfer capacitance	C_{rss}		11.6		pF	
C_{oss} stored energy	E_{OSS}		16		μJ	
Turn-on delay time	$t_{d(on)}$		12.5		ns	V_{GS}=0/18 V, V_{DS}=400 V, R_G=5 Ω, I_D=55 A, L=120 μH, T_j=25 °C
Rise time	t_r		20		ns	
Turn-off delay time	$t_{d(off)}$		30		ns	
Fall time	t_f		9		ns	
Turn-on switching energy	E_{on}		190		μJ	
Turn-off switching energy	E_{off}		132		μJ	
Turn-on switching energy	E_{on}		163		μJ	V_{GS}=0/18 V, V_{DS}=400 V, R_G=5 Ω, I_D=55 A, L=120 μH, T_j=175 °C
Turn-off switching energy	E_{off}		169		μJ	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		80		nC	V_{GS}=0/18 V, V_{DS}=400 V, I_D=55 A
Gate-source charge	Q_{gs}		30.2		nC	
Gate-drain charge	Q_{gd}		15.6		nC	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}		4.0		V	I_S=39 A, V_{GS}=0 V, T_j=25 °C
			3.7		V	I_S=39 A, V_{GS}=0 V, T_j=175 °C
Continuous diode forward current	Is		81		A	V_{GS}=0 V, T_j=25 °C
Reverse recovery time	t_{rr}		15.2		ns	V_{GS}=0 V, V_R=400 V, I_S=55 A, di/dt=3000 A/μs, T_j=25 °C
Reverse recovery charge	Q_{rr}		206		nC	
Peak reverse recovery current	I_{rrm}		25		A	
Reverse recovery time	t_{rr}		26.6		ns	V_{GS}=0 V, V_R=400 V, I_S=55 A, di/dt=1000 A/μs, T_j=175 °C
Reverse recovery charge	Q_{rr}		203		nC	
Peak reverse recovery current	I_{rrm}		12.7		A	

Note

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating; pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.

Electrical Characteristics Diagrams

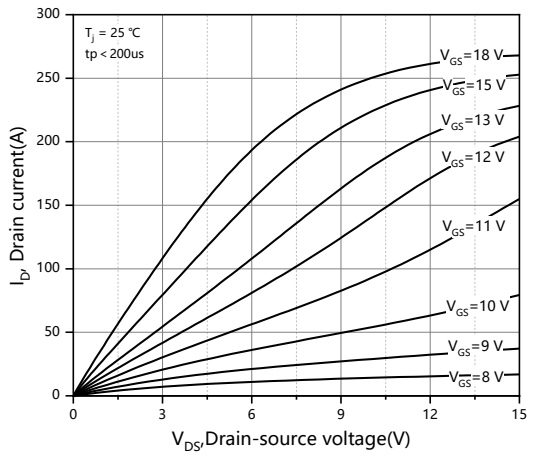


Figure 1.Output characteristics T_j=25℃

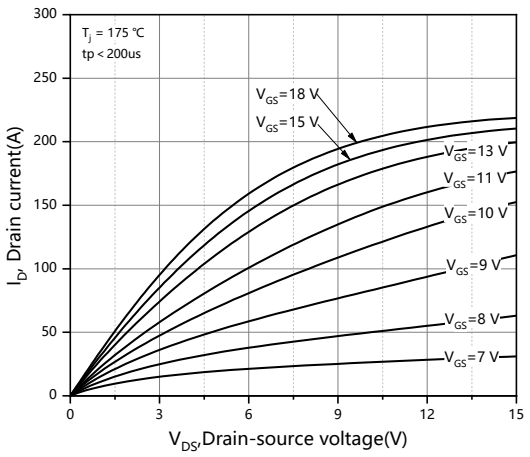


Figure 2.Output characteristics T_j=175℃

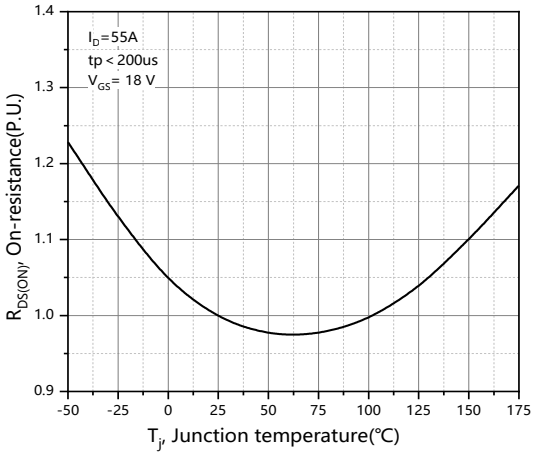


Figure 3. Normalized on-resistance vs. temperature

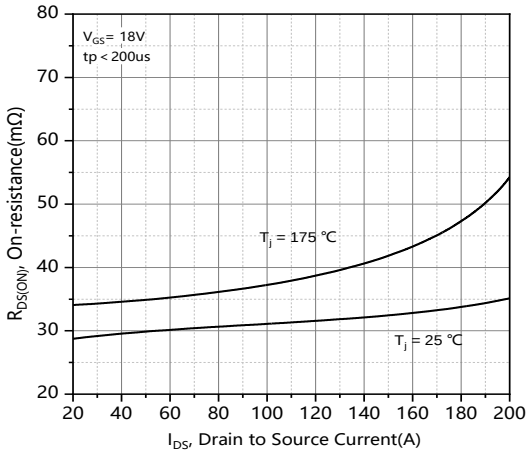


Figure 4. On-resistance vs. drain current for various temperatures

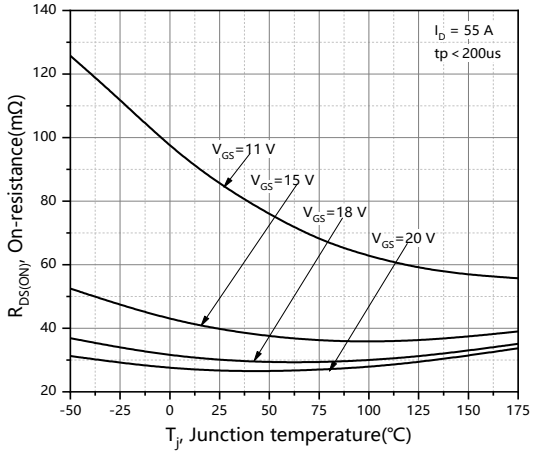


Figure 5. On-resistance vs. temperature for various gate voltage

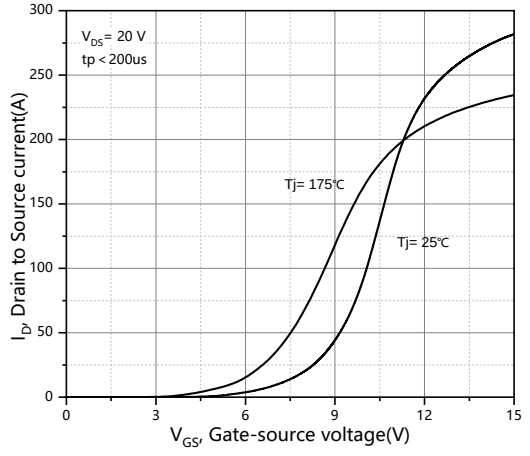


Figure 6. Transfer characteristic for various junction temperatures

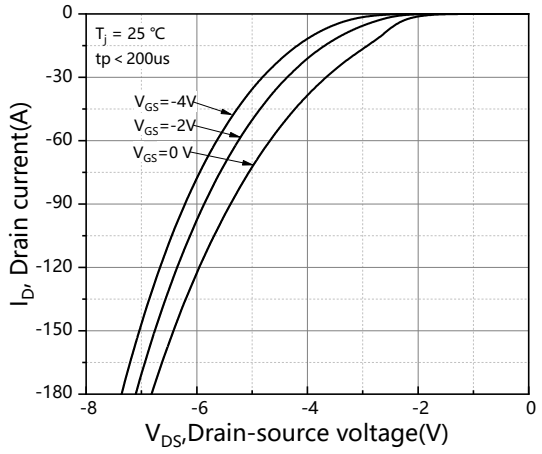


Figure 7. Body diode characteristic at T_j = 25 ºC

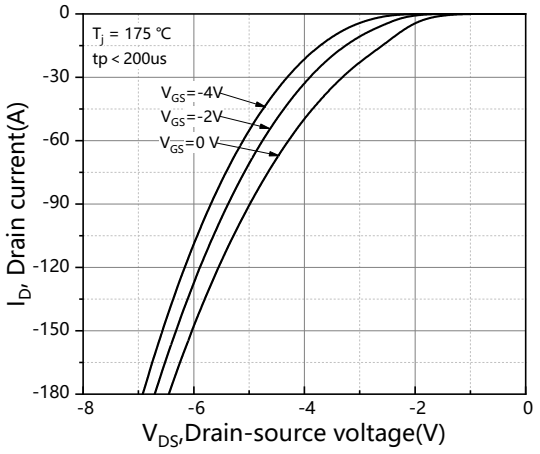


Figure 8. Body diode characteristic at T_j = 175ºC

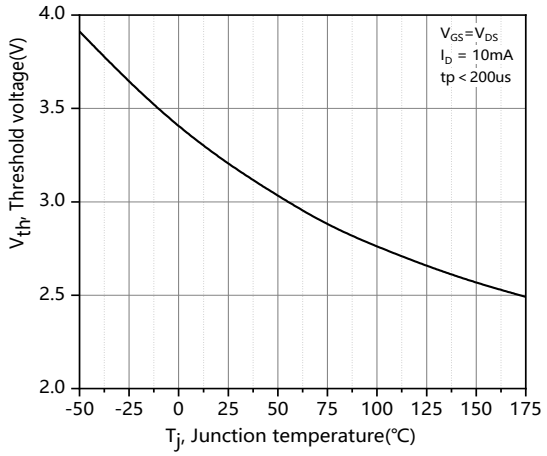


Figure 9. Threshold voltage vs. temperature

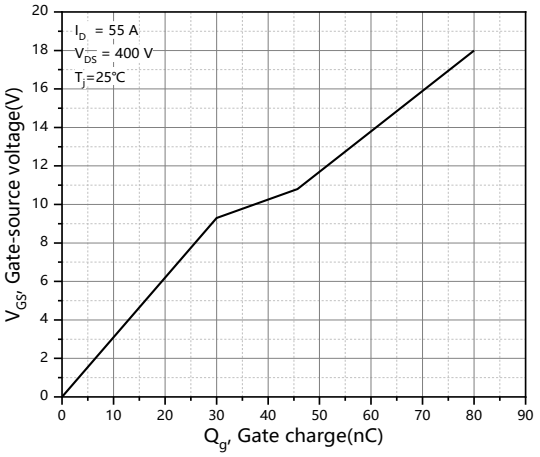


Figure 10. Gate charge characteristic

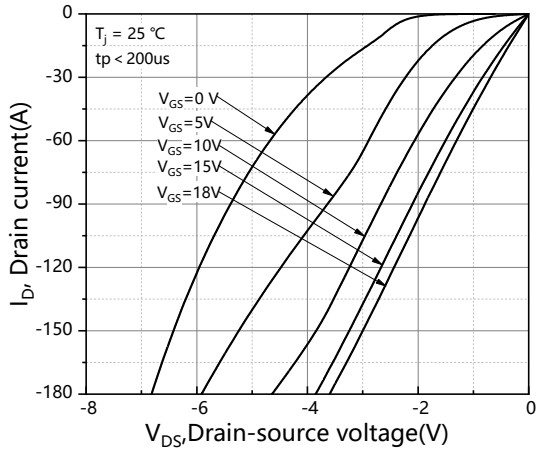


Figure 11. 3rd quadrant characteristic at T_j = 25 ºC

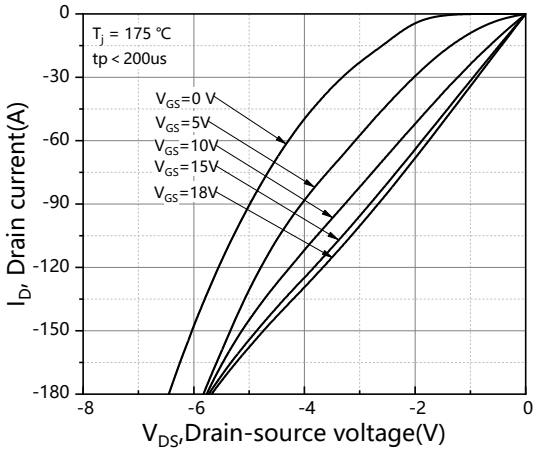


Figure 12. 3rd quadrant characteristic at T_j = 175 ºC

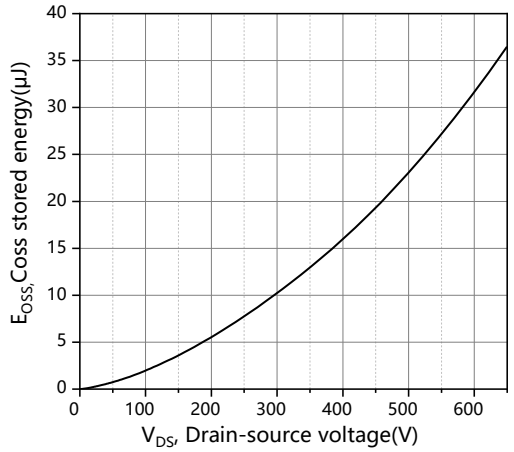


Figure 13. Output capacitor stored energy

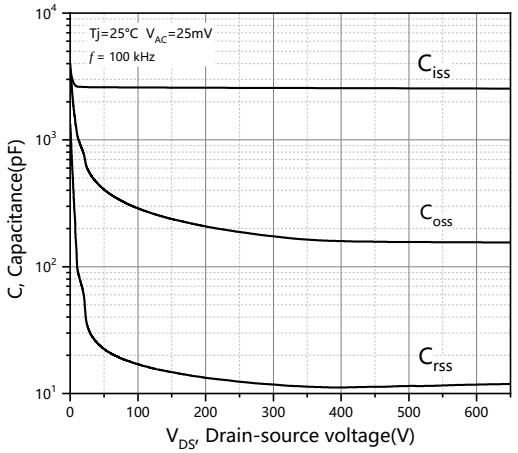


Figure 14. Capacitances vs. drain-source voltage

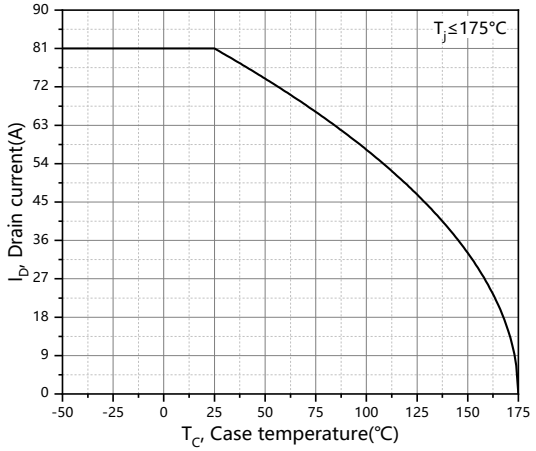


Figure 15. Continuous drain current derating vs.case temperature

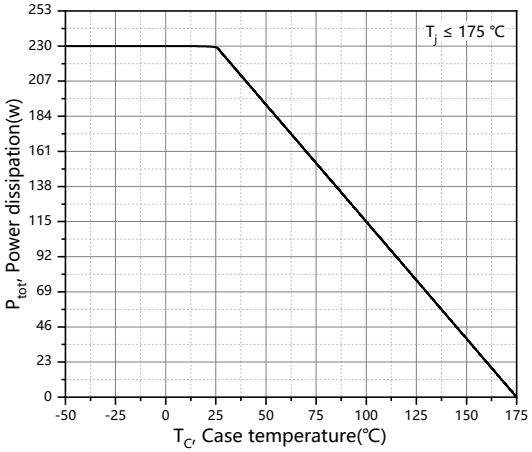


Figure 16. Maximum power dissipation derating vs.case temperature

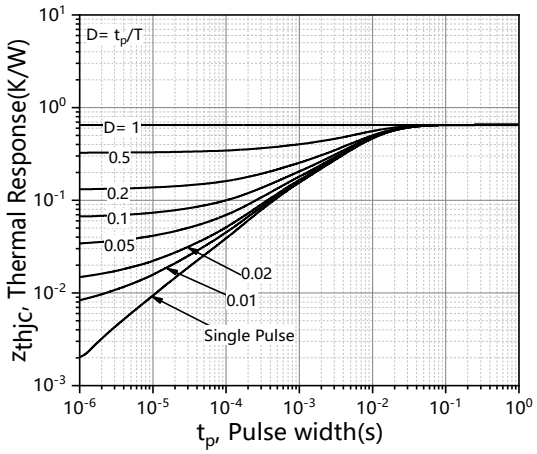


Figure 17. Transient thermal impedance (junction - case)

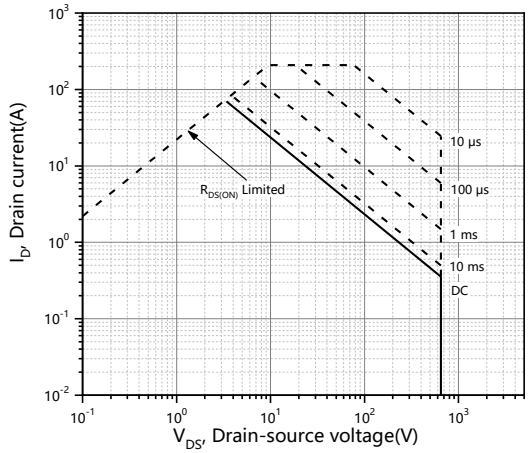


Figure 18. Safe operating area

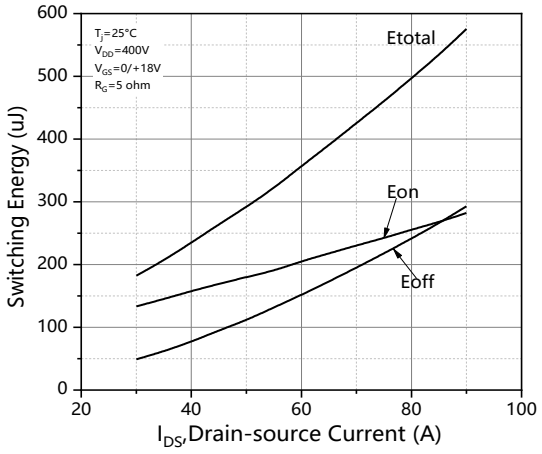


Figure 19. Clamped Inductive switching energy vs.drain current

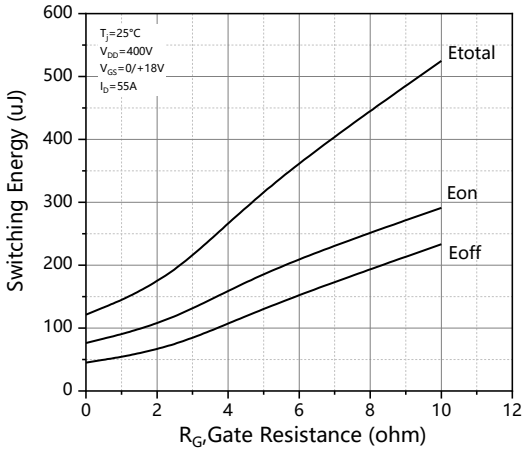


Figure 20. Clamped inductive switching energy vs. R_G

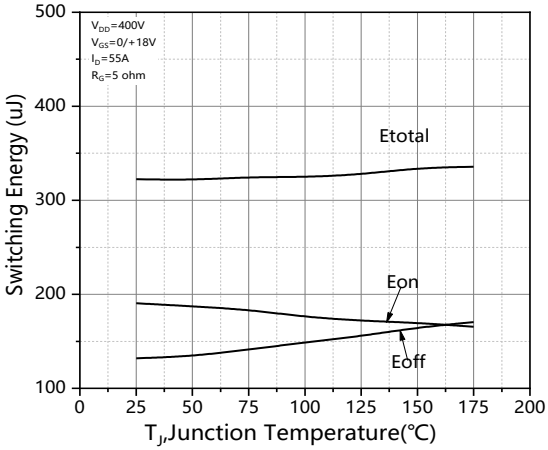


Figure 21. Clamped inductive switching energy vs. temperature

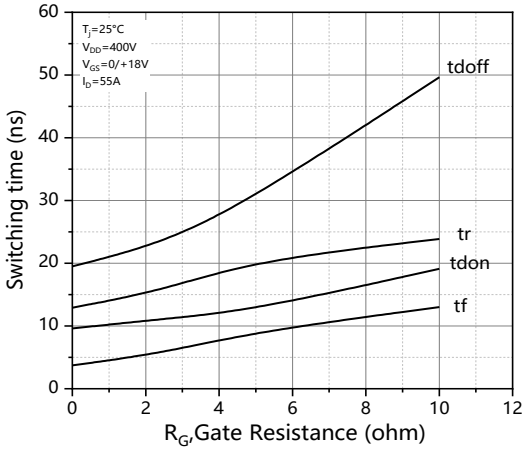
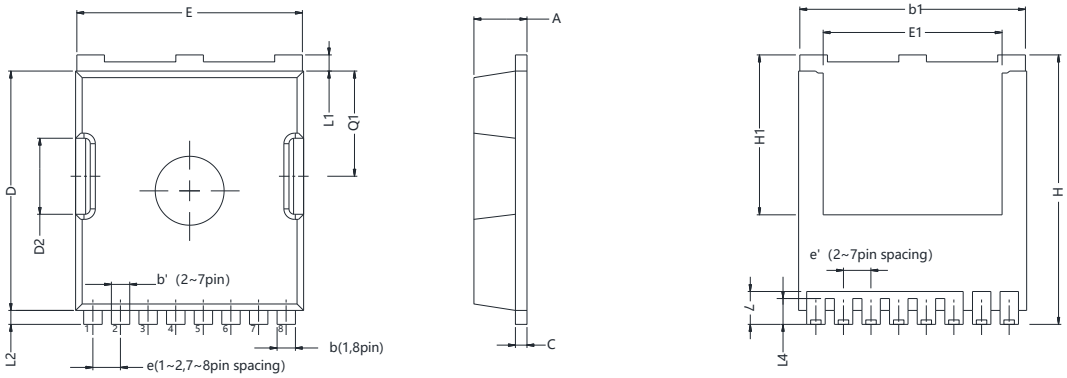


Figure 22. Switching times vs. R_G

Package Information



Symbol	mm		
	Min	Nom	Max
A	2.15	2.30	2.45
b	0.75	0.75	0.85
b'	0.70	0.70	0.80
b1	9.65	9.80	9.95
C	0.45	0.50	0.60
D	10.18	10.38	10.58
D2	3.15	3.30	3.45
E	9.70	9.90	10.10
E1	7.95	8.10	8.25
e	BSC 1.225		
e'	BSC 1.20		
Q1	4.40	4.55	4.70
H	11.48	11.68	11.88
H1	6.80	6.95	7.10
L	1.60	1.80	2.00
L1	0.50	0.70	0.90
L2	0.48	0.60	0.72
L4	1.00	1.15	1.30

Version : TOLL-P package outline dimension

Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TOLL-P	1200	1	1200	5	6000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSQ65R030TT4NF	TOLL	yes	yes	yes

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Revision History

Version	Revision History	Date
V1.0	Initial release	2026-02-25

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