

## General Description

The silicon carbide power MOSFET utilizes Oriental-Semi's advanced technology to achieve low on-resistance, low gate charge, low Qrr and excellent thermal performance by exploiting innovative properties of wide bandgap materials. It is engineered to minimize conduction loss and provide superior switching performance almost independent of temperature.

The silicon carbide power MOSFET provides high reliability and extremely high efficiency. It is targeted to improve application performance in frequency, energy efficiency, reliability, system size and weight reduction.

## Features

- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- IGBT-compatible driving voltage (15V for turn-on)
- Fast intrinsic diode with low reverse recovery (Qrr)
- Temperature independent turn-off switching losses
- Halogen free, RoHS compliant

## Applications

- On-board charger/PFC
- EV motor drive
- High voltage DC/DC converters
- Switch mode power supplies

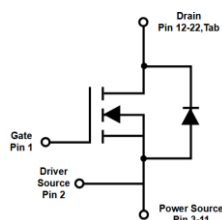
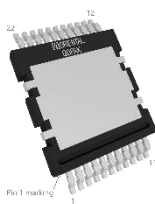
## Key Performance Parameters

Parameter	Value	Unit
$V_{DS}$	650	V
$I_D$ , pulse	218	A
$R_{DS(ON)}$ , typ @ $V_{GS}=18V$	20	m $\Omega$
$Q_g$	113	nC

## Marking Information

Product Name	Package	Marking
OSQ65R020QDT2F	QDPAK	OSQ65R020QDT2

## Package & Pin Information



**Absolute Maximum Ratings** at  $T_j=25^{\circ}\text{C}$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	$V_{DS}$	650	V
Gate-source voltage (max static)	$V_{GS}$	-8/+22	V
Gate-source voltage (recommended operating values)	$V_{GS}$	-4/+18	V
Continuous drain current <sup>1)</sup> , $T_C=25^{\circ}\text{C}$	$I_D$	81	A
Continuous drain current <sup>1)</sup> , $T_C=100^{\circ}\text{C}$		57	
Pulsed drain current <sup>2)</sup>	$I_{D, pulse}$	218	A
Power dissipation <sup>3)</sup> , $T_C=25^{\circ}\text{C}$	$P_D$	300	W
Operation and storage temperature	$T_{stg}, T_j$	-55 to 175	$^{\circ}\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction-case	$R_{\theta JC}$		0.38	0.50	$^{\circ}\text{C/W}$
Thermal resistance, junction-ambient	$R_{\theta JA}$			62	$^{\circ}\text{C/W}$

**Electrical Characteristics** at  $T_j=25^{\circ}\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	650			V	$V_{GS}=0\text{ V}, I_D=100\ \mu\text{A}$
Gate threshold voltage	$V_{GS(th)}$	2.3	2.8	3.6	V	$V_{DS}=V_{GS}, I_D=10\ \text{mA}$
Drain-source on-state resistance	$R_{DS(ON)}$		20	27	m $\Omega$	$V_{GS}=18\ \text{V}, I_D=30\ \text{A}$
			28			$V_{GS}=18\ \text{V}, I_D=30\ \text{A}, T_j=175^{\circ}\text{C}$
			25	34	m $\Omega$	$V_{GS}=15\ \text{V}, I_D=30\ \text{A}$
			31			$V_{GS}=15\ \text{V}, I_D=30\ \text{A}, T_j=175^{\circ}\text{C}$
Transconductance	$g_{fs}$		24		S	$V_{DS} = 20\ \text{V}, I_D = 30\ \text{A}$
Gate-source leakage current	$I_{GSS}$			$\pm 100$	nA	$V_{GS}=-8\sim 22\ \text{V}$
Drain-source leakage current	$I_{DSS}$			100	$\mu\text{A}$	$V_{DS}=650\ \text{V}, V_{GS}=0\ \text{V}$
				500	$\mu\text{A}$	$V_{DS}=650\ \text{V}, V_{GS}=0\ \text{V}, T_j=175^{\circ}\text{C}$
Gate resistance	$R_G$		1.5		$\Omega$	$f=1\ \text{MHz}, \text{Open drain}, V_{AC}=25\ \text{mV}$

### Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	$C_{iss}$		3039		pF	$V_{GS}=0\text{ V}$ , $V_{DS}=400\text{ V}$ , $f=100\text{ kHz}$ , $V_{AC}=25\text{ mV}$
Output capacitance	$C_{oss}$		254		pF	
Reverse transfer capacitance	$C_{rss}$		15		pF	
$C_{oss}$ stored energy	$E_{oss}$		24		$\mu\text{J}$	
Turn-on delay time	$t_{d(on)}$		15.6		ns	$V_{GS}=-4/18\text{ V}$ , $V_{DS}=400\text{ V}$ , $R_G=5\ \Omega$ , $I_D=30\text{ A}$ , $L=120\ \mu\text{H}$ , $T_j=25\text{ }^\circ\text{C}$
Rise time	$t_r$		15.2		ns	
Turn-off delay time	$t_{d(off)}$		37		ns	
Fall time	$t_f$		10.2		ns	
Turn-on switching energy	$E_{on}$		75		$\mu\text{J}$	
Turn-off switching energy	$E_{off}$		46		$\mu\text{J}$	
Turn-on switching energy	$E_{on}$		64		$\mu\text{J}$	$V_{GS}=-4/18\text{ V}$ , $V_{DS}=400\text{ V}$ , $R_G=5\ \Omega$ , $I_D=30\text{ A}$ , $L=120\ \mu\text{H}$ , $T_j=175\text{ }^\circ\text{C}$
Turn-off switching energy	$E_{off}$		53		$\mu\text{J}$	

### Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	$Q_g$		113		nC	$V_{GS}=-4/18\text{ V}$ , $V_{DS}=400\text{ V}$ , $I_D=30\text{ A}$
Gate-source charge	$Q_{gs}$		36.5		nC	
Gate-drain charge	$Q_{gd}$		19		nC	

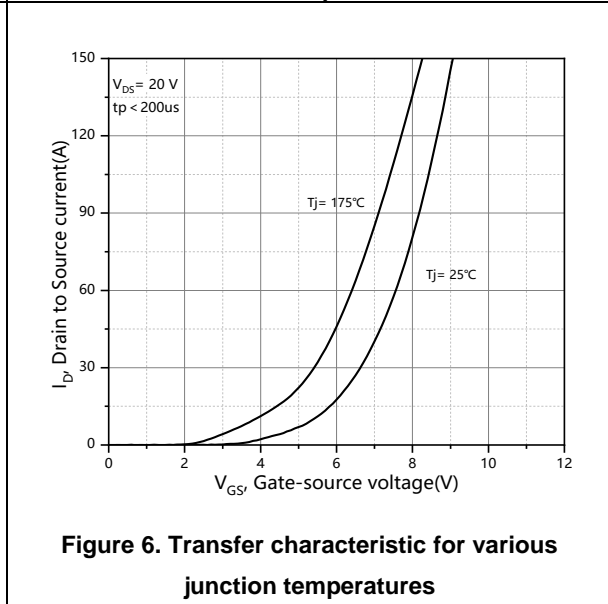
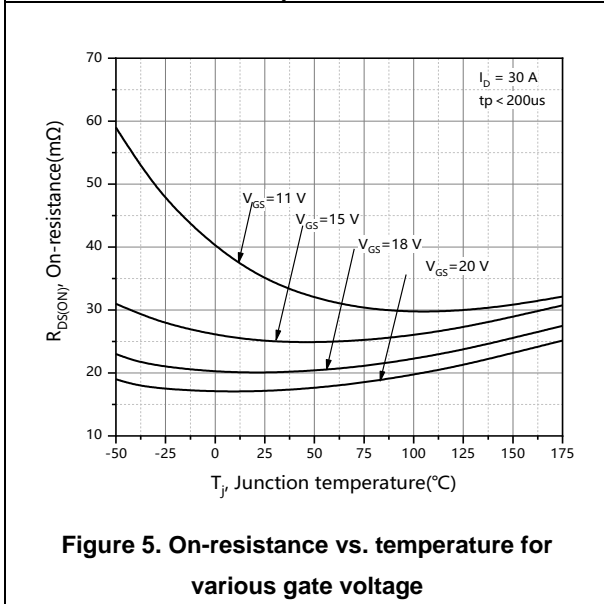
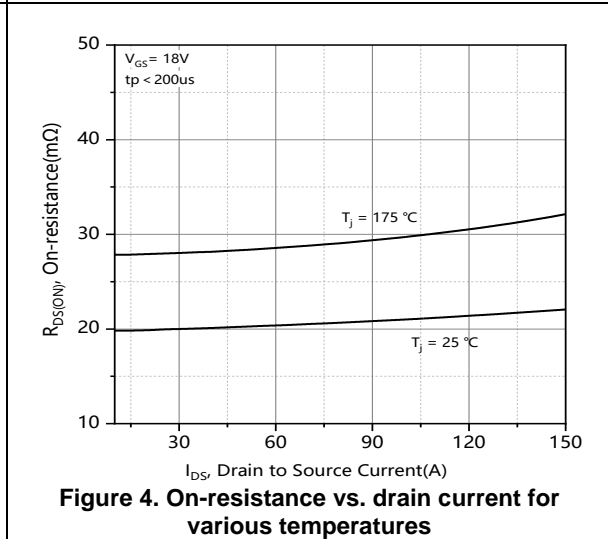
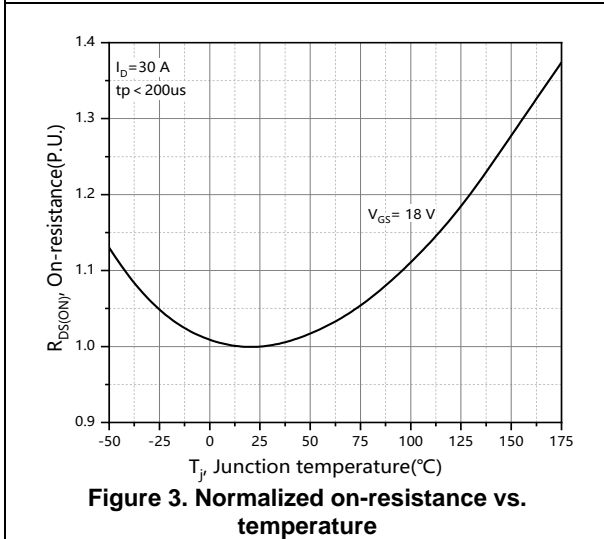
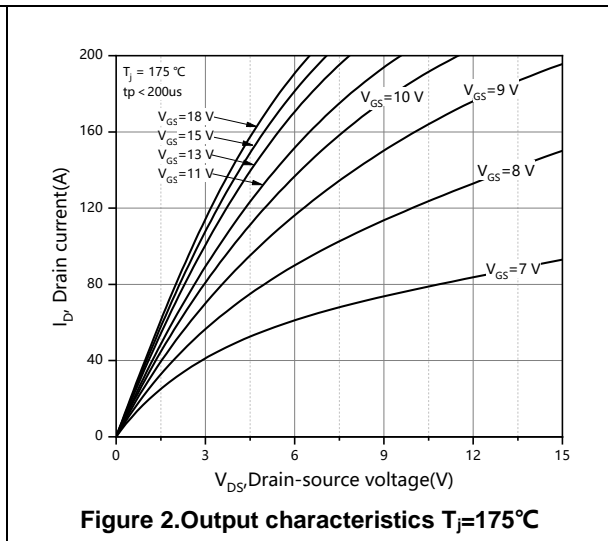
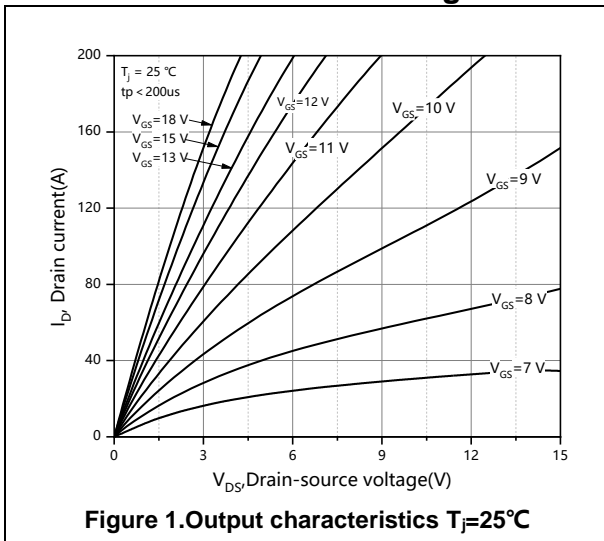
**Body Diode Characteristics**

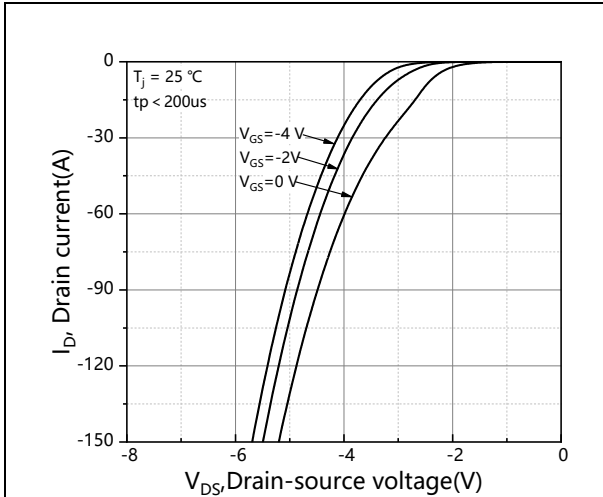
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	$V_{SD}$		4.1		V	$I_S=30\text{ A}$ , $V_{GS}=-4\text{ V}$ , $T_J=25\text{ °C}$
			3.7		V	$I_S=30\text{ A}$ , $V_{GS}=-4\text{ V}$ , $T_J=175\text{ °C}$
Continuous diode forward current	$I_S$		81		A	$V_{GS}=-4\text{ V}$ , $T_J=25\text{ °C}$
Reverse recovery time	$t_{rr}$		16		ns	$V_{GS}=-4\text{ V}$ , $V_R=400\text{ V}$ ,
Reverse recovery charge	$Q_{rr}$		220		nC	$I_S=30\text{ A}$ ,
Peak reverse recovery current	$I_{rrm}$		24.7		A	$di/dt=3000\text{ A}/\mu\text{s}$ , $T_J=25\text{ °C}$
Reverse recovery time	$t_{rr}$		26.6		ns	$V_{GS}=-4\text{ V}$ , $V_R=400\text{ V}$ ,
Reverse recovery charge	$Q_{rr}$		200		nC	$I_S=30\text{ A}$ ,
Peak reverse recovery current	$I_{rrm}$		13		A	$di/dt=1000\text{ A}/\mu\text{s}$ , $T_J=175\text{ °C}$

**Note**

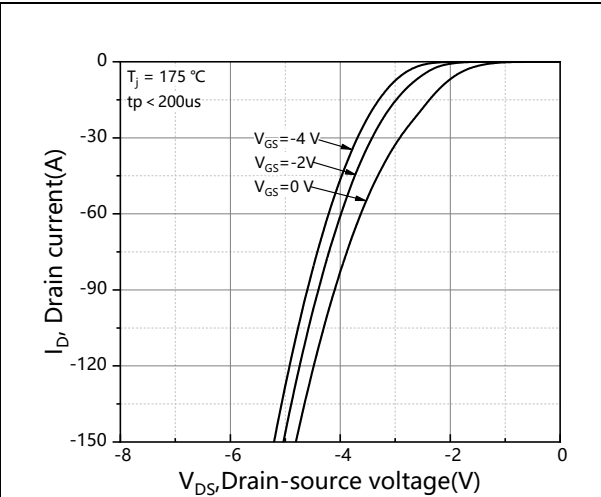
- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3)  $P_d$  is based on max. junction temperature, using junction-case thermal resistance.

**Electrical Characteristics Diagrams**

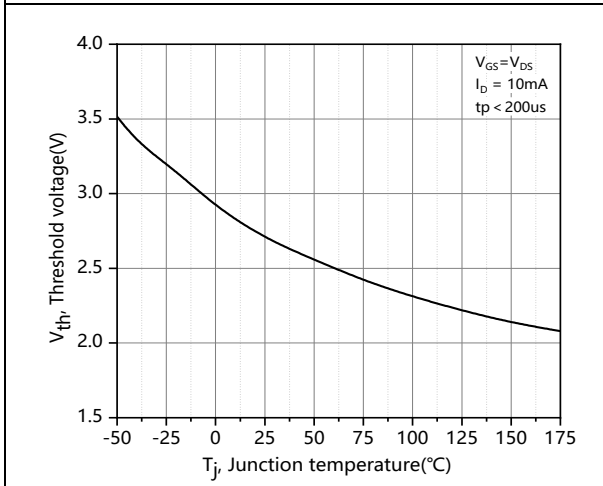




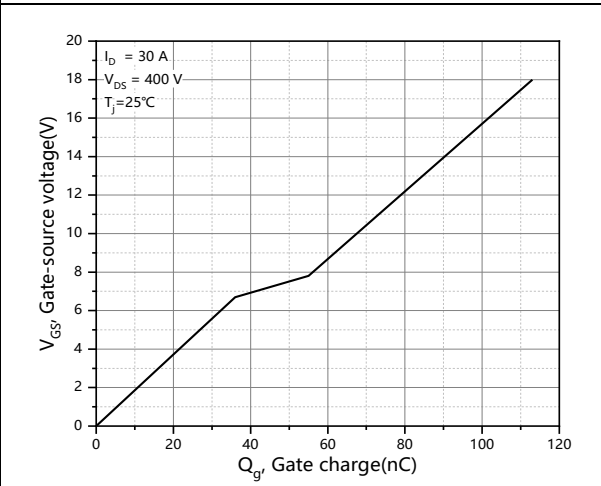
**Figure 7. Body diode characteristic at  $T_j = 25\text{ }^\circ\text{C}$**



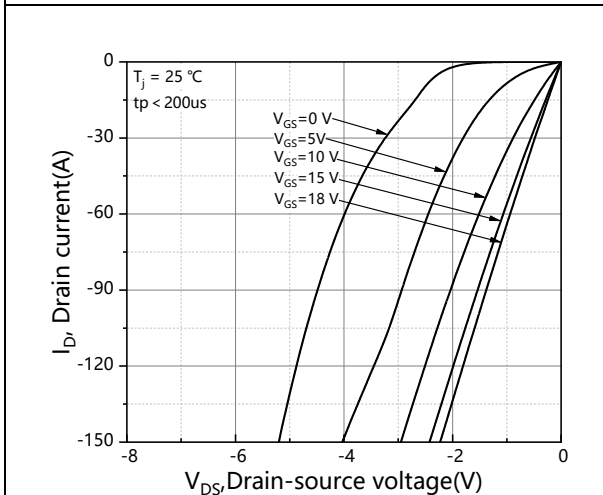
**Figure 8. Body diode characteristic at  $T_j = 175\text{ }^\circ\text{C}$**



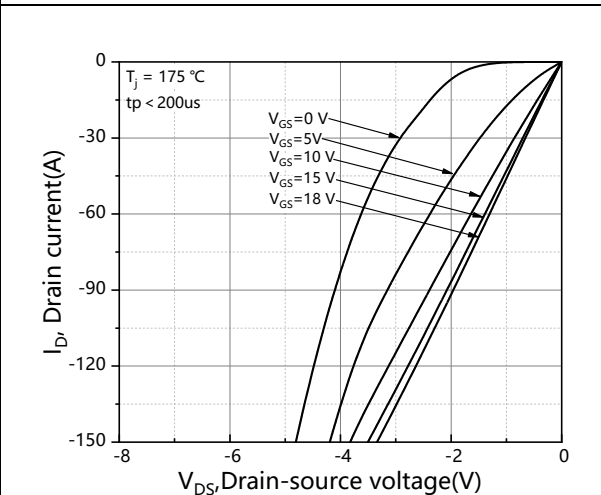
**Figure 9. Threshold voltage vs. temperature**



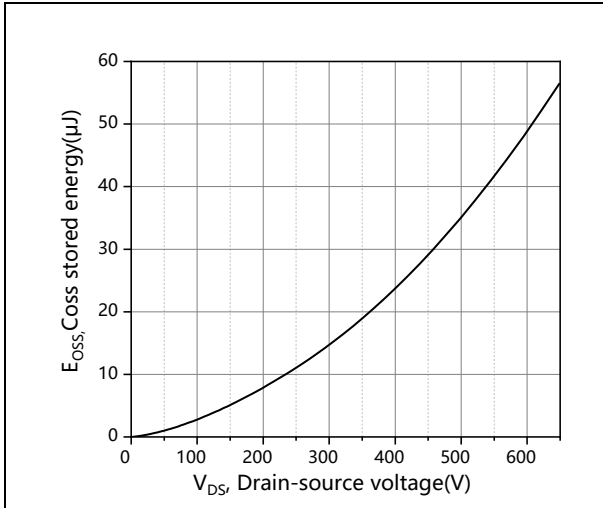
**Figure 10. Gate charge characteristic**



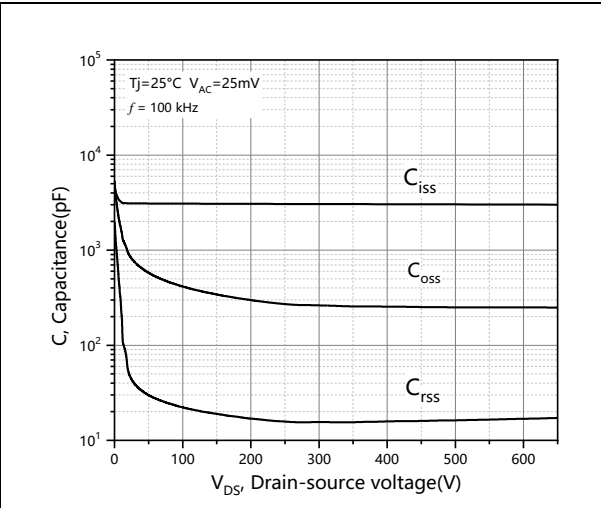
**Figure 11. 3rd quadrant characteristic at  $T_j = 25\text{ }^\circ\text{C}$**



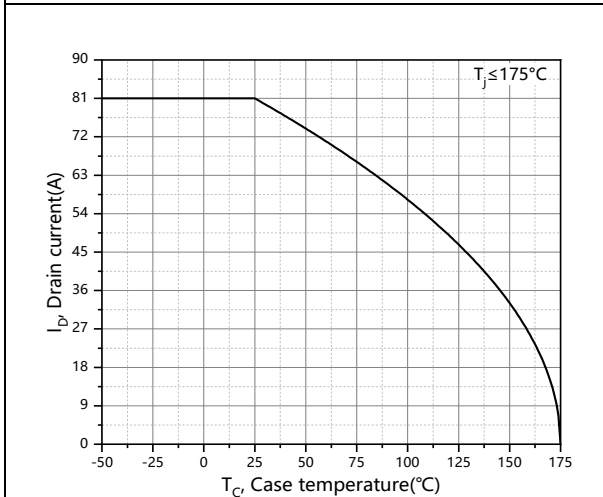
**Figure 12. 3rd quadrant characteristic at  $T_j = 175\text{ }^\circ\text{C}$**



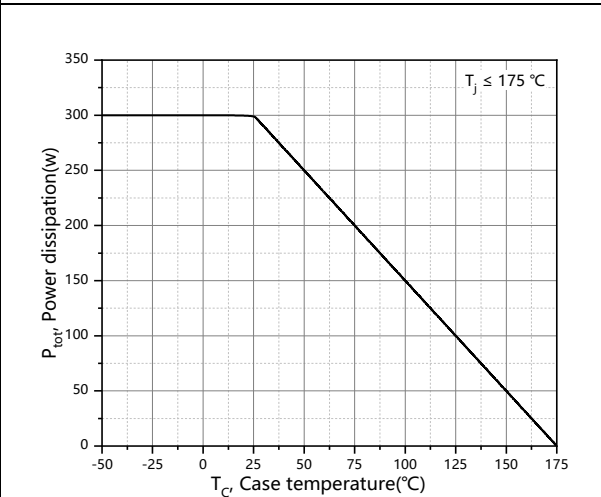
**Figure 13. Output capacitor stored energy**



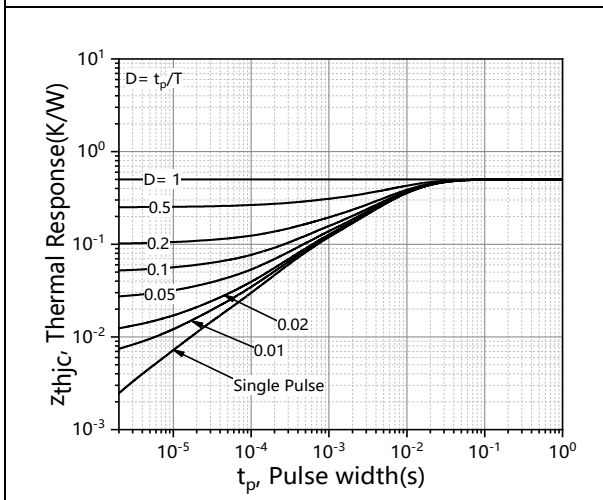
**Figure 14. Capacitances vs. drain-source voltage**



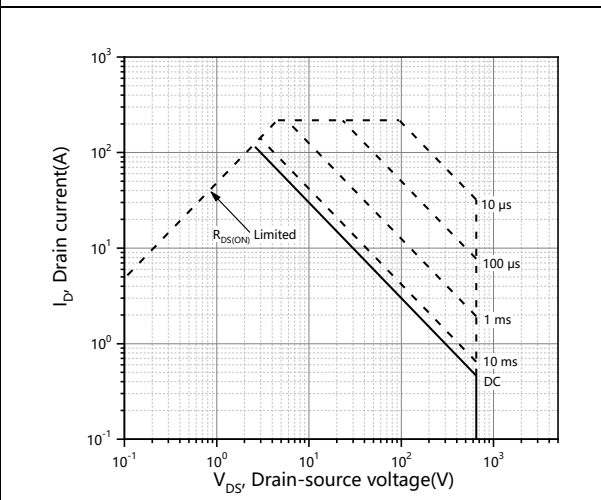
**Figure 15. Continuous drain current derating vs. case temperature**



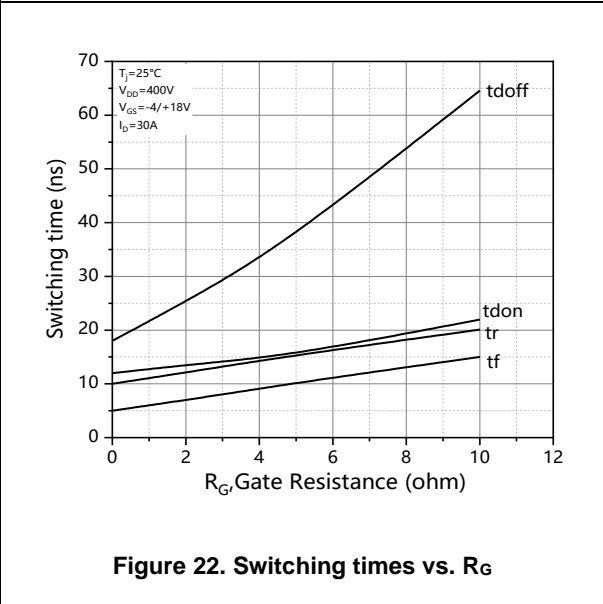
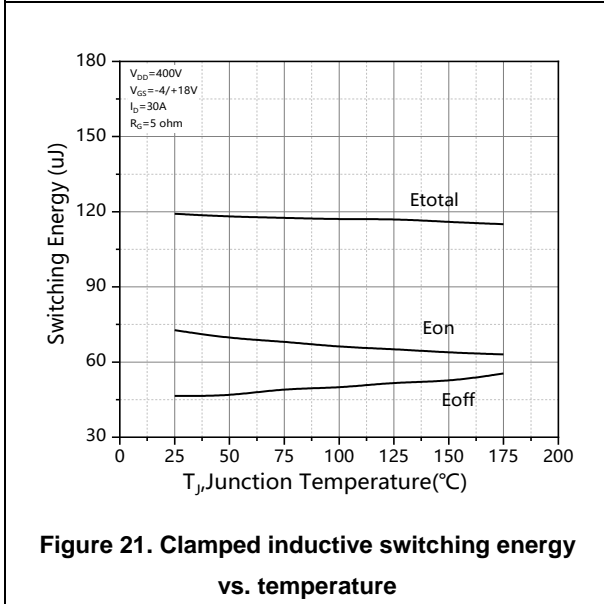
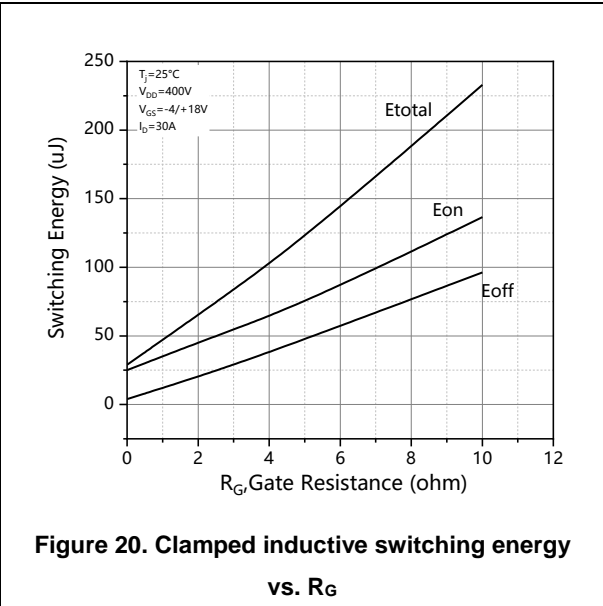
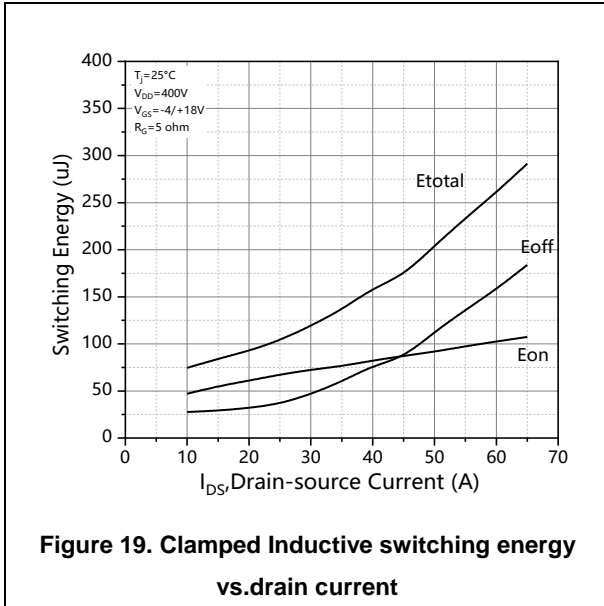
**Figure 16. Maximum power dissipation derating vs. case temperature**



**Figure 17. Transient thermal impedance (junction - case)**

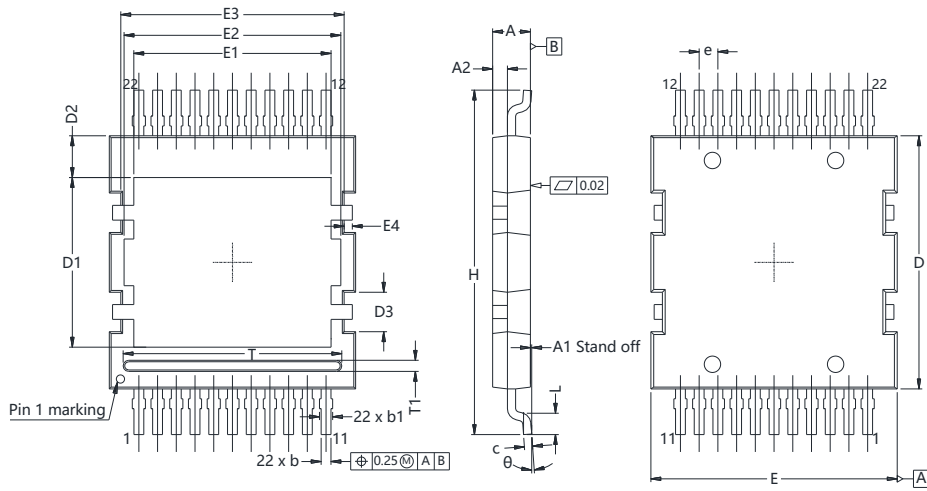


**Figure 18. Safe operating area**





**Package Information**



mm			
Symbol	Min	Nom	Max
A	2.200	2.300	2.350
A1	0.000	\	0.150
A2	0.800	0.900	1.000
b	0.500	0.600	0.700
b1	0.500	\	0.900
c	0.460	0.500	0.580
e		1.140 BSC	
D	15.300	15.400	15.500
D1	10.230	10.320	10.430
D2		2.540 REF	
D3		2.400 REF	
E	14.900	15.000	15.100
E1	11.910	12.010	12.110
E2		13.200 REF	
E3		13.600 REF	
E4		0.500 REF	
H	20.810	20.960	21.110
L	1.200	1.300	1.400
θ	0°	\	8°
T	13.210	13.310	13.410
T1	0.500	0.670	0.700

Version : QDPAK-W package outline dimension

### Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
QDPAK-W	1000	1	1000	5	5000

### Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSQ65R020QDT2F	QDPAK	yes	yes	yes

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## Revision History

Version	Revision History	Date
V0.1	Preliminary release	2025-06-04