

General Description

The silicon carbide power MOSFET utilizes Oriental-Semi's advanced technology to achieve low on-resistance, low gate charge, low Qrr and excellent thermal performance by exploiting innovative properties of wide bandgap materials. It is engineered to minimize conduction loss and provide superior switching performance almost independent of temperature.

The silicon carbide power MOSFET provides high reliability and extremely high efficiency. It is targeted to improve application performance in frequency, energy efficiency, reliability, system size and weight reduction.

Features

- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- IGBT-compatible driving voltage (15V for turn-on)
- Fast intrinsic diode with low reverse recovery (Qrr)
- Temperature independent turn-off switching losses
- Halogen free, RoHS compliant
- AEC-Q101 Qualified



Applications

- Solar inverters
- On-board charger/PFC
- EV motor drive
- High voltage DC/DC converters
- Switch mode power supplies

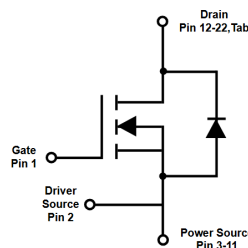
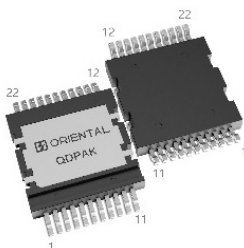
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	1200	V
I_D , pulse	123	A
$R_{DS(ON)}$, typ @ $V_{GS}=18V$	47	m Ω
Q_g	51	nC

Marking Information

Product Name	Package	Marking
OSQ120R045QDT3AF	QDPAK	OSQ120R045QDT3A

Package & Pin Information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	1200	V
Gate-source voltage (max static)	V_{GS}	-8/+22	V
Gate-source voltage (recommended operating values)	V_{GS}	-4/+18	V
Continuous drain current ¹⁾ , $T_C=25^\circ\text{C}$	I_D	48	A
Continuous drain current ¹⁾ , $T_C=100^\circ\text{C}$		34	
Pulsed drain current ²⁾ , $T_C=25^\circ\text{C}$	$I_{D, pulse}$	123	A
Power dissipation ³⁾ , $T_C=25^\circ\text{C}$	P_D	205	W
Operation and storage temperature	T_{stg}, T_j	-55 to 175	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction-case	$R_{\theta JC}$		0.56	0.73	$^\circ\text{C/W}$
Thermal resistance, junction-ambient	$R_{\theta JA}$			62	$^\circ\text{C/W}$

Electrical Characteristics at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	1200			V	$V_{GS}=0\text{ V}, I_D=100\ \mu\text{A}$
Gate threshold voltage	$V_{GS(th)}$	2.3	2.8	3.6	V	$V_{DS}=V_{GS}, I_D=8\text{ mA}$
Drain-source on-state resistance	$R_{DS(ON)}$		47	61	m Ω	$V_{GS}=18\text{ V}, I_D=30\text{ A}$
			89			$V_{GS}=18\text{ V}, I_D=30\text{ A}, T_j=175^\circ\text{C}$
			58	75	m Ω	$V_{GS}=15\text{ V}, I_D=30\text{ A}$
			93			$V_{GS}=15\text{ V}, I_D=30\text{ A}, T_j=175^\circ\text{C}$
Transconductance	g_{fs}		28		S	$V_{DS}=20\text{ V}, I_D=30\text{ A}$
Gate-source leakage current	I_{GSS}			± 100	nA	$V_{GS}=-8\sim 22\text{ V}$
Drain-source leakage current	I_{DSS}			100	μA	$V_{DS}=1200\text{ V}, V_{GS}=0\text{ V}$
				500	μA	$V_{DS}=1200\text{ V}, V_{GS}=0\text{ V}, T_j=175^\circ\text{C}$
Gate resistance	R_G		5		Ω	$f=1\text{ MHz}, \text{Open drain}, V_{AC}=25\text{ mV}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		1498		pF	$V_{GS}=0\text{ V}$, $V_{DS}=800\text{ V}$, $f=100\text{ KHz}$, $V_{AC}=25\text{ mV}$
Output capacitance	C_{oss}		61.3		pF	
Reverse transfer capacitance	C_{rss}		1.7		pF	
C_{oss} stored energy	E_{OSS}		25.1		μJ	
Turn-on delay time	$t_{d(on)}$		12.9		ns	$V_{GS}=-4/18\text{ V}$, $V_{DS}=800\text{ V}$, $R_G=5\ \Omega$, $I_D=30\text{ A}$, $L=120\ \mu\text{H}$, $T_j=25\text{ }^\circ\text{C}$
Rise time	t_r		12.1		ns	
Turn-off delay time	$t_{d(off)}$		23.2		ns	
Fall time	t_f		7.6		ns	
Turn-on switching energy	E_{on}		238		μJ	
Turn-off switching energy	E_{off}		58.6		μJ	
Turn-on switching energy	E_{on}		230		μJ	$V_{GS}=-4/18\text{ V}$, $V_{DS}=800\text{ V}$, $R_G=5\ \Omega$, $I_D=30\text{ A}$, $L=120\ \mu\text{H}$, $T_j=175\text{ }^\circ\text{C}$
Turn-off switching energy	E_{off}		71		μJ	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		51		nC	$V_{GS}=-4/18\text{ V}$, $V_{DS}=800\text{ V}$, $I_D=30\text{ A}$
Gate-source charge	Q_{gs}		18.2		nC	
Gate-drain charge	Q_{gd}		10.1		nC	

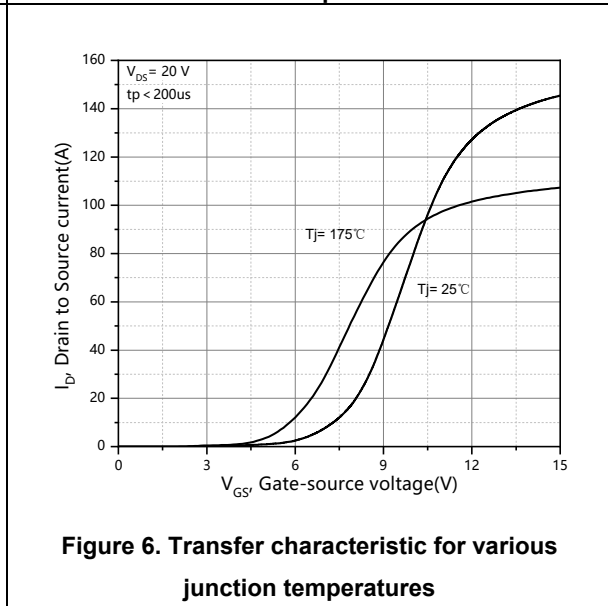
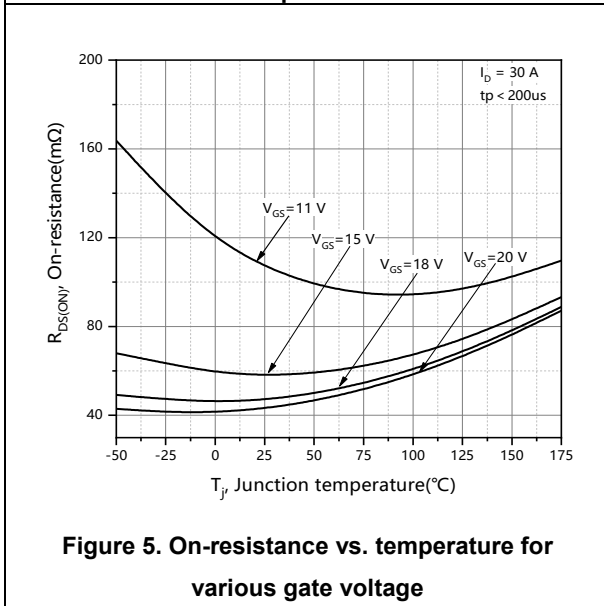
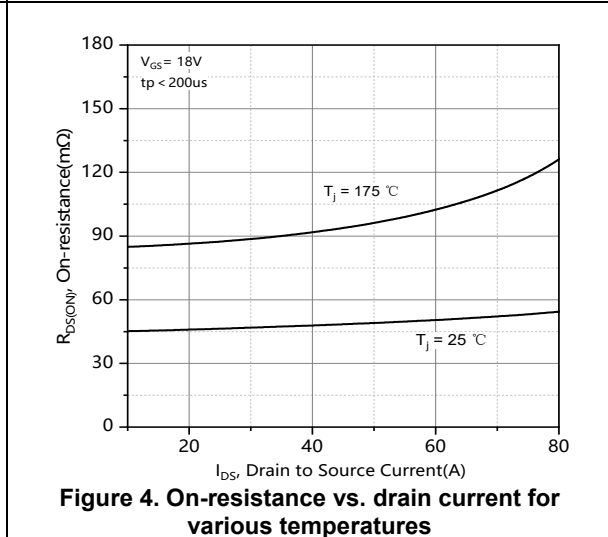
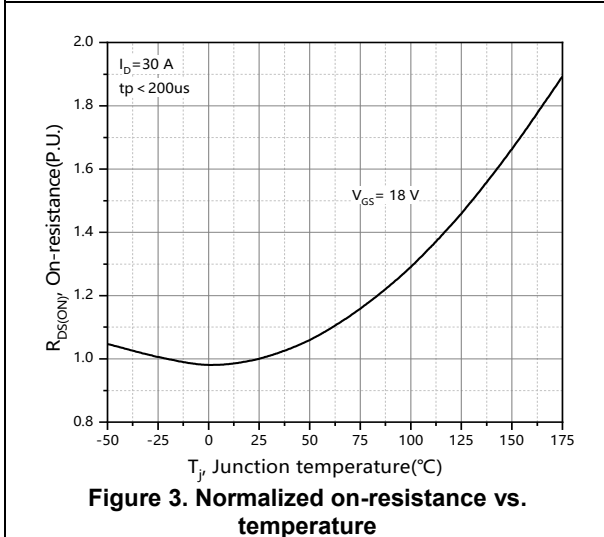
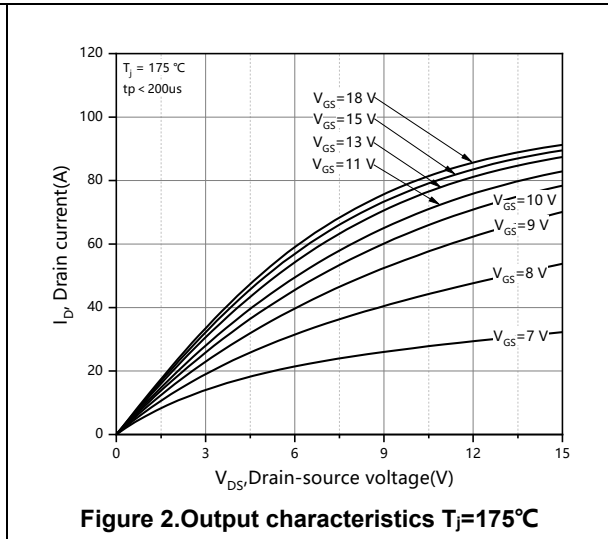
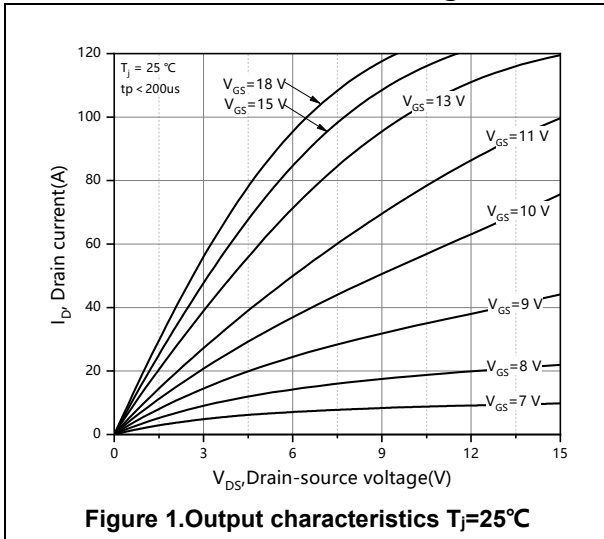
Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}		4.2		V	$I_S=20\text{ A}$, $V_{GS}=-4\text{ V}$, $T_J=25\text{ °C}$
			3.7		V	$I_S=20\text{ A}$, $V_{GS}=-4\text{ V}$, $T_J=175\text{ °C}$
Continuous diode forward current	I_S		48		A	$V_{GS}=-4\text{ V}$, $T_J=25\text{ °C}$
Reverse recovery time	t_{rr}		11.7		ns	$V_{GS}=-4\text{ V}$, $V_R=800\text{ V}$,
Reverse recovery charge	Q_{rr}		109		nC	$I_S=30\text{ A}$, $di/dt=3000\text{ A}/\mu\text{s}$, $T_J=25\text{ °C}$
Peak reverse recovery current	I_{rrm}		16.6		A	$T_J=25\text{ °C}$
Reverse recovery time	t_{rr}		76.8		ns	$V_{GS}=-4\text{ V}$, $V_R=800\text{ V}$,
Reverse recovery charge	Q_{rr}		480		nC	$I_S=30\text{ A}$, $di/dt=1000\text{ A}/\mu\text{s}$,
Peak reverse recovery current	I_{rrm}		11		A	$T_J=175\text{ °C}$

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.

Electrical Characteristics Diagrams



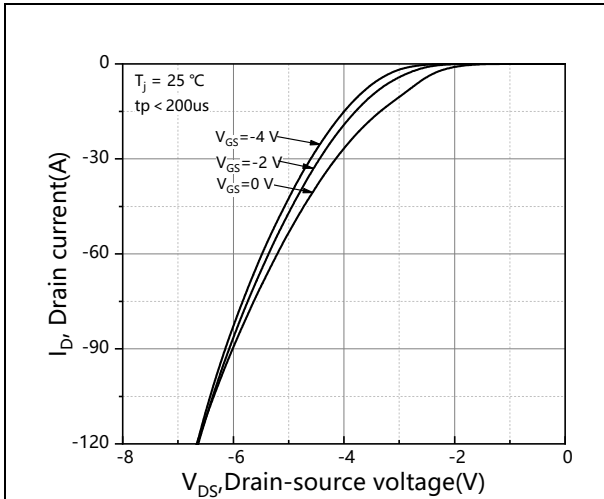


Figure 7. Body diode characteristic at $T_j = 25\text{ °C}$

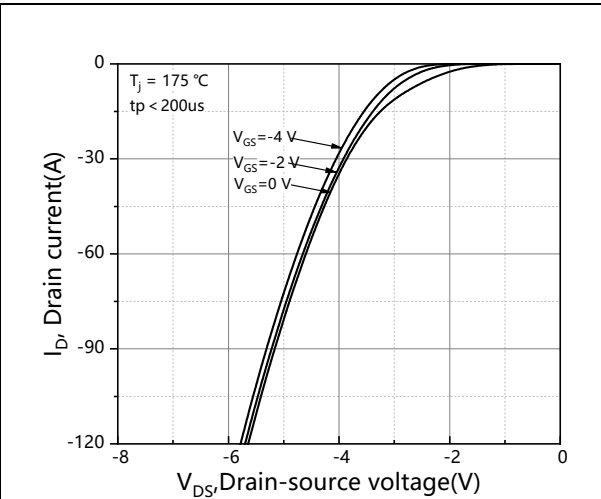


Figure 8. Body diode characteristic at $T_j = 175\text{ °C}$

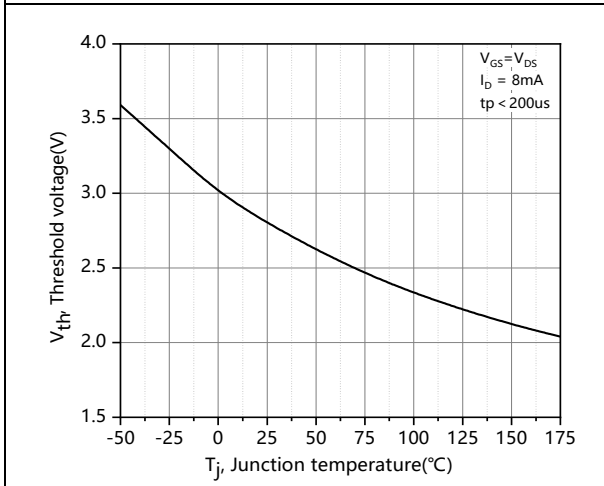


Figure 9. Threshold voltage vs. temperature

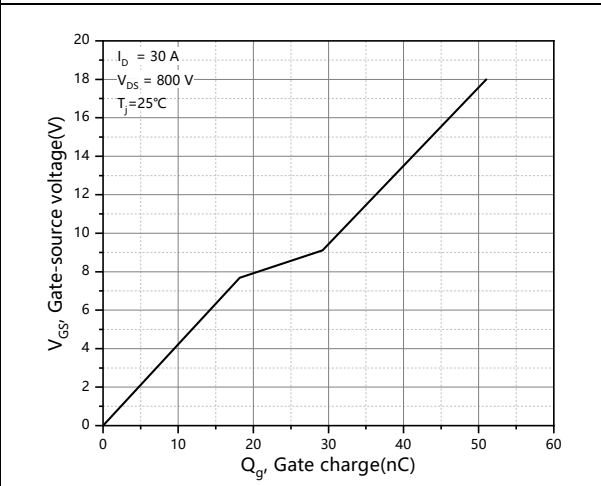


Figure 10. Gate charge characteristic

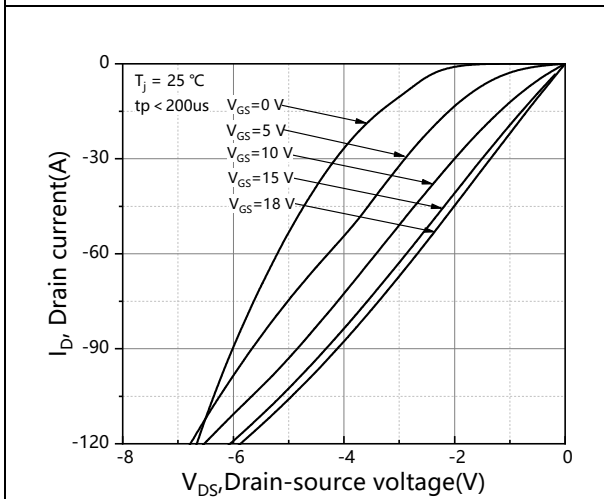


Figure 11. 3rd quadrant characteristic at $T_j = 25\text{ °C}$

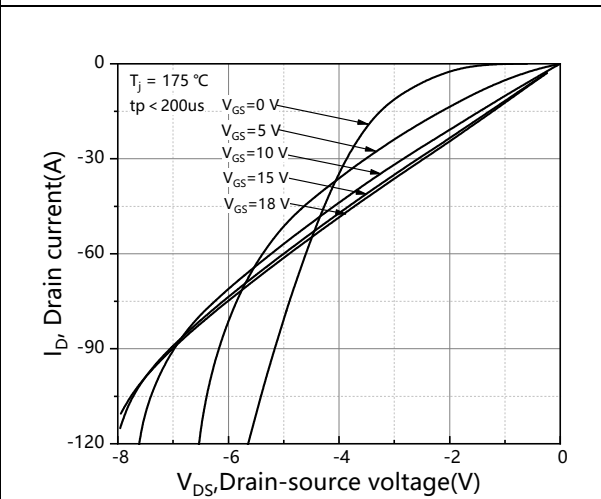


Figure 12. 3rd quadrant characteristic at $T_j = 175\text{ °C}$

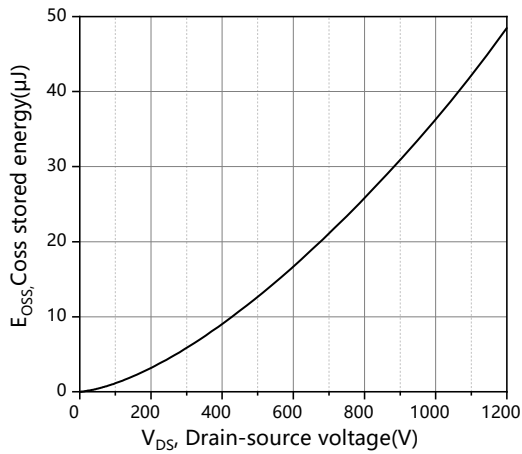


Figure 13. Output capacitor stored energy

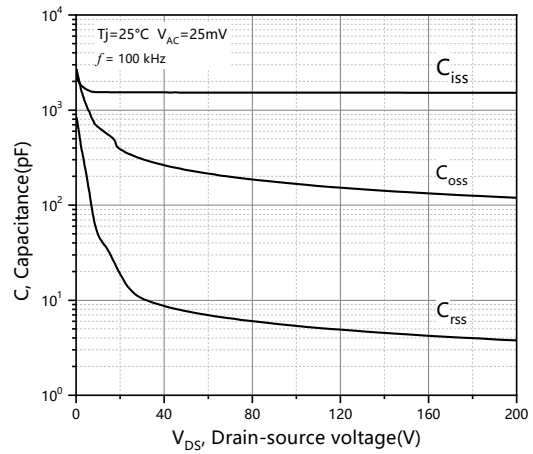


Figure 14. Capacitances vs. drain-source voltage (0 - 200V)

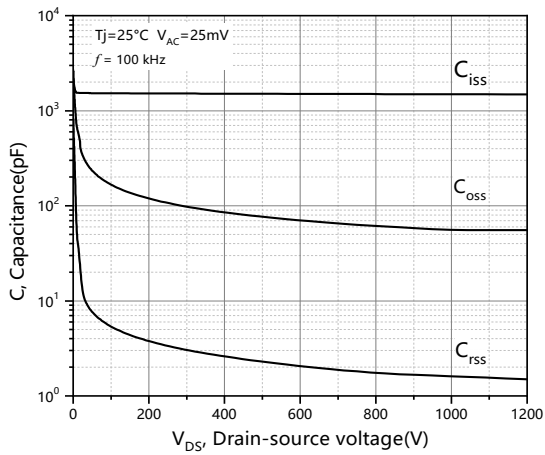


Figure 15. Capacitances vs. drain-source voltage (0 - 1200V)

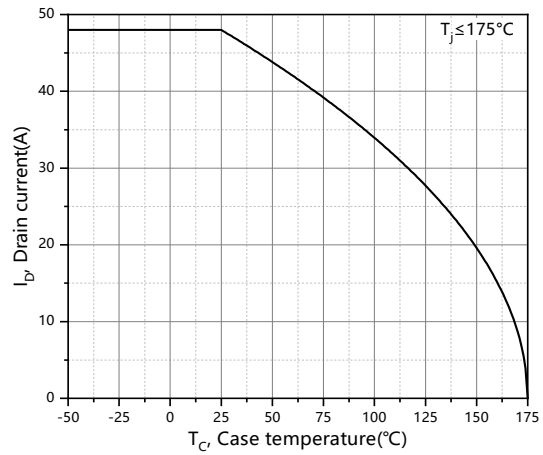


Figure 16. Continuous drain current derating vs. case temperature

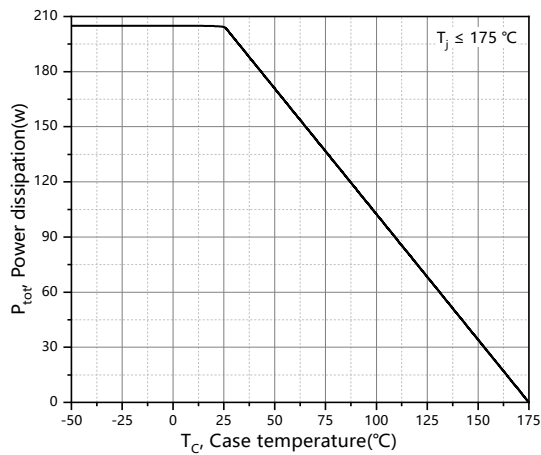


Figure 17. Maximum power dissipation derating vs. case temperature

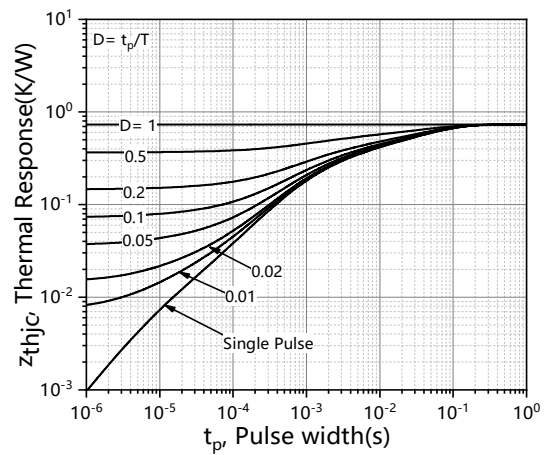
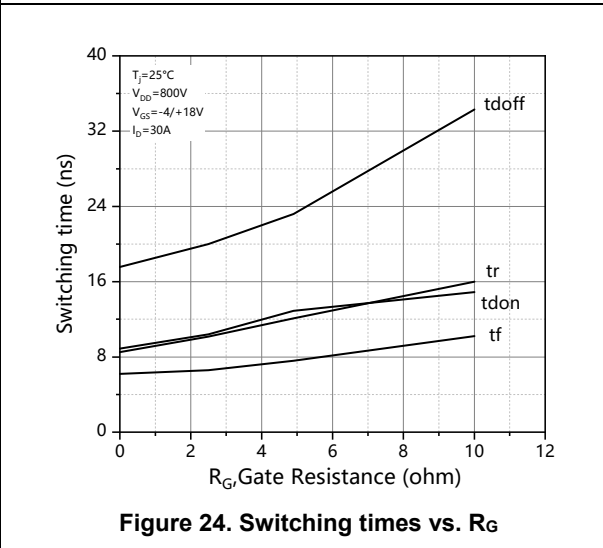
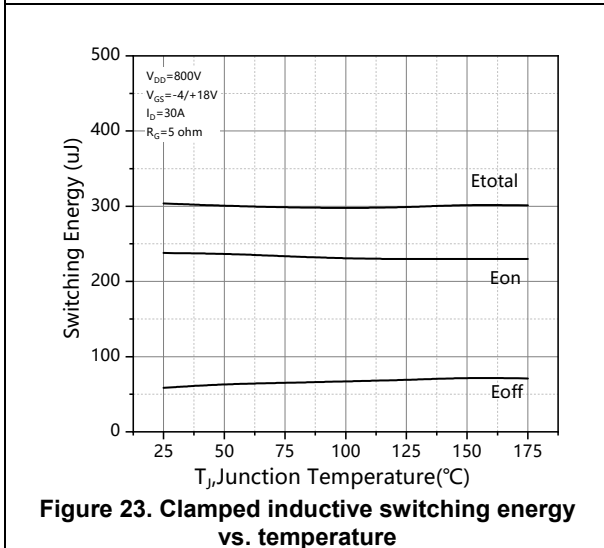
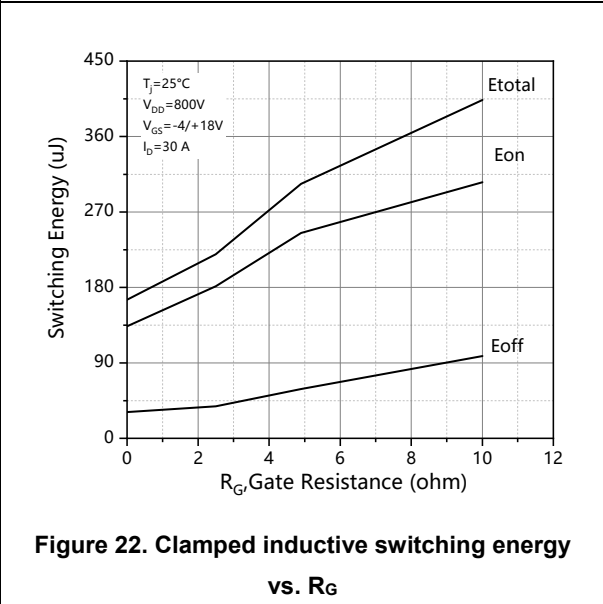
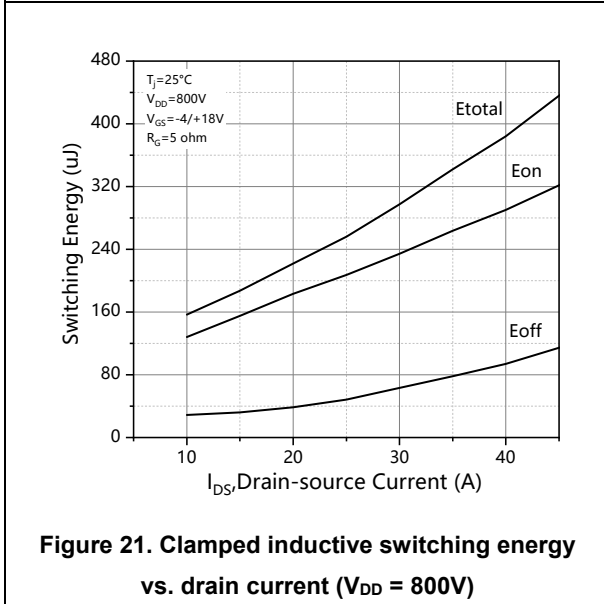
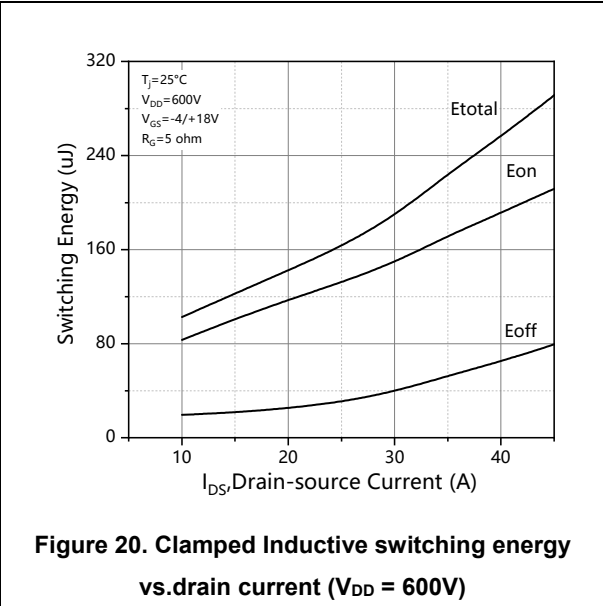
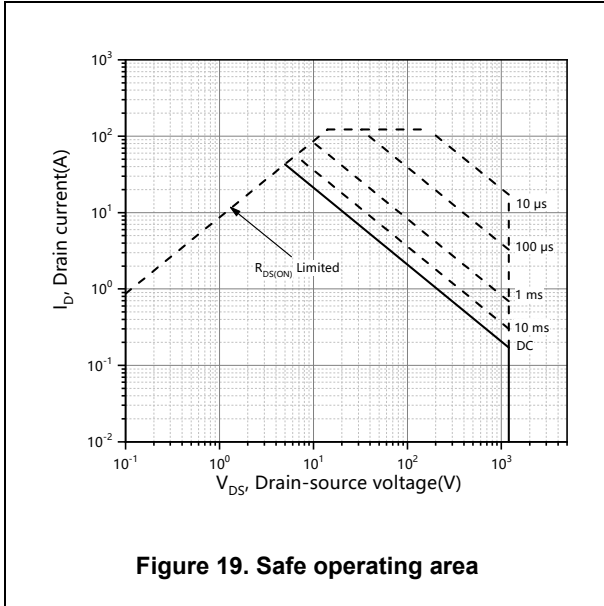
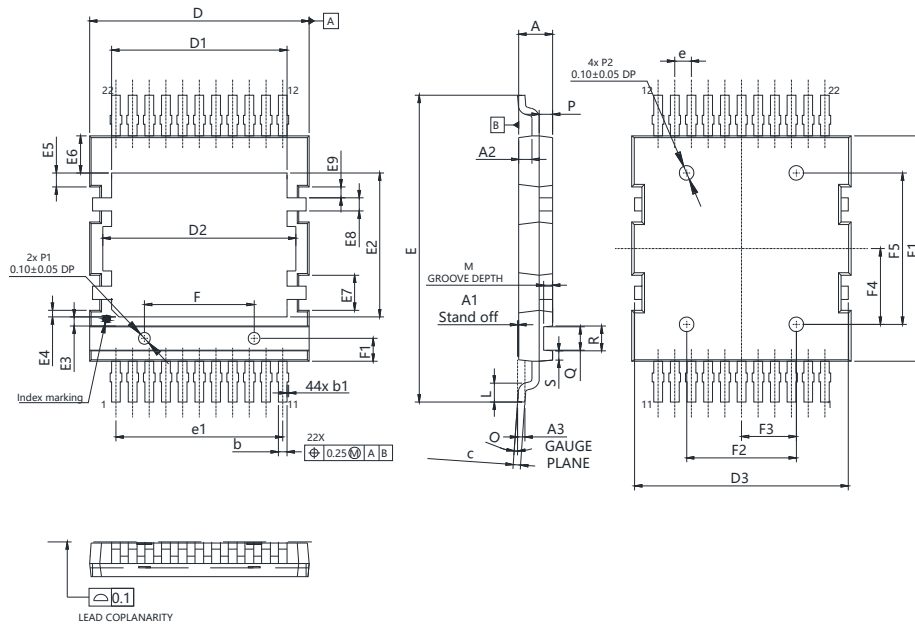


Figure 18. Transient thermal impedance (junction - case)



Package Information



Symbol	mm		Symbol	mm	
	Min	Max		Min	Max
A	2.25	2.35	E9	0.75	
A1	0.00	0.15	e	1.14	
A2	0.90		e1	11.4	
A3	0.50		F	7.40	7.60
b	0.50	0.70	F1	1.47	1.67
b1	/	0.15	F2	7.40	7.60
c	0.46	0.58	F3	3.65	3.85
D	14.90	15.10	F4	5.07	5.27
D1	12.00		F5	10.24	10.44
D2	13.20		L	1.30	
D3	14.50	14.70	M	0.60	
E	20.81	21.11	N	22	
E1	15.30	15.50	O	0°	8°
E2	9.83		P	0.90	
E3	0.625		P1	0.70	0.90
E4	0.45		P2	0.90	1.10
E5	0.95		Q	1.60	
E6	2.53		R	1.70	
E7	2.40		S	0.631	
E8	0.90				

Version : QDPAK-S package outline dimension

Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
QDPAK-S	800	1	800	4	3200

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSQ120R045QDT3AF	QDPAK	yes	yes	yes

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Oriental Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

For further information on technology, delivery terms and conditions and prices, please contact the Oriental Semiconductor sales representatives (www.orientalsemi.com).

© Oriental Semiconductor Co.,Ltd. All Rights Reserved /

Revision History

Version	Revision History	Date
V1.0	Initial release	2026-01-20