General Description

FSMOS® MOSFET is based on Oriental Semiconductor's unique device design to achieve low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. The low V_{th} series is specially optimized for synchronous rectification systems with low driving voltage.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery

RoHS compliant

Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switching mode power supply

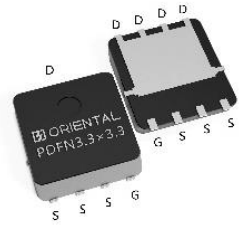
Key Performance Parameters

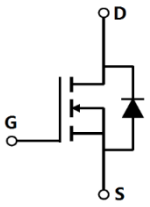
Parameter	Value	Unit
V_{DS}	25	V
$I_{D, pulse}$	255	A
$R_{DS(ON), max}$ @ V_{GS}=10V	1.9	mΩ
Q_g	34.6	nC

Marking Information

Product Name	Package	Marking
SFSE2R019UNF	PDFN3.3*3.3	SFSE2R019UN

Package & Pin information





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Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	25	V
Gate-source voltage	V_{GS}	±15	V
Continuous drain current[1], T_C=25 °C	I_D	85	A
Pulsed drain current[2], T_C=25 °C	$I_{D,\ pulse}$	255	A
Continuous diode forward current[1], T_C=25 °C	I_S	85	A
Diode pulsed current[2], T_C=25 °C	$I_{S,\ pulse}$	255	A
Power dissipation[3], T_C=25 °C	P_D	65	W
Single pulsed avalanche energy[5]	E_{AS}	109	mJ
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	1.92	°C/W
Thermal resistance, junction-ambient[4]	$R_{\theta JA}$	62	°C/W

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	25			V	V_{GS}=0 V, I_D=250 μA
Gate threshold voltage	$V_{GS(th)}$	1.0		3.0	V	V_{DS}=V_{GS}, I_D=250 μA
Drain-source on-state resistance	$R_{DS(ON)}$		1.5	1.9	mΩ	V_{GS}=10 V, I_D=50 A
Drain-source on-state resistance	$R_{DS(ON)}$		2.7	3.0	mΩ	V_{GS}=4.5 V, I_D=25 A
Gate-source leakage current	I_{GSS}			100	nA	V_{GS}=15 V
				-100		V_{GS}=-15 V
Drain-source leakage current	I_{DSS}			1	μA	V_{DS}=25 V, V_{GS}=0 V
Gate resistance	R_G		5		Ω	f=1 MHz, Open drain

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Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		2671		pF	V_{GS}=0 V, V_{DS}=25 V, f=100 kHz
Output capacitance	C_{oss}		1021		pF	
Reverse transfer capacitance	C_{rss}		50.2		pF	
Turn-on delay time	$t_{d(on)}$		21.6		ns	V_{GS}=10 V, V_{DS}=20 V, R_G=2 Ω, I_D=40 A
Rise time	t_r		7.4		ns	
Turn-off delay time	$t_{d(off)}$		51		ns	
Fall time	t_f		14.4		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		34.6		nC	V_{GS}=10 V, V_{DS}=20 V, I_D=40 A
Gate-source charge	Q_{gs}		8.3		nC	
Gate-drain charge	Q_{gd}		4.3		nC	
Gate plateau voltage	$V_{plateau}$		3.8		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	I_S=20 A, V_{GS}=0 V
Reverse recovery time	t_{rr}		56		ns	V_R=20 V, I_S=40 A, di/dt=100 A/μs
Reverse recovery charge	Q_{rr}		46		nC	
Peak reverse recovery current	I_{rrm}		1.3		A	

Note

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating; pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in^2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
5) V_{DD}=30 V,V_{GS}=10 V, L=0.3 mH, starting T_j=25 °C.

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Electrical Characteristics Diagrams

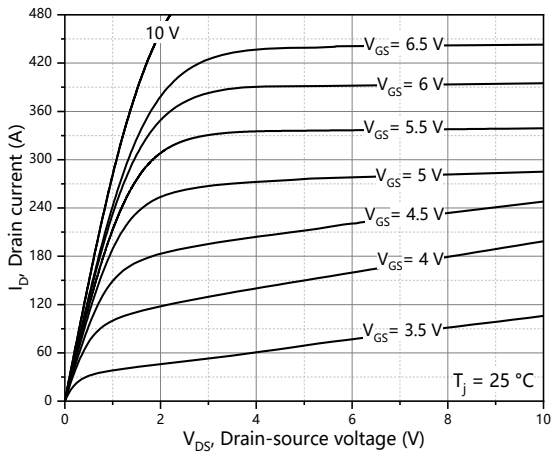

Figure 1. Typ. output characteristics

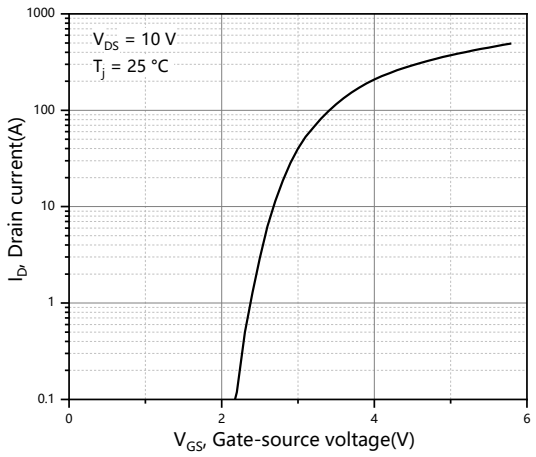

Figure 2. Typ. transfer characteristics

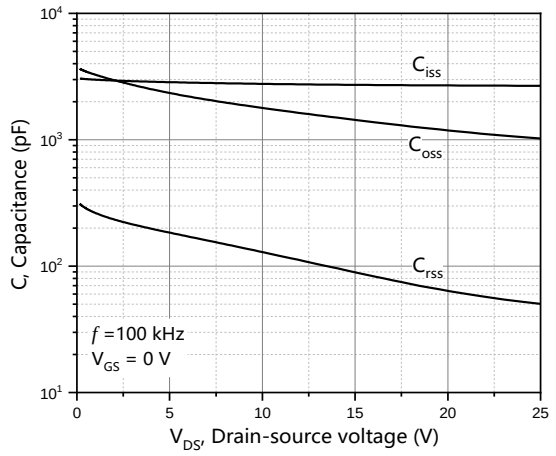

Figure 3. Typ. capacitances

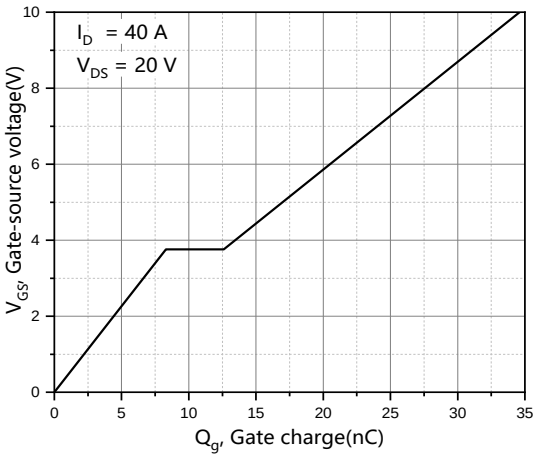

Figure 4. Typ. gate charge

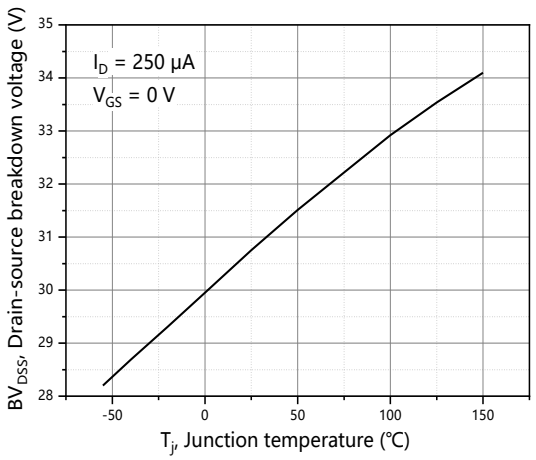

Figure 5. Drain-source breakdown voltage

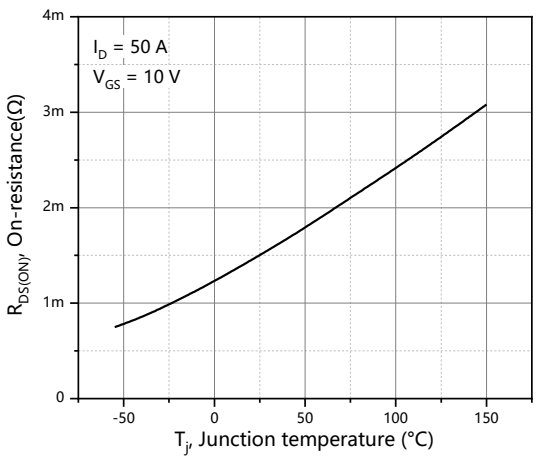

Figure 6. Drain-source on-state resistance

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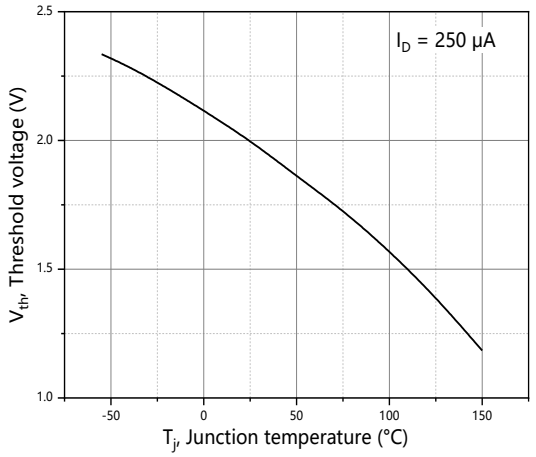

Figure 7. Threshold voltage

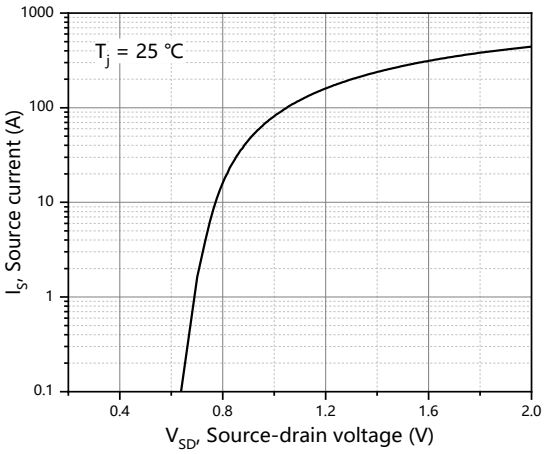

Figure 8. Forward characteristic of body diode

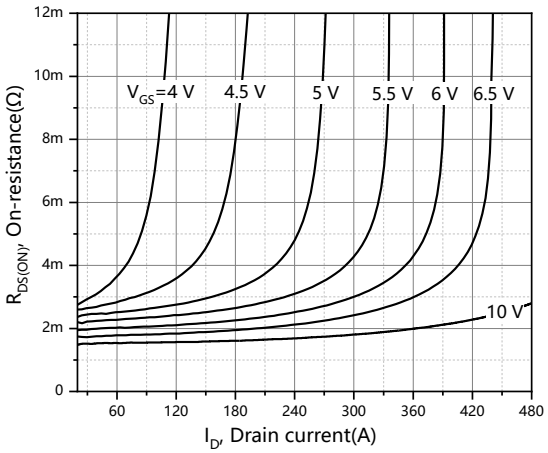

Figure 9. Drain-source on-state resistance

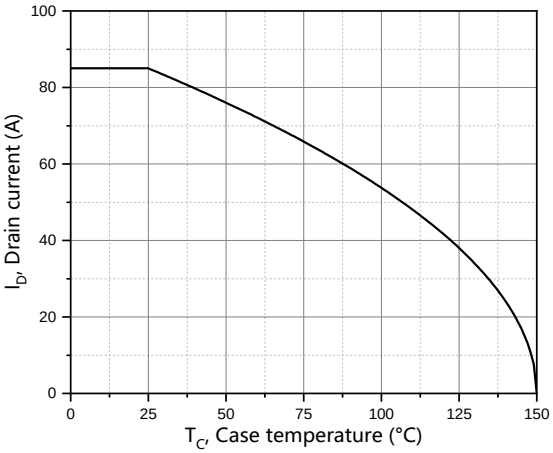

Figure 10. Drain current

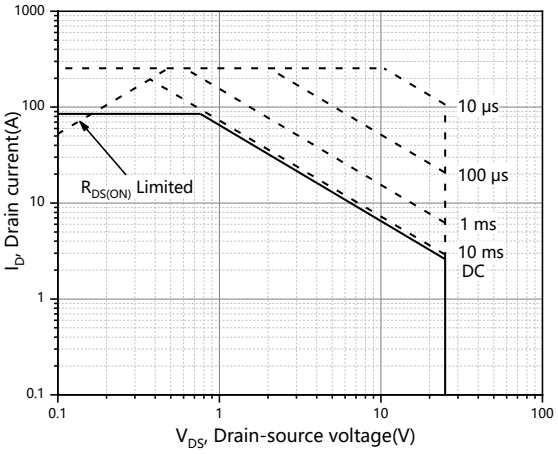

Figure 11. Safe operation area T_C=25℃

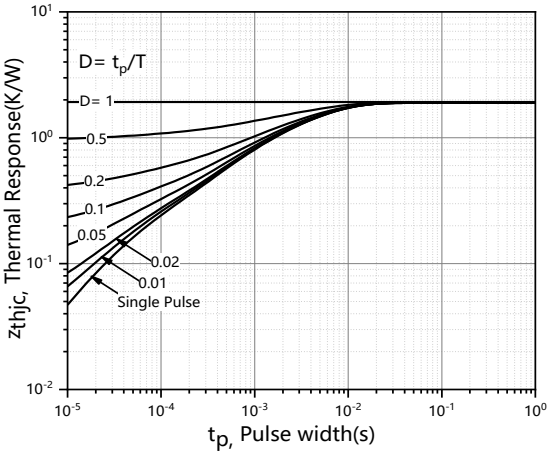

Figure 12. Max. transient thermal impedance

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Test circuits and waveforms

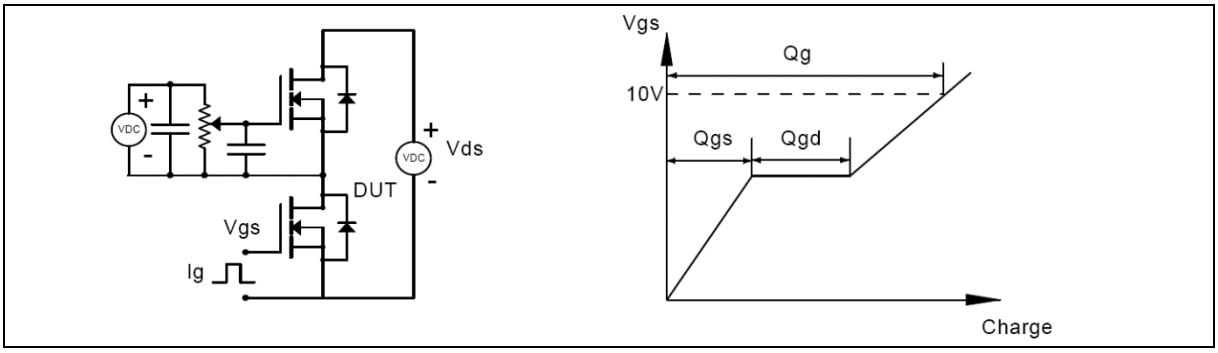


Figure 1. Gate charge test circuit & waveform

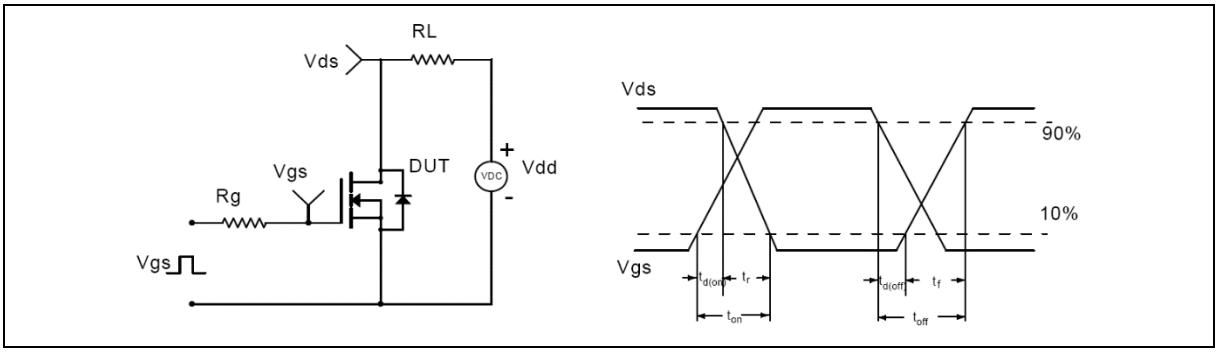


Figure 2. Switching time test circuit & waveforms

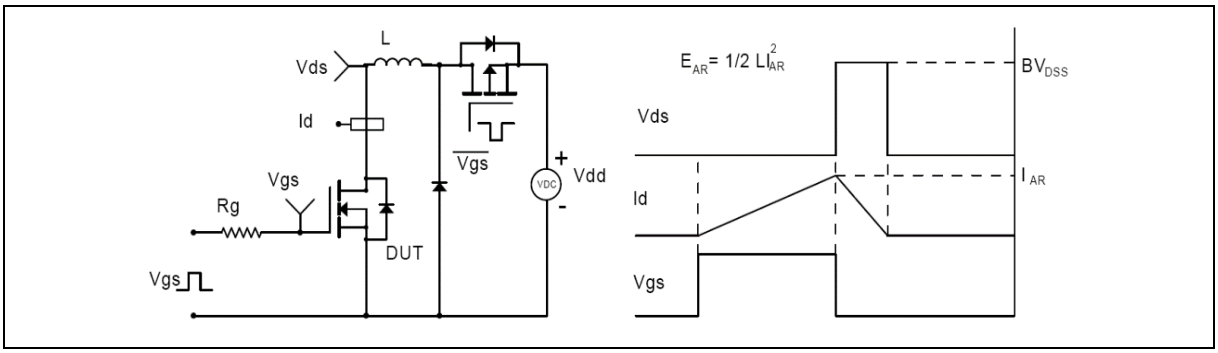


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

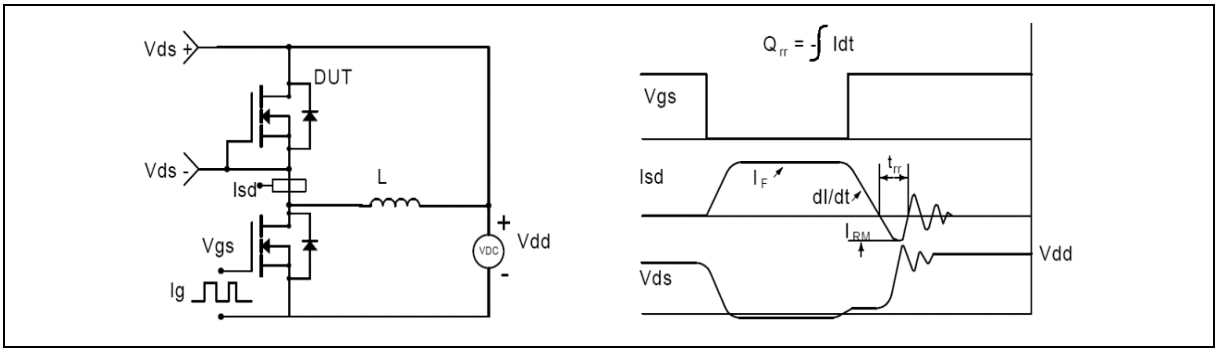
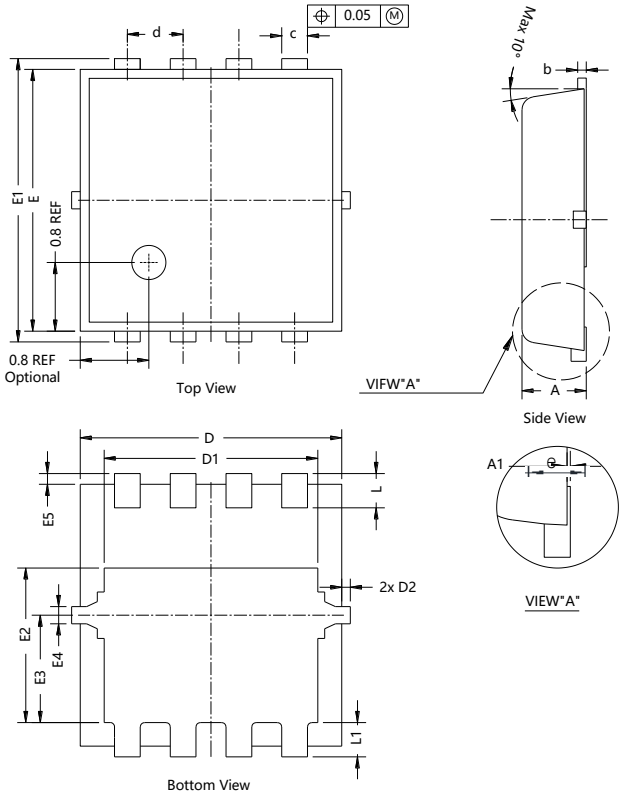


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Swmbols	mm		
	Min	Nom	Max
A	0.700	0.750	0.800
A1	-	-	0.05
b	0.144	0.152	0.202
c	0.250	0.300	0.350
d		0.65 BSC	
D	2.950	3.050	3.150
D1	2.390	2.490	2.590
D2	-	-	0.125
E	2.950	3.050	3.150
E1	3.200	3.300	3.400
E2	1.700	1.800	1.900
E3	1.150	1.250	1.350
E4	0.150	0.200	0.250
E5	0.075	0.125	0.175
L	0.300	0.400	0.500
L1	0.300	0.400	0.500

Version : PDFN3.3*3.3-H package outline dimension

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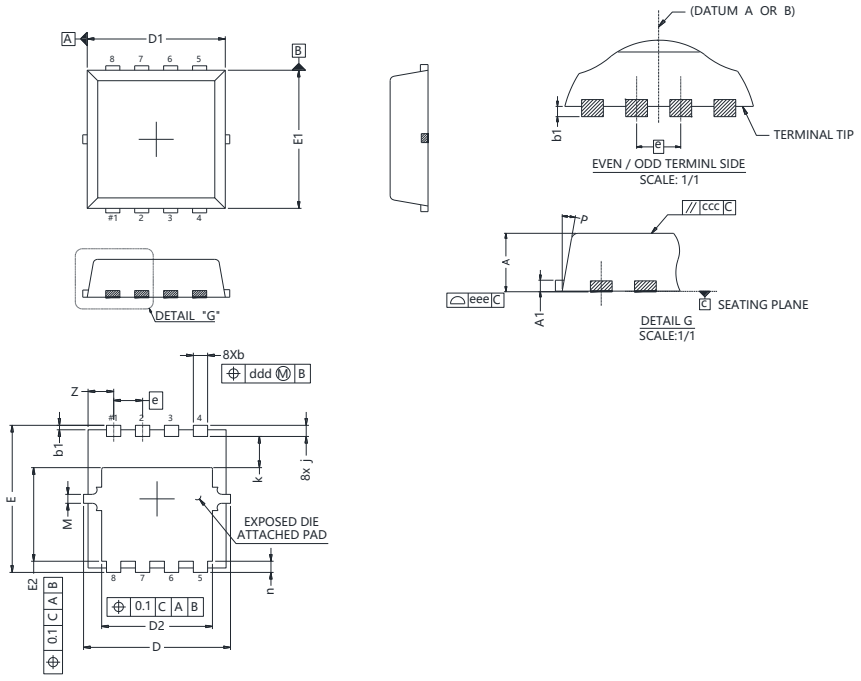
Package Information



Symbol	mm	
	Min	Max
A	0.80	0.90
A1	0.12	0.22
b	0.27	0.37
b1	0.05	0.15
D	3.00	3.60
D1	3.00	3.20
D2	2.29	2.69
E	3.00	3.60
E1	3.00	3.2
E2	2.00	2.2
e	0.60	0.70
j	0.15	0.35
k	0.60	0.80
n	0.15	0.35
M	0.20	
P	9°	11°
z	0.58	
ccc	0.10	
ddd	0.05	
eee	0.05	

Version : PDFN3.3*3.3-S package outline dimension

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Ordering Information

Package Type	Units/ Reel	Reels/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
PDFN3.3*3.3-H	5000	2	10000	5	50000
PDFN3.3*3.3-S	5000	1	5000	8	40000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFSE2R019UNF	PDFN3.3*3.3	yes	yes	yes

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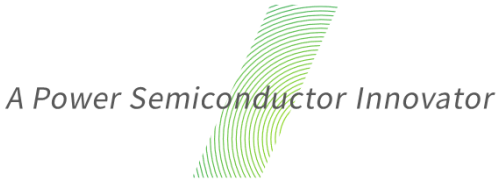
Revision History

Version	Revision History	Date
V1.0	Initial release	2020-12-08
V1.1	Update pod	2026-04-16



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