



General Description

FSMOS[®] MOSFET is based on Oriental Semiconductor's unique device design to achieve low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. The high V_{th} series is specially designed to use in motor control systems with driving voltage of more than 10V.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery



Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switching mode power supply

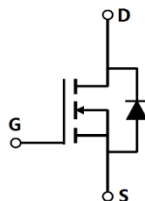
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	150	V
I_D , pulse	920	A
$R_{DS(ON)}$, max @ $V_{GS}=10V$	4	m Ω
Q_g	90	nC

Marking Information

Product Name	Package	Marking
SFS15R045K7NF	TO263-6L	SFS15R045K7N

Package & Pin information



Absolute Maximum Ratings at $T_j=25^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	150	V
Gate-source voltage	V_{GS}	± 20	V
Continuous drain current ¹⁾ , $T_C=25^{\circ}\text{C}$	I_D	230	A
Pulsed drain current ²⁾ , $T_C=25^{\circ}\text{C}$	$I_{D, \text{pulse}}$	920	A
Continuous diode forward current ¹⁾ , $T_C=25^{\circ}\text{C}$	I_S	230	A
Diode pulsed current ²⁾ , $T_C=25^{\circ}\text{C}$	$I_{S, \text{pulse}}$	920	A
Power dissipation ³⁾ , $T_C=25^{\circ}\text{C}$	P_D	535	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	487	mJ
Operation and storage temperature	T_{stg}, T_j	-55 to 175	$^{\circ}\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	0.28	$^{\circ}\text{C/W}$
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	62	$^{\circ}\text{C/W}$

Electrical Characteristics at $T_j=25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	150			V	$V_{GS}=0\text{ V}, I_D=1\text{ mA}$
Gate threshold voltage	$V_{GS(th)}$	3		5	V	$V_{DS}=V_{GS}, I_D=250\text{ }\mu\text{A}$
Drain-source on-state resistance	$R_{DS(ON)}$		3	4	$\text{m}\Omega$	$V_{GS}=10\text{ V}, I_D=50\text{ A}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=20\text{ V}$
				-100		$V_{GS}=-20\text{ V}$
Drain-source leakage current	I_{DSS}			1	μA	$V_{DS}=150\text{ V}, V_{GS}=0\text{ V}$
Gate resistance	R_G		3.5		Ω	$f=1\text{ MHz}$, Open drain

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		6186		pF	$V_{GS}=0\text{ V}$, $V_{DS}=25\text{ V}$, $f=100\text{ kHz}$
Output capacitance	C_{oss}		3582		pF	
Reverse transfer capacitance	C_{rss}		219		pF	
Turn-on delay time	$t_{d(on)}$		24		ns	$V_{GS}=10\text{ V}$, $V_{DS}=80\text{ V}$, $R_G=2\ \Omega$, $I_D=40\text{ A}$
Rise time	t_r		22		ns	
Turn-off delay time	$t_{d(off)}$		93		ns	
Fall time	t_f		44		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		90		nC	$V_{GS}=10\text{ V}$, $V_{DS}=80\text{ V}$, $I_D=40\text{ A}$
Gate-source charge	Q_{gs}		28		nC	
Gate-drain charge	Q_{gd}		27		nC	
Gate plateau voltage	$V_{plateau}$		5.2		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	$I_S=50\text{ A}$, $V_{GS}=0\text{ V}$
Reverse recovery time	t_{rr}		95		ns	$V_R=80\text{ V}$, $I_S=40\text{ A}$, $di/dt=100\text{ A}/\mu\text{s}$
Reverse recovery charge	Q_{rr}		150		nC	
Peak reverse recovery current	I_{rrm}		2.7		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 inch² FR-4 board with 2oz. Copper, in a still air environment with $T_a=25\text{ }^{\circ}\text{C}$.
- 5) $V_{DD}=50\text{ V}$, $V_{GS}=10\text{ V}$, $L=0.3\text{ mH}$, starting $T_j=25\text{ }^{\circ}\text{C}$.

Electrical Characteristics Diagrams

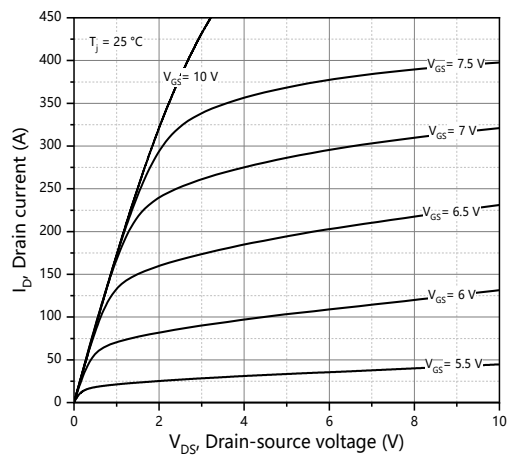


Figure 1. Typ. output characteristics

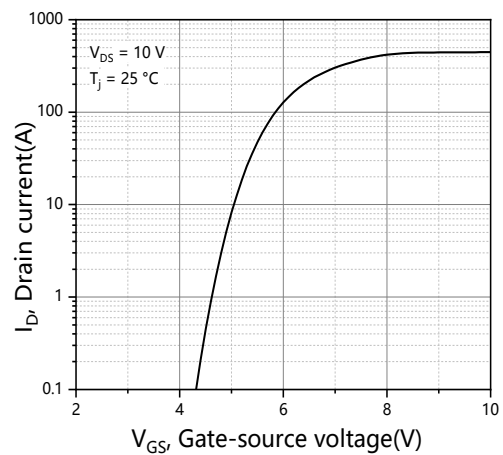


Figure 2. Typ. transfer characteristics

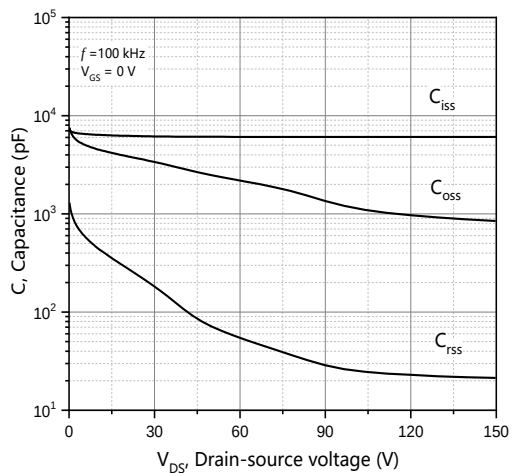


Figure 3. Typ. capacitances

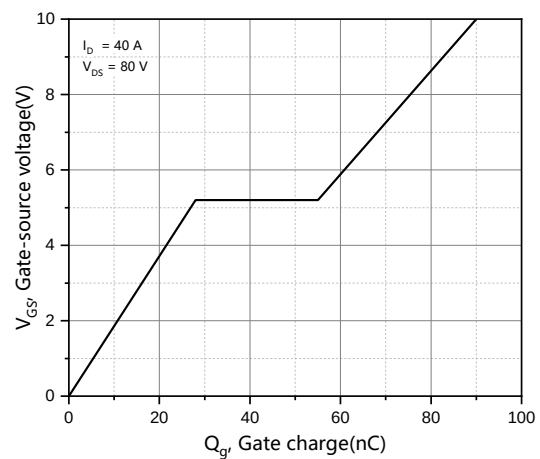


Figure 4. Typ. gate charge

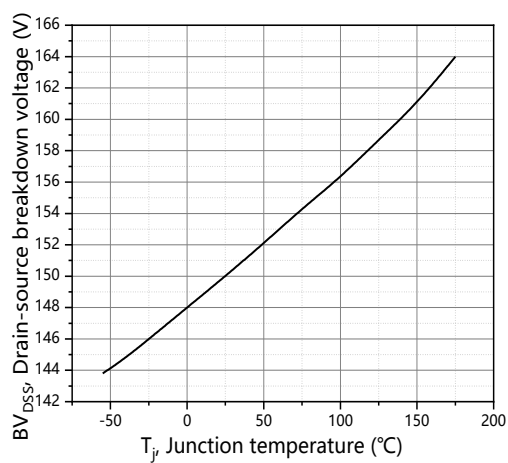


Figure 5. Drain-source breakdown voltage

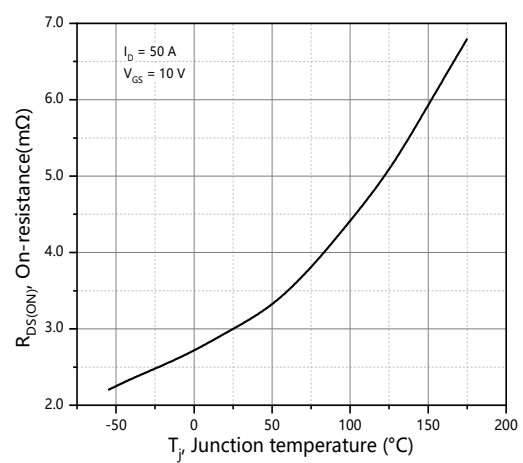


Figure 6. Drain-source on-state resistance

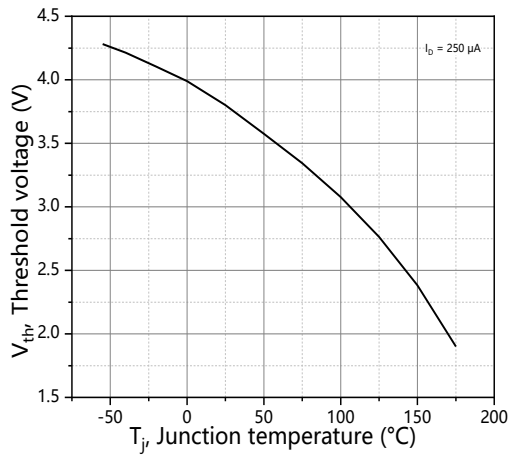


Figure 7. Threshold voltage

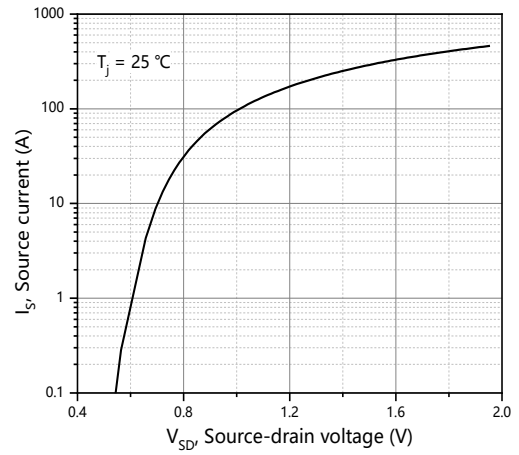


Figure 8. Forward characteristic of body diode

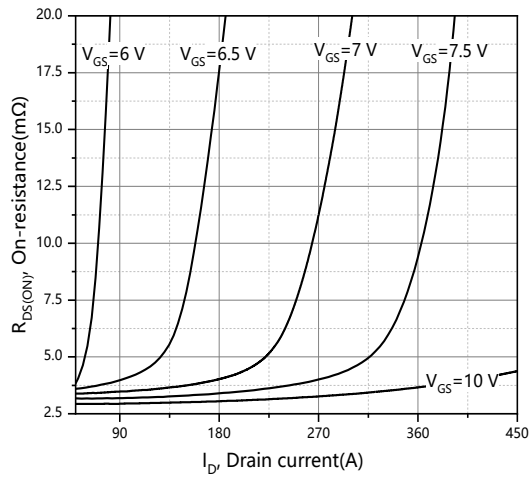


Figure 9. Drain-source on-state resistance

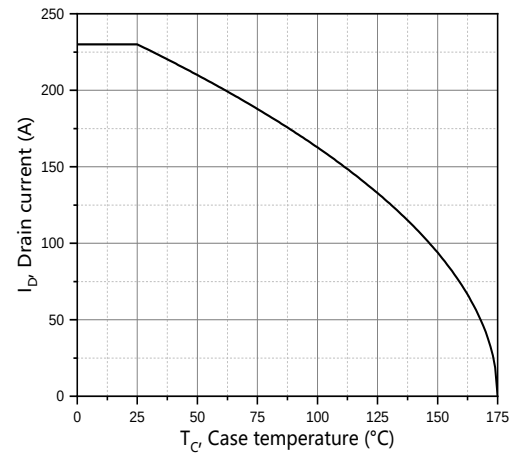


Figure 10. Drain current

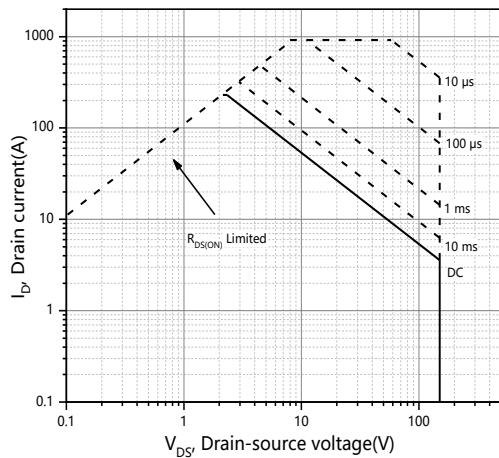


Figure 11. Safe operation area $T_C=25^\circ C$

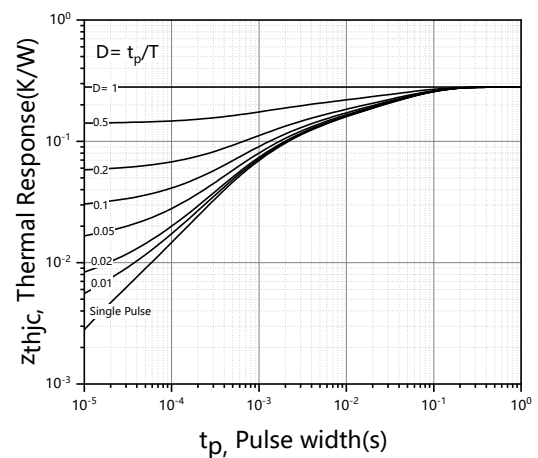


Figure 12. Max. transient thermal impedance

Test circuits and waveforms

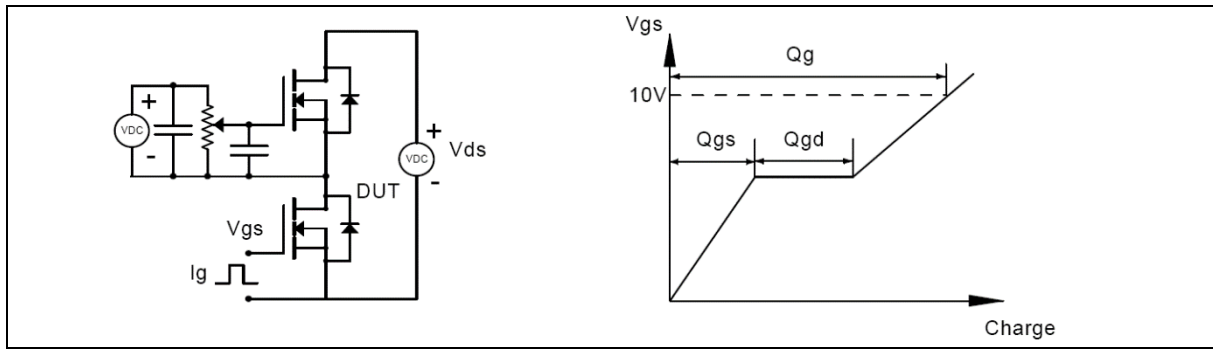


Figure 1. Gate charge test circuit & waveform

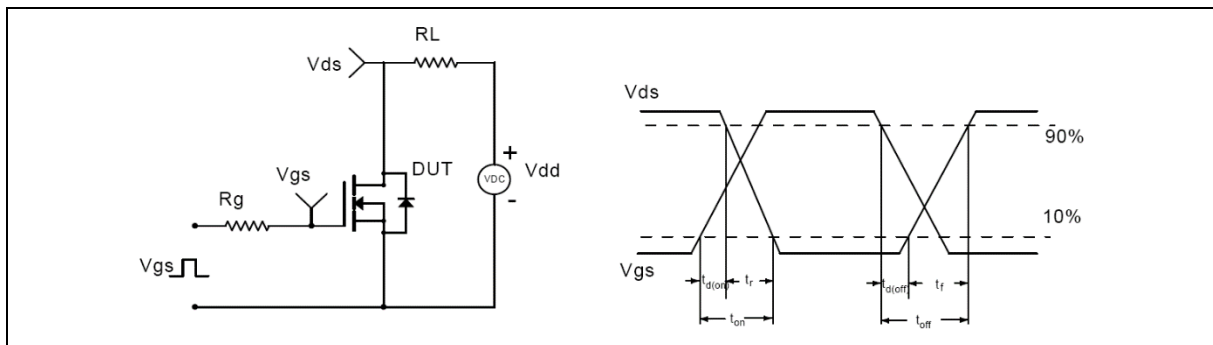


Figure 2. Switching time test circuit & waveforms

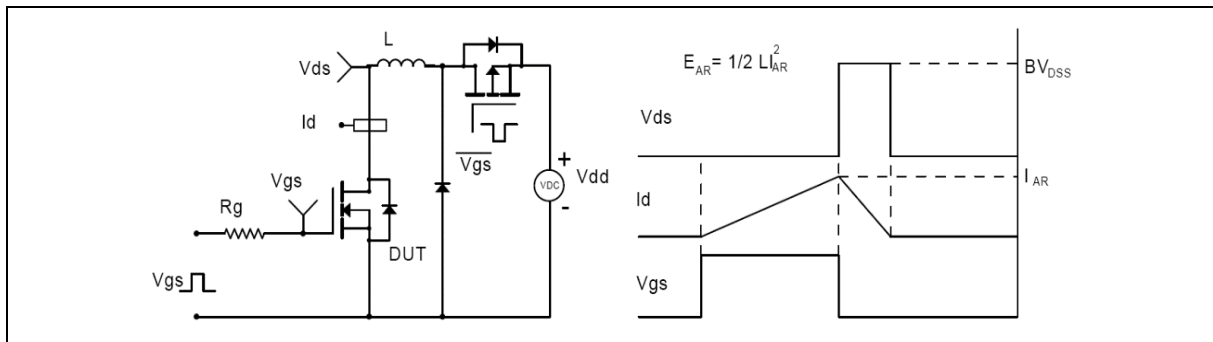


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

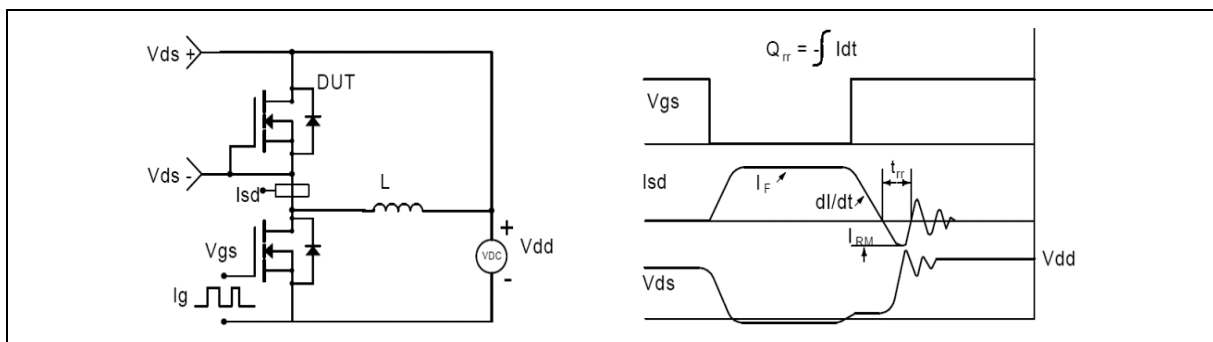
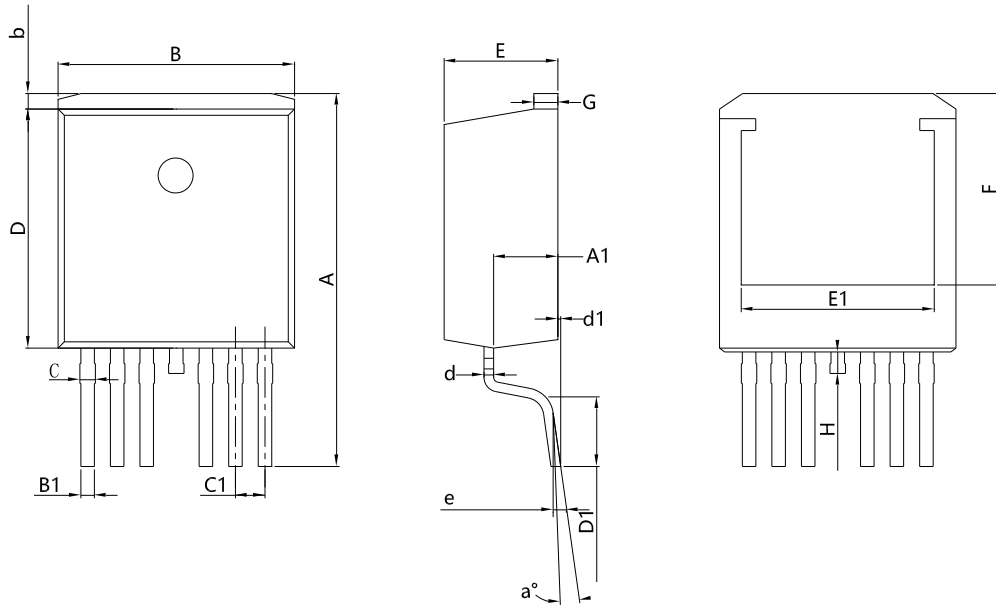


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	14.65	15.00	15.35
A1	2.25	2.40	2.55
B	9.80	10.00	10.20
B1	0.50	0.60	0.70
b	0.80	1.00	1.20
C	0.58	0.68	0.84
C1	1.27 BSC		
D	9.05	9.25	9.45
D1	2.40	2.70	3.00
d	0.40	0.50	0.60
d1	0.01	0.13	0.25
E	4.25	4.40	4.55
E1	7.25	-	-
F	6.90	-	-
e	0.25 BSC		
G	1.20	1.30	1.40
a	2°	5°	8°
H	0.85	1.00	1.15

Version 1: TO263 -6L-P package outline dimension

Ordering Information

Package Type	Units/ Tube	Tubes / Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO263 -6L-P	800	1	800	5	4000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFS15R045K7NF	TO263 -6L	yes	yes	yes

Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Oriental Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

For further information on technology, delivery terms and conditions and prices, please contact the Oriental Semiconductor sales representatives (www.orientalsemi.com).

© Oriental Semiconductor Co.,Ltd. All Rights Reserved 