General Description

FSMOS® MOSFET is based on Oriental Semiconductor's unique device design to achieve low $R_{DS(ON)}$, low gate charge, fast switching and excellent avalanche characteristics. The low V_{th} series is specially optimized for synchronous rectification systems with low driving voltage.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery

RoHS compliant

Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switching mode power supply

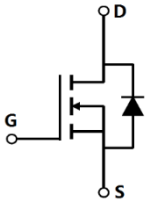
Key Performance Parameters

Parameter	Value	Unit
V_{DS}	100	V
$I_{D, pulse}$	216	A
$R_{DS(ON), max}$ @ V_{GS}=10V	7.5	mΩ
Q_g	30	nC
PD	36	W

Marking Information

Product Name	Package	Marking
SFS10R075FF_NB	TO-220F-NL	SFS10R075F

Package & Pin information



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Absolute Maximum Ratings at T_j=25°C unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	100	V
Gate-source voltage	V_{GS}	±20	V
Continuous drain current[1], T_C=25 °C	I_D	54	A
Pulsed drain current[2], T_C=25 °C	$I_{D,\ pulse}$	216	A
Continuous diode forward current[1], T_C=25 °C	I_S	54	A
Diode pulsed current[2], T_C=25 °C	$I_{S,\ pulse}$	216	A
Power dissipation[3], T_C=25 °C	P_D	36	W
Single pulsed avalanche energy[4]	E_{AS}	72	mJ
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	3.5	°C/W
Thermal resistance, junction-ambient	$R_{\theta JA}$	62.5	°C/W

Electrical Characteristics at T_j=25°C unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	100			V	V_{GS}=0 V, I_D=250 μA
Gate threshold voltage	$V_{GS(th)}$	1		2.3	V	V_{DS}=V_{GS}, I_D=250 μA
Drain-source on-state resistance	$R_{DS(ON)}$		7	7.5	mΩ	V_{GS}=10 V, I_D=50 A
Drain-source on-state resistance	$R_{DS(ON)}$		9.3	10	mΩ	V_{GS}=4.5 V, I_D=50 A
Gate-source leakage current	I_{GSS}			100	nA	V_{GS}=20 V
				-100		V_{GS}=-20 V
Drain-source leakage current	I_{DSS}			1	μA	V_{DS}=100 V, V_{GS}=0 V
Gate resistance	R_G		1.4		Ω	f=1 MHz, Open drain

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Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{iss}		2170		pF	V_{GS}=0 V, V_{DS}=25 V, f=100 kHz
Output capacitance	C_{oss}		1070		pF	
Reverse transfer capacitance	C_{rss}		62		pF	
Turn-on delay time	$t_{d(on)}$		9		ns	V_{GS}=10 V, V_{DS}=50 V, R_G=2 Ω, I_D=25 A
Rise time	t_r		3		ns	
Turn-off delay time	$t_{d(off)}$		20		ns	
Fall time	t_f		3.5		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		30		nC	V_{GS}=10 V, V_{DS}=50 V, I_D=25 A
Gate-source charge	Q_{gs}		5		nC	
Gate-drain charge	Q_{gd}		5		nC	
Gate plateau voltage	$V_{plateau}$		3		V	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V_{SD}			1.3	V	I_S=50 A, V_{GS}=0 V
Reverse recovery time	t_{rr}		37		ns	V_R=50 V, I_S=25 A, di/dt=100 A/μs
Reverse recovery charge	Q_{rr}		41		nC	
Peak reverse recovery current	I_{rrm}		2		A	

Note

1) Calculated continuous current based on maximum allowable junction temperature.
2) Repetitive rating; pulse width limited by max. junction temperature.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) V_{DD}=50 V,V_{GS}=10 V, L=0.3 mH, starting T_j=25 °C.

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Electrical Characteristics Diagrams

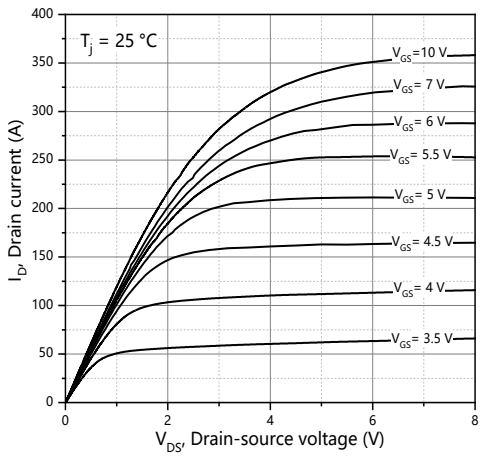

Figure 1. Typ. output characteristics

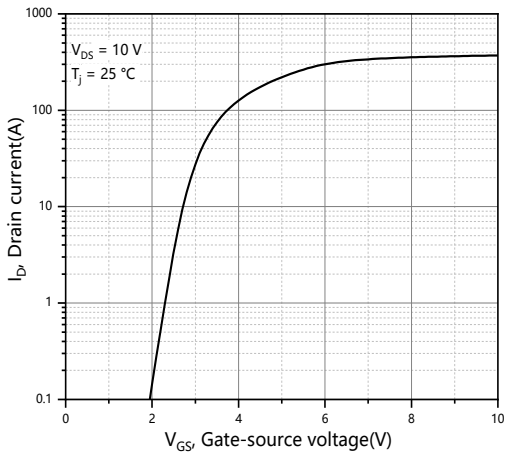

Figure 2. Typ. transfer characteristics

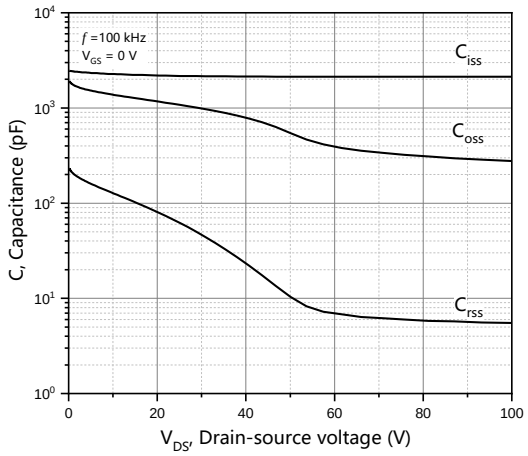

Figure 3. Typ. capacitances

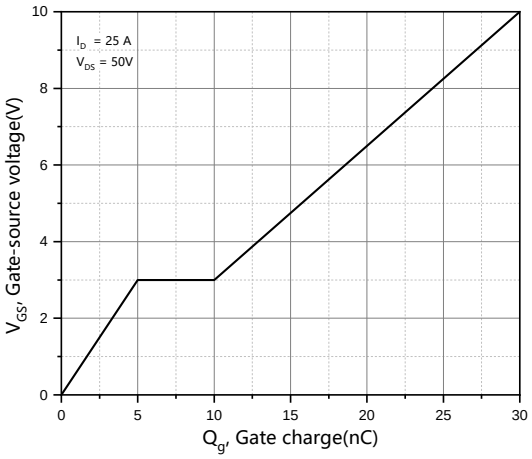

Figure 4. Typ. gate charge

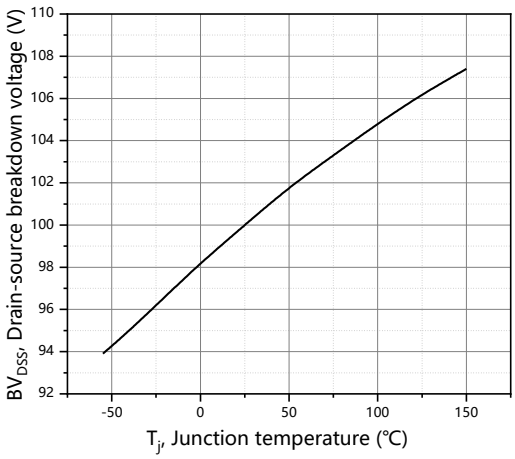

Figure 5. Drain-source breakdown voltage

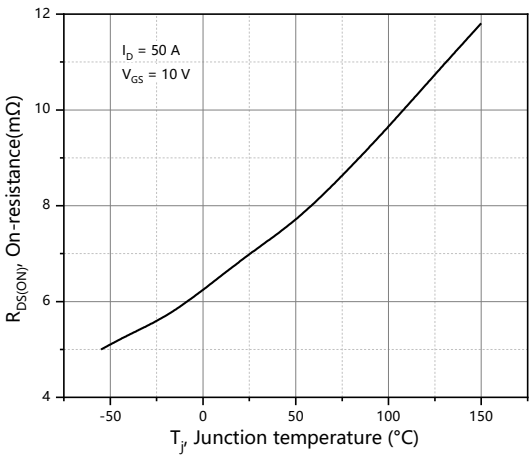

Figure 6. Drain-source on-state resistance

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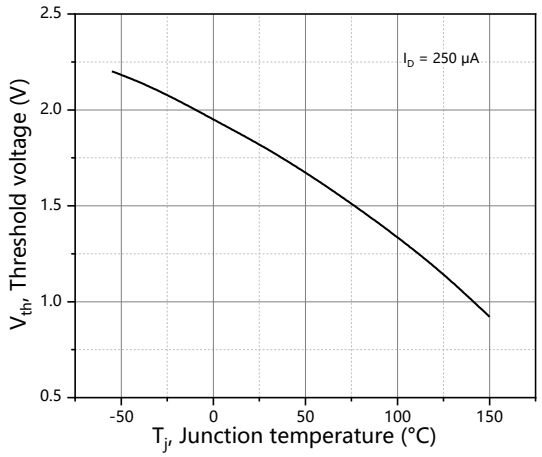


Figure 7. Threshold voltage

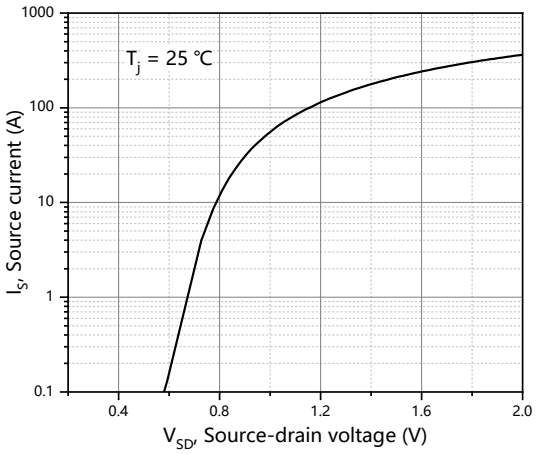


Figure 8. Forward characteristic of body diode

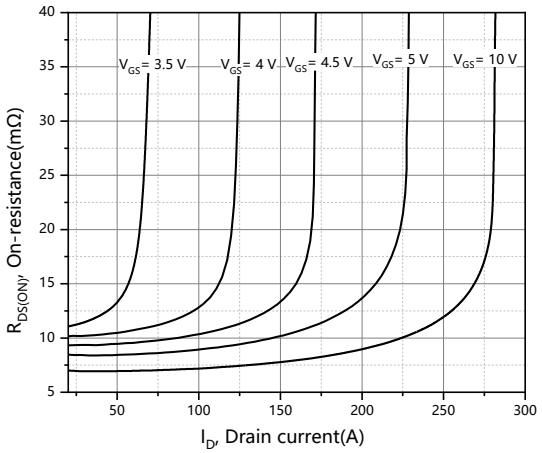


Figure 9. Drain-source on-state resistance

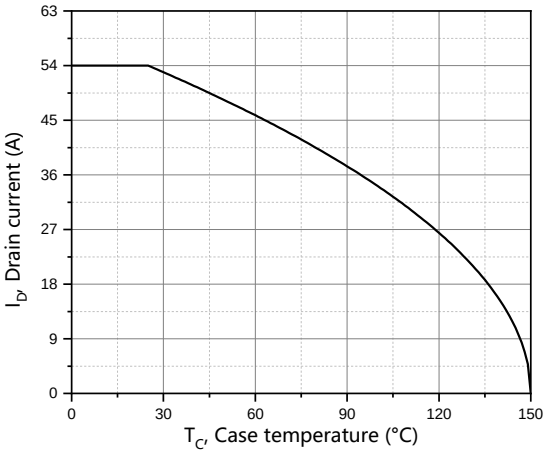


Figure 10. Drain current

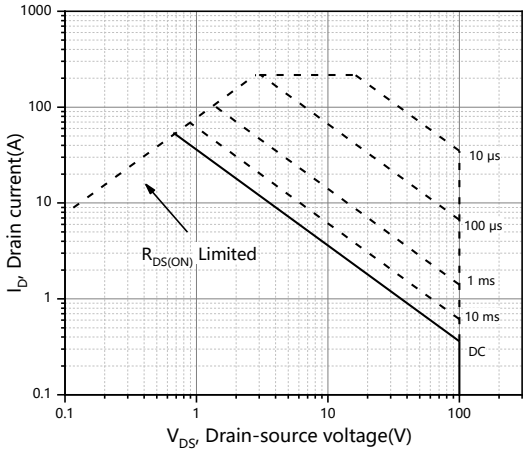


Figure 11. Safe operation area T_C=25℃

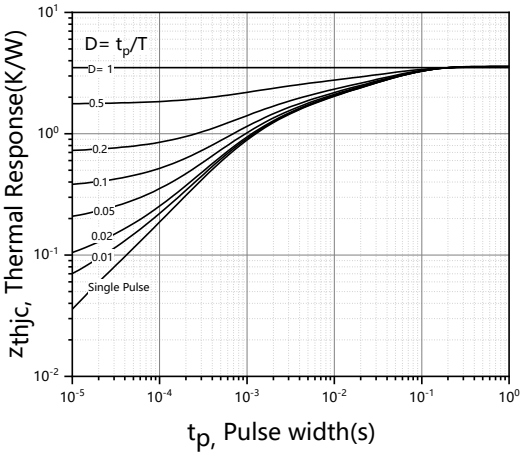


Figure 12. Max. transient thermal impedance

Test circuits and waveforms

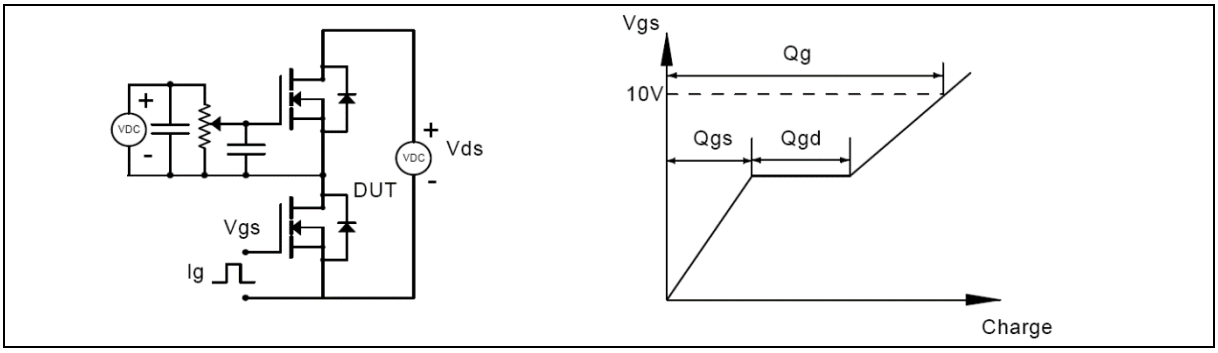


Figure 1. Gate charge test circuit & waveform

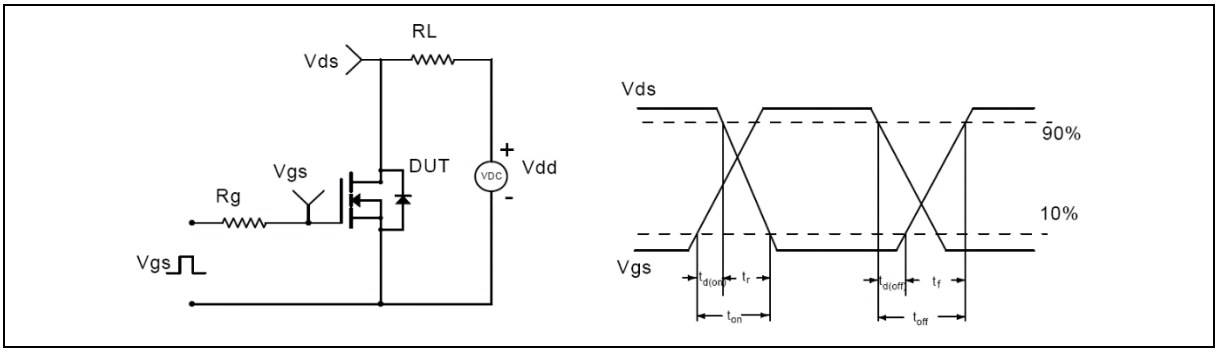


Figure 2. Switching time test circuit & waveforms

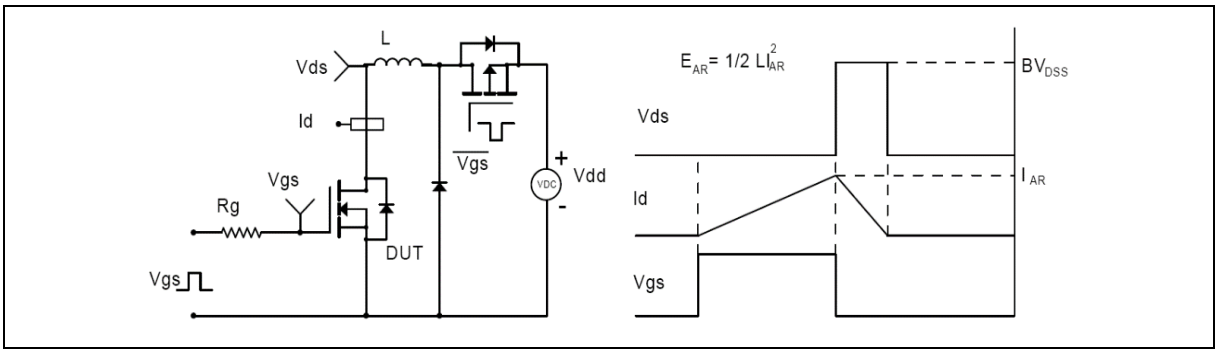


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

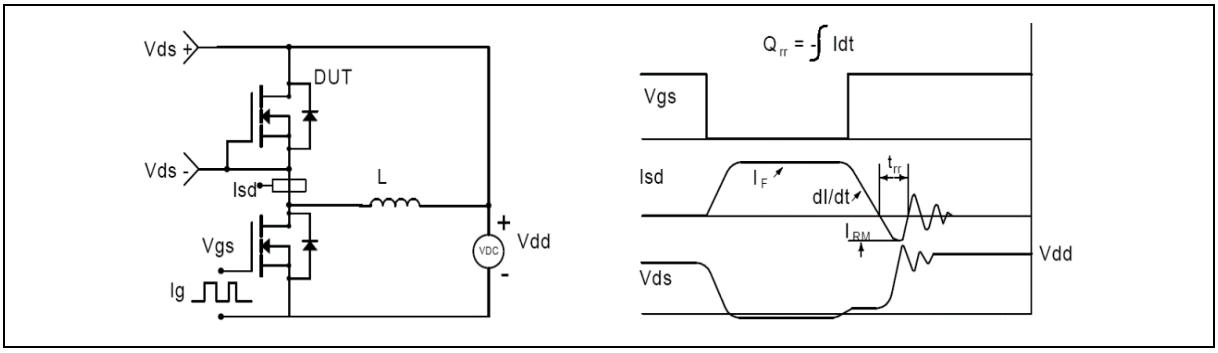
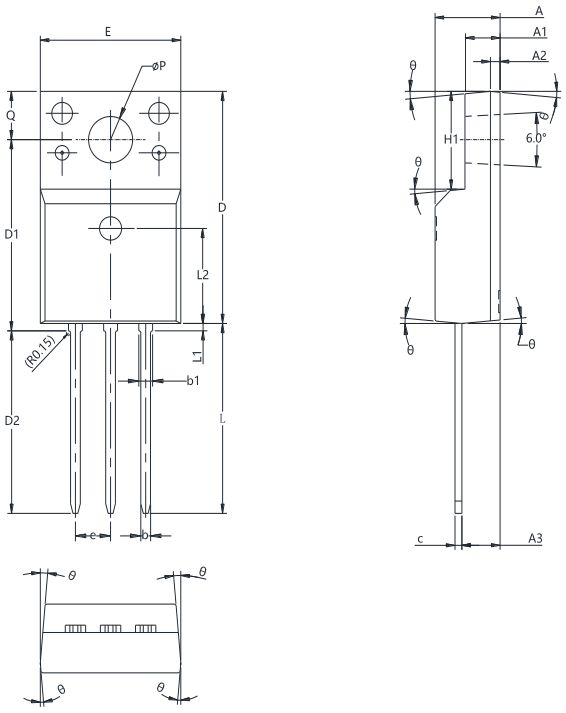


Figure 4. Diode reverse recovery test circuit & waveforms

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.5	4.7	4.83
A1	2.34	2.54	2.74
A2		0.70 REF	
A3	2.56	2.76	2.93
b	0.6	-	0.8
b1	0.9	-	1.1
c	0.45	0.5	0.6
D	15.67	15.87	16.07
D1	12.87	13.07	13.27
D2	12.28	12.48	12.68
E	9.96	10.16	10.36
e		2.54 BSC	
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	-	-	0.85
L2		6.50 REF	
ΦP	3.08	3.18	3.28
Q	3.20	-	3.40
θ1	1º	3º	5º

Version 1: TO-220F-NL -J package outline dimension

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Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO-220F-NL-J	50	20	1000	5	5000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFS10R075FF_NB	TO-220F-NL	yes	yes	yes

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Revision History

Version	Revision History	Date
V1.0	Initial release	2024-04-12

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