



General Description

OST75N65H4EVF uses advanced Oriental-Semi's patented Trident-Gate Bipolar Transistor (TGBT™) technology to provide extremely low $V_{CE(sat)}$, low gate charge, and excellent switching performance. This device is suitable for mid to high range switching frequency converters.

Features

- Advanced TGBT™ technology
- Excellent conduction and switching loss
- Excellent stability and uniformity
- Fast and soft antiparallel diode



Applications

- Induction converters
- Uninterruptible power supplies

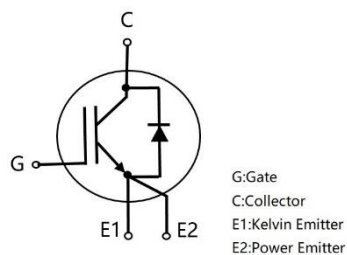
Key Performance Parameters

Parameter	Value	Unit
$V_{CES, min} @ 25^{\circ}C$	650	V
Maximum junction temperature	175	$^{\circ}C$
$I_C, pulse$	300	A
$V_{CE(sat), typ} @ V_{GE}=15V$	1.35	V
Q_g	192	nC

Marking Information

Product Name	Package	Marking
OST75N65H4EVF	TO247-4L	OST75N65H4EV

Package & Pin Information




Absolute Maximum Ratings at $T_{vj}=25^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Collector emitter voltage	V_{CES}	650	V
Gate emitter voltage	V_{GES}	± 20	V
Transient gate emitter voltage, $T_P \leq 10\mu\text{s}$, $D < 0.01$		± 30	V
Continuous collector current ¹⁾ , $T_C = 25^{\circ}\text{C}$	I_C	90	A
Continuous collector current ¹⁾ , $T_C = 100^{\circ}\text{C}$		75	A
Pulsed collector current ²⁾ , $T_C = 25^{\circ}\text{C}$	$I_{C, \text{pulse}}$	300	A
Diode forward current ¹⁾ , $T_C = 25^{\circ}\text{C}$	I_F	90	A
Diode forward current ¹⁾ , $T_C = 100^{\circ}\text{C}$		75	A
Diode pulsed current ²⁾ , $T_C = 25^{\circ}\text{C}$	$I_{F, \text{pulse}}$	300	A
Power dissipation ³⁾ , $T_C = 25^{\circ}\text{C}$	P_D	395	W
Power dissipation ³⁾ , $T_C = 100^{\circ}\text{C}$		198	W
Operation and storage temperature	T_{stg}, T_{vj}	-55 to 175	$^{\circ}\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
IGBT thermal resistance, junction-case	$R_{\theta JC}$	0.38	$^{\circ}\text{C/W}$
Diode thermal resistance, junction-case	$R_{\theta JC}$	0.45	$^{\circ}\text{C/W}$
Thermal resistance, junction-ambient	$R_{\theta JA}$	40	$^{\circ}\text{C/W}$


Electrical Characteristics at $T_{vj}=25^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Collector-emitter breakdown voltage	$V_{(BR)CES}$	650			V	$V_{GE}=0\text{ V}$, $I_C=0.5\text{ mA}$
Collector-emitter saturation voltage	$V_{CE(sat)}$		1.35	1.75	V	$V_{GE}=15\text{ V}$, $I_C=75\text{ A}$, $T_{vj}=25^{\circ}\text{C}$
			1.53		V	$V_{GE}=15\text{ V}$, $I_C=75\text{ A}$, $T_{vj}=125^{\circ}\text{C}$
			1.66		V	$V_{GE}=15\text{ V}$, $I_C=75\text{ A}$, $T_{vj}=175^{\circ}\text{C}$
Gate-emitter threshold voltage	$V_{GE(th)}$	3.5	4.2	5.0	V	$V_{CE}=V_{GE}$, $I_D=0.5\text{ mA}$
Diode forward voltage	V_F		1.23	1.5	V	$V_{GE}=0\text{ V}$, $I_F=75\text{ A}$, $T_{vj}=25^{\circ}\text{C}$
			1.15		V	$V_{GE}=0\text{ V}$, $I_F=75\text{ A}$, $T_{vj}=125^{\circ}\text{C}$
			1.1		V	$V_{GE}=0\text{ V}$, $I_F=75\text{ A}$, $T_{vj}=175^{\circ}\text{C}$
Gate-emitter leakage current	I_{GES}			100	nA	$V_{CE}=0\text{ V}$, $V_{GE}=20\text{ V}$
Zero gate voltage collector current	I_{CES}			10	μA	$V_{CE}=650\text{ V}$, $V_{GE}=0\text{ V}$



Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{ies}		7385		pF	$V_{GE}=0\text{ V}$, $V_{CE}=25\text{ V}$, $f=100\text{ kHz}$
Output capacitance	C_{oes}		245		pF	
Reverse transfer capacitance	C_{res}		55		pF	
Turn-on delay time	$t_{d(on)}$		45		ns	$V_{GE}=15\text{ V}$, $V_{CC}=400\text{ V}$, $R_G=10\ \Omega$, $I_C=75\text{ A}$
Rise time	t_r		41		ns	
Turn-off delay time	$t_{d(off)}$		276		ns	
Fall time	t_f		15		ns	
Turn-on energy	E_{on}		1.04		mJ	
Turn-off energy	E_{off}		1.27		mJ	
Turn-on delay time	$t_{d(on)}$		37		ns	$V_{GE}=15\text{ V}$, $V_{CC}=400\text{ V}$, $R_G=10\ \Omega$, $I_C=30\text{ A}$
Rise time	t_r		26		ns	
Turn-off delay time	$t_{d(off)}$		297		ns	
Fall time	t_f		15		ns	
Turn-on energy	E_{on}		0.37		mJ	
Turn-off energy	E_{off}		0.45		mJ	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		192		nC	$V_{GE}=15\text{ V}$, $V_{CC}=520\text{ V}$, $I_C=75\text{ A}$
Gate-emitter charge	Q_{ge}		58		nC	
Gate-collector charge	Q_{gc}		66		nC	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode reverse recovery time	t_{rr}		191		ns	$V_R=400\text{ V}$, $I_F=75\text{ A}$, $di_F/dt=500\text{ A}/\mu\text{s}$ $T_{vj} = 25^\circ\text{C}$
Diode reverse recovery charge	Q_{rr}		3.86		μC	
Diode peak reverse recovery current	I_{rrm}		30		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.

Electrical Characteristics Diagrams

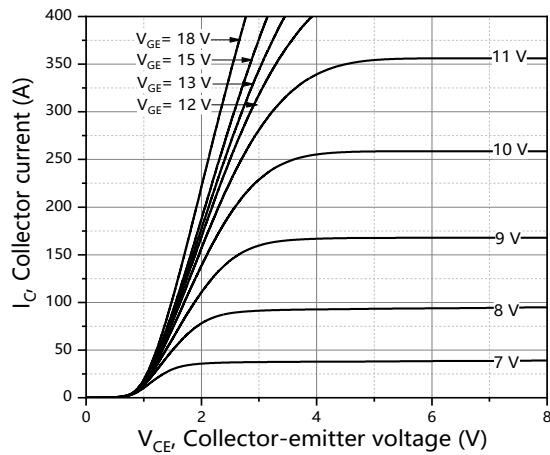


Figure 1. Typical output characteristics
($T_{vj}=25^{\circ}\text{C}$)

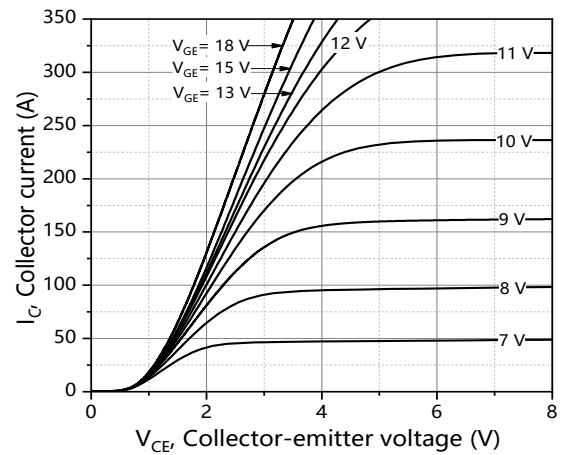


Figure 2. Typical output characteristics
($T_{vj}=150^{\circ}\text{C}$)

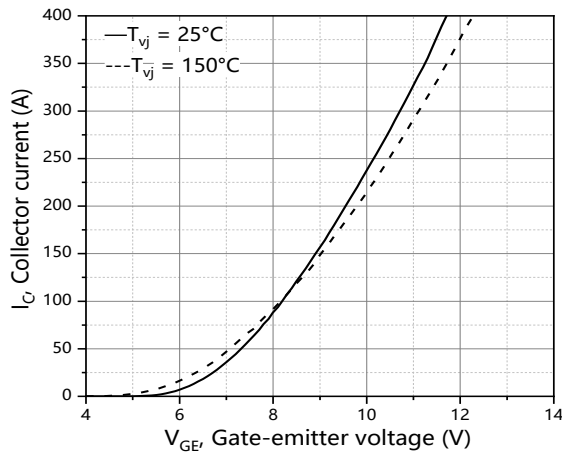


Figure 3. Typical transfer characteristics
($V_{CE}=20\text{V}$)

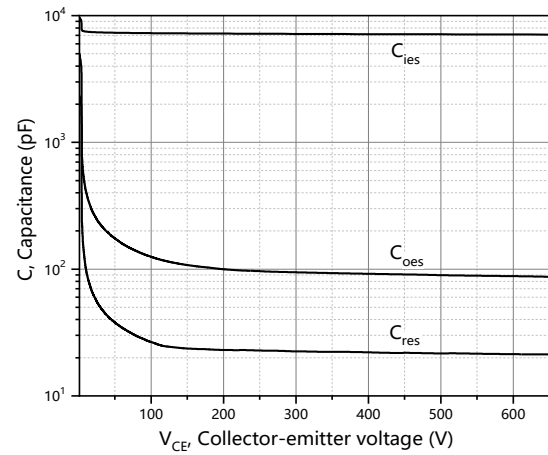


Figure 4. Typical capacitance
($V_{GE}=0\text{V}$, $f=100\text{ kHz}$)

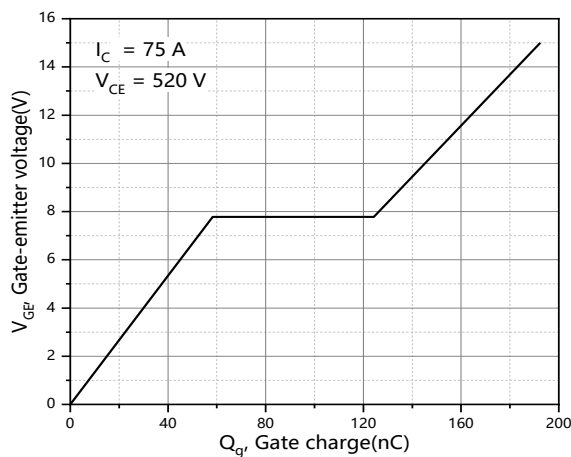


Figure 5. Typical gate charge

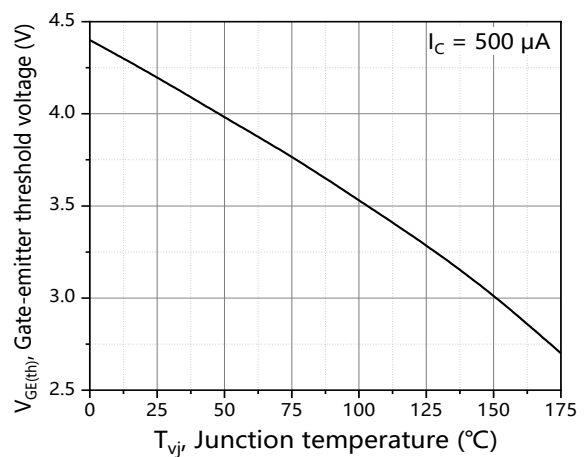
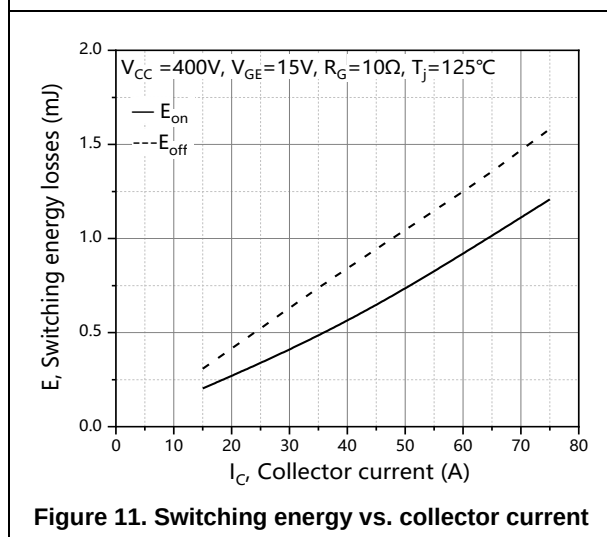
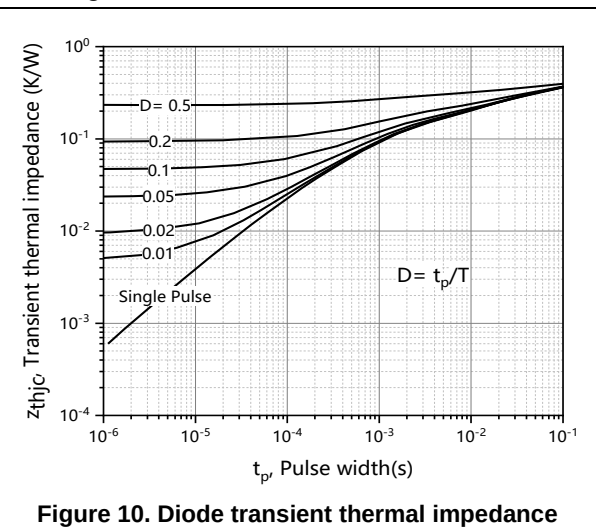
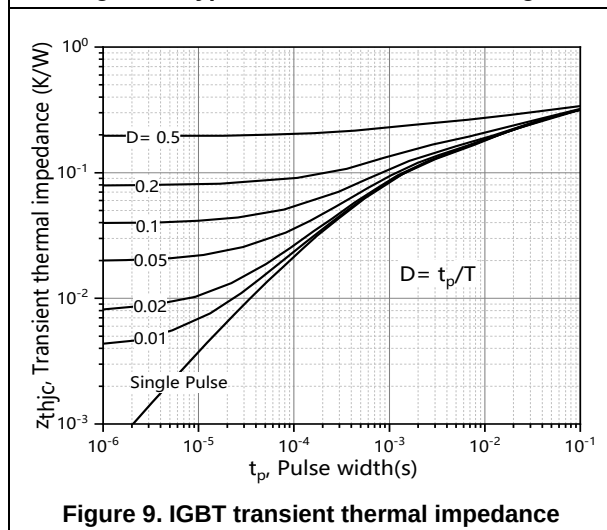
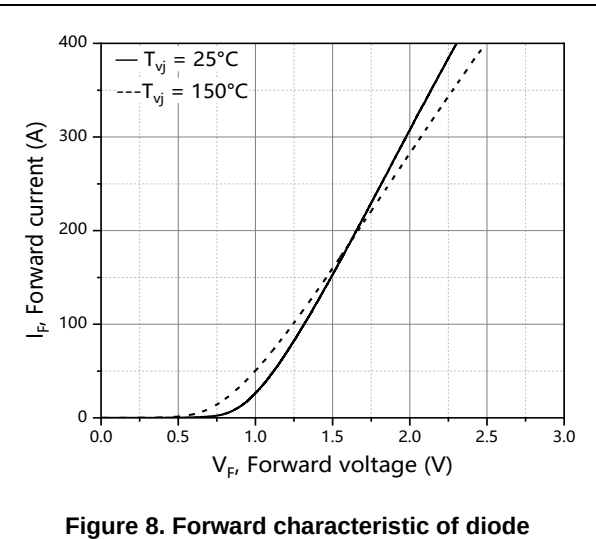
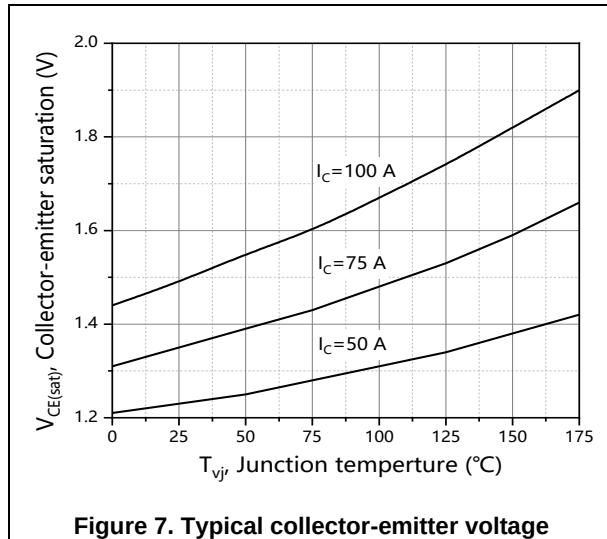
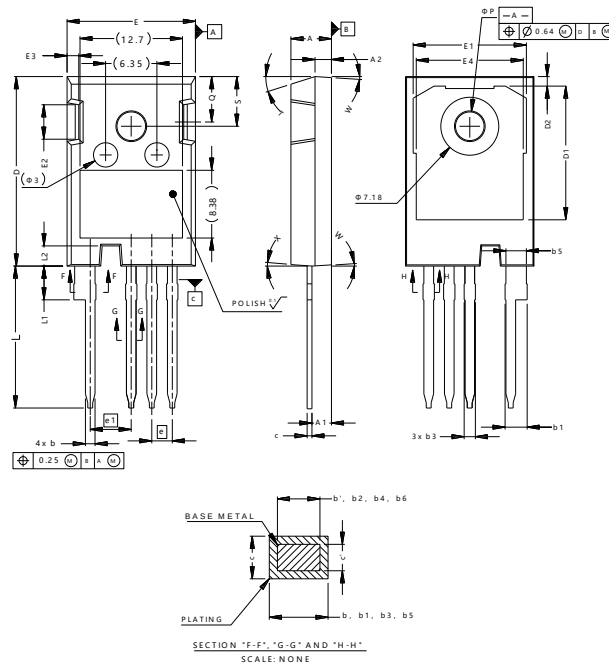


Figure 6. Gate-emitter threshold voltage



Package Information



Symbol	mm	
	Min	Max
A	4.83	5.21
A1	2.29	2.54
A2	1.91	2.16
b'	1.07	1.28
b	1.07	1.33
b1	2.39	2.94
b2	2.39	2.84
b3	1.07	1.60
b4	1.07	1.50
b5	2.39	2.69
b6	2.39	2.64
c'	0.55	0.65
c	0.55	0.68
D	23.30	23.60
D1	16.25	17.65
D2	0.95	1.25
E	15.75	16.13
E1	13.10	14.15
E2	3.68	5.10
E3	1.00	1.90
E4	12.38	13.43
e	2.54 BSC	
e1	5.08 BSC	
N	4	
L	17.31	17.82
L1	3.97	4.37
L2	2.35	2.65
ΦP	3.51	3.65
Q	5.49	6.00
S	6.04	6.30
T	17.5° REF	
W	3.5° REF	
X	4° REF	

Version, TO247-4L-S package outline dimension

Ordering Information

Package Type	Units/ Tubes	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO247-4L-S	30	15	450	4	1800

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OST75N65H4EVF	TO247-4L	yes	yes	yes

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