



General Description

OST50N65HTMF uses advanced Oriental-Semi's patented Trident-Gate Bipolar Transistor (TGBT™) technology to provide extremely low $V_{CE(sat)}$, low gate charge, and excellent switching performance. This device is suitable for mid to high range switching frequency converters.

Features

- Advanced TGBT™ technology
- Excellent conduction and switching loss
- Excellent stability and uniformity
- Fast and soft antiparallel diode



Applications

- Induction converters
- On-board charger

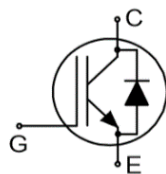
Key Performance Parameters

Parameter	Value	Unit
$V_{CES, min} @ 25\text{ }^{\circ}\text{C}$	650	V
Maximum junction temperature	175	$^{\circ}\text{C}$
$I_C, pulse$	200	A
$V_{CE(sat), typ} @ V_{GE}=15\text{ V}$	1.36	V
Q_g	127	nC

Marking Information

Product Name	Package	Marking
OST50N65HTMF	TO247	OST50N65HTM

Package & Pin Information




Absolute Maximum Ratings at $T_{vj}=25\text{ }^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Collector emitter voltage	V_{CES}	650	V
Gate emitter voltage	V_{GES}	± 20	V
Transient gate emitter voltage, $T_P \leq 10\text{ }\mu\text{s}$, $D < 0.01$		± 30	V
Continuous collector current ¹⁾ , $T_C = 25\text{ }^{\circ}\text{C}$	I_C	80	A
Continuous collector current ¹⁾ , $T_C = 100\text{ }^{\circ}\text{C}$		50	A
Pulsed collector current ²⁾ , $T_C = 25\text{ }^{\circ}\text{C}$	$I_{C, \text{ pulse}}$	200	A
Diode forward current ¹⁾ , $T_C = 25\text{ }^{\circ}\text{C}$	I_F	80	A
Diode forward current ¹⁾ , $T_C = 100\text{ }^{\circ}\text{C}$		50	A
Diode pulsed current ²⁾ , $T_C = 25\text{ }^{\circ}\text{C}$	$I_{F, \text{ pulse}}$	200	A
Power dissipation ³⁾ , $T_C = 25\text{ }^{\circ}\text{C}$	P_D	231	W
Power dissipation ³⁾ , $T_C = 100\text{ }^{\circ}\text{C}$		116	W
Operation and storage temperature	T_{stg}, T_{vj}	-55 to 175	$^{\circ}\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
IGBT thermal resistance, junction-case	$R_{\theta JC}$	0.65	$^{\circ}\text{C/W}$
Diode thermal resistance, junction-case	$R_{\theta JC}$	0.38	$^{\circ}\text{C/W}$
Thermal resistance, junction-ambient	$R_{\theta JA}$	40	$^{\circ}\text{C/W}$

Electrical Characteristics at $T_{vj}=25\text{ }^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Collector-emitter breakdown voltage	$V_{(BR)CES}$	650			V	$V_{GE}=0\text{ V}$, $I_C=0.5\text{ mA}$
Collector-emitter saturation voltage	$V_{CE(sat)}$		1.36	1.6	V	$V_{GE}=15\text{ V}$, $I_C=50\text{ A}$, $T_{vj}=25\text{ }^{\circ}\text{C}$
			1.46		V	$V_{GE}=15\text{ V}$, $I_C=50\text{ A}$, $T_{vj}=125\text{ }^{\circ}\text{C}$
			1.53		V	$V_{GE}=15\text{ V}$, $I_C=50\text{ A}$, $T_{vj}=175\text{ }^{\circ}\text{C}$
Gate-emitter threshold voltage	$V_{GE(th)}$	3.5	4.0	4.5	V	$V_{CE}=V_{GE}$, $I_C=0.5\text{ mA}$
Diode forward voltage	V_F		1.50	1.9	V	$V_{GE}=0\text{ V}$, $I_F=50\text{ A}$, $T_{vj}=25\text{ }^{\circ}\text{C}$
			1.33		V	$V_{GE}=0\text{ V}$, $I_F=50\text{ A}$, $T_{vj}=125\text{ }^{\circ}\text{C}$
			1.27		V	$V_{GE}=0\text{ V}$, $I_F=50\text{ A}$, $T_{vj}=175\text{ }^{\circ}\text{C}$
Gate-emitter leakage current	I_{GES}			100	nA	$V_{CE}=0\text{ V}$, $V_{GE}=20\text{ V}$
Zero gate voltage collector current	I_{CES}			10	μA	$V_{CE}=650\text{ V}$, $V_{GE}=0\text{ V}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C_{ies}		5554		pF	$V_{GE}=0\text{ V}$, $V_{CE}=25\text{ V}$, $f=100\text{ kHz}$
Output capacitance	C_{oes}		149		pF	
Reverse transfer capacitance	C_{res}		18		pF	
Turn-on delay time	$t_{d(on)}$		119		ns	$V_{GE}=15\text{ V}$, $V_{CC}=400\text{ V}$, $R_G^{(4)}=48\ \Omega$, $I_C=50\text{ A}$
Rise time	t_r		70		ns	
Turn-off delay time	$t_{d(off)}$		521		ns	
Fall time	t_f		61		ns	
Turn-on energy	E_{on}		1.88		mJ	
Turn-off energy	E_{off}		1.33		mJ	
Turn-on delay time	$t_{d(on)}$		112		ns	$V_{GE}=15\text{ V}$, $V_{CC}=400\text{ V}$, $R_G^{(4)}=48\ \Omega$, $I_C=25\text{ A}$
Rise time	t_r		42		ns	
Turn-off delay time	$t_{d(off)}$		577		ns	
Fall time	t_f		51		ns	
Turn-on energy	E_{on}		0.79		mJ	
Turn-off energy	E_{off}		0.56		mJ	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q_g		127		nC	$V_{GE}=15\text{ V}$, $V_{CC}=520\text{ V}$, $I_C=50\text{ A}$
Gate-emitter charge	Q_{ge}		38		nC	
Gate-collector charge	Q_{gc}		35		nC	

Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode reverse recovery time	t_{rr}		99		ns	$V_R=400\text{ V}$, $I_F=50\text{ A}$, $di_F/dt=500\text{ A}/\mu\text{s}$, $T_{vj}=25\text{ }^\circ\text{C}$
Diode reverse recovery charge	Q_{rr}		0.90		μC	
Diode peak reverse recovery current	I_{rrm}		14		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) Recommended drive resistance greater than or equal to $66\ \Omega$.

Electrical Characteristics Diagrams

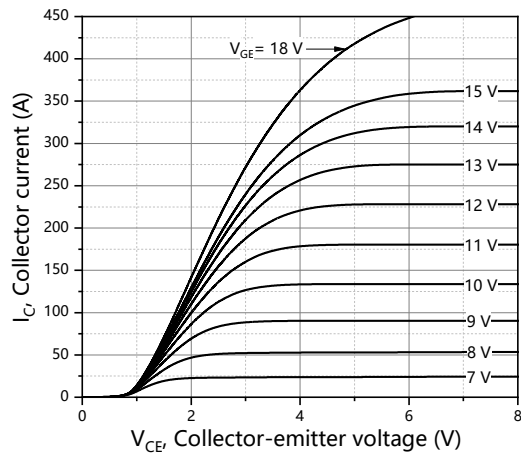


Figure 1. Typical output characteristics
($T_{vj}=25\text{ }^{\circ}\text{C}$)

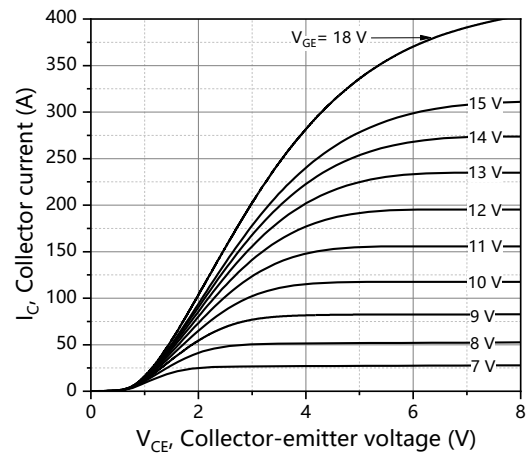


Figure 2. Typical output characteristics
($T_{vj}=150\text{ }^{\circ}\text{C}$)

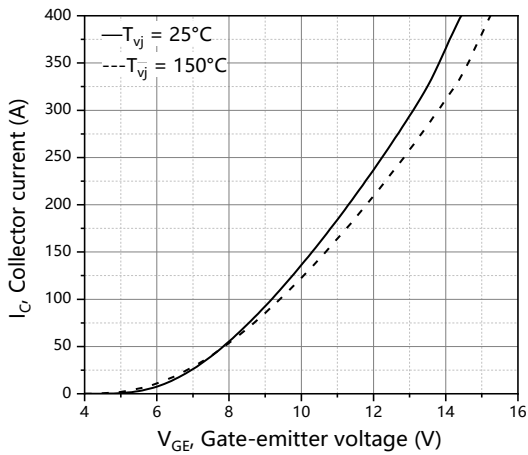


Figure 3. Typical transfer characteristics
($V_{CE}=20\text{ V}$)

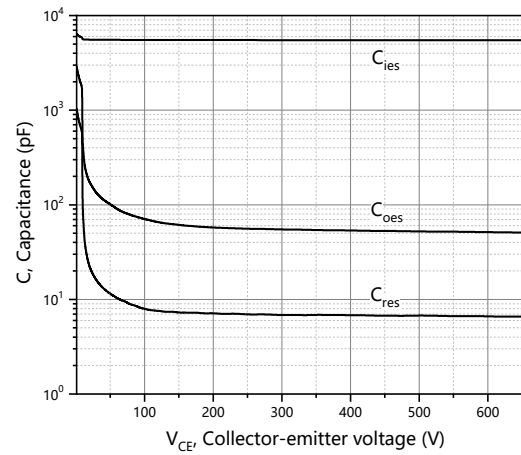


Figure 4. Typical capacitance
($V_{GE}=0\text{ V}$, $f=100\text{ kHz}$)

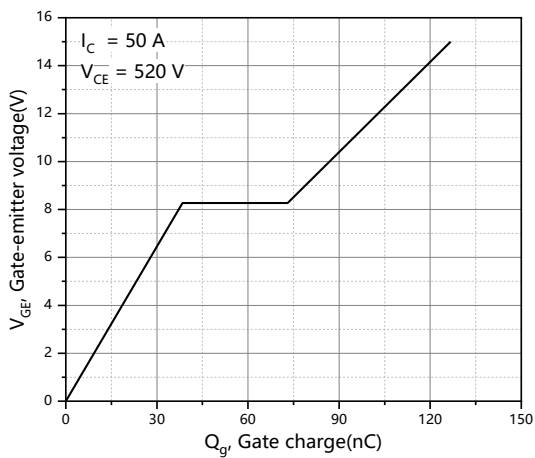


Figure 5. Typical gate charge

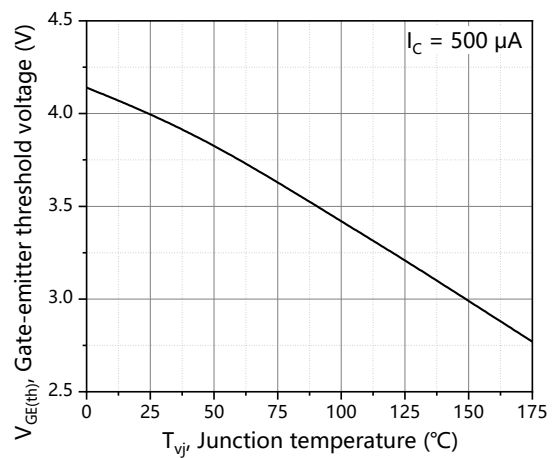


Figure 6. Gate-emitter threshold voltage

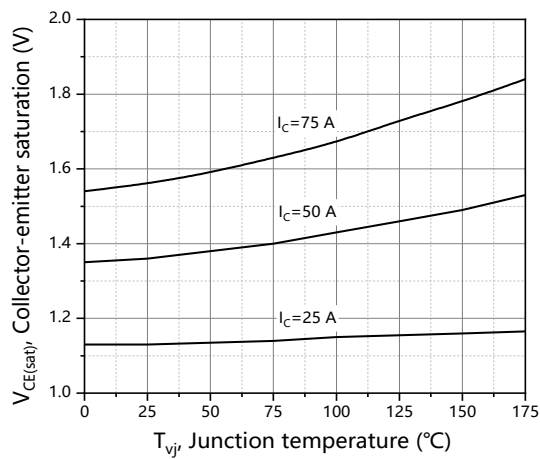


Figure 7. Typical collector-emitter voltage

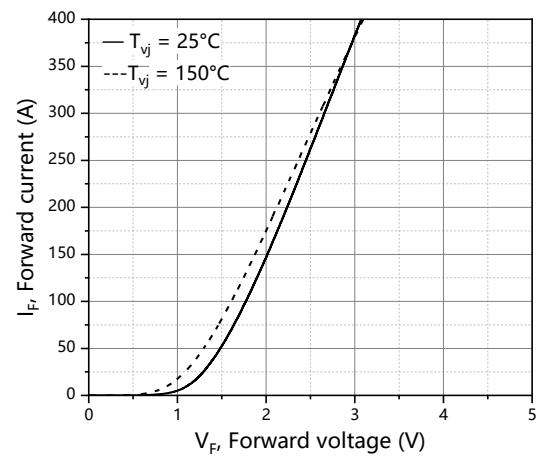


Figure 8. Forward characteristic of diode

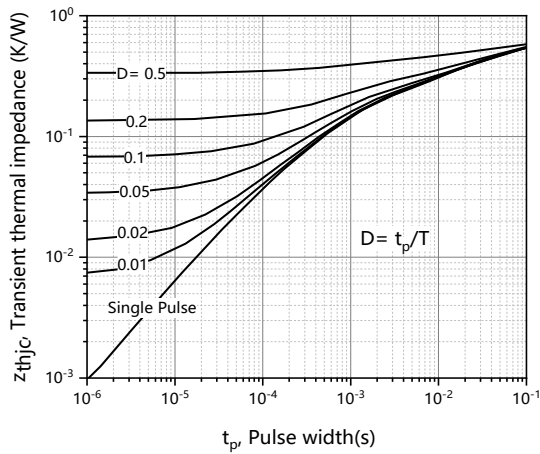


Figure 9. IGBT transient thermal impedance

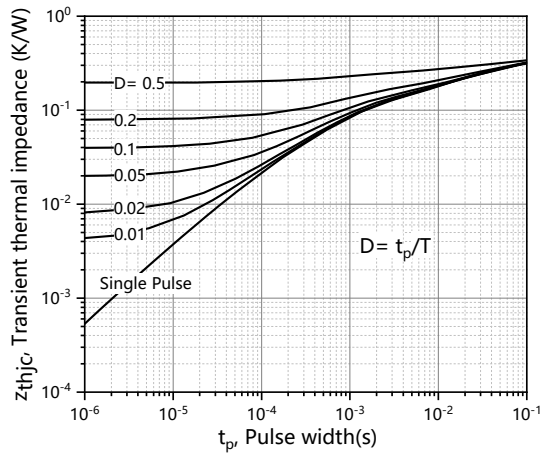
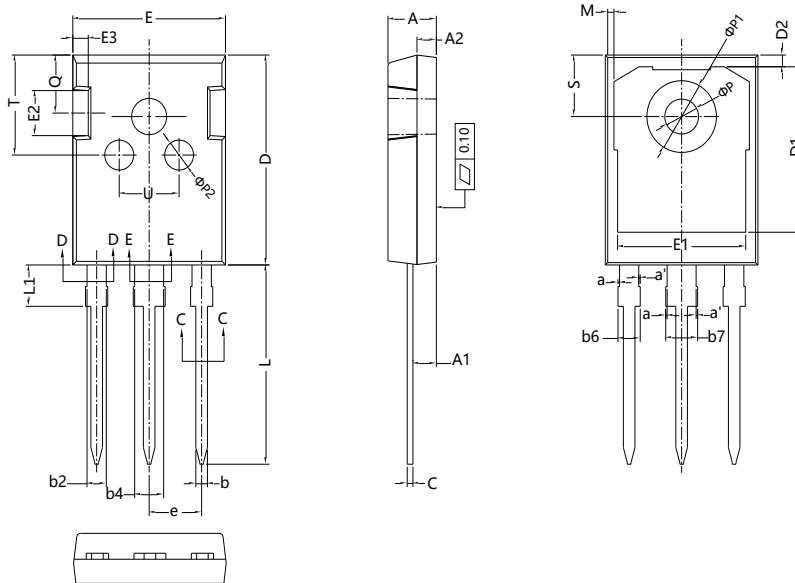


Figure 10. Diode transient thermal impedance

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
a	0.00	-	0.15
a'	0.00	-	0.15
b	1.16	-	1.26
b2	1.96	-	2.06
b4	2.96	-	3.06
b6	-	-	2.25
b7	-	-	3.25
c	0.59	-	0.66
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.40	4.50	4.60
E3	1.50	1.60	1.70
e	5.436 BSC		
L	19.80	19.92	20.10
L1	-	-	4.30
M	0.35	-	0.95
P	3.40	3.50	3.60
P1	7.00	-	7.40
P2	2.40	2.5	2.6
Q	5.60	-	6.0
S	6.05	6.15	6.25
T	9.8	-	10.20
U	6.00	-	6.40

Version : TO247-J package outline dimension

Ordering Information

Package Type	Units/ Tube	Tubes/ Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO247-J	30	20	600	4	2400

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OST50N65HTMF	TO247	yes	yes	yes

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Revision History

Version	Revision History	Data
V1.0	Initial release	2025-08-08