

## General Description

The GreenMOS<sup>®</sup> Z high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS<sup>®</sup> Z Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

## Features

- Low  $R_{DS(ON)}$  & FOM
- Extremely low switching loss
- Excellent stability and uniformity

GreenMOS<sup>®</sup>



## Applications

- PC power
- LED lighting
- Telecom power
- Server power
- EV Charger
- Solar/UPS

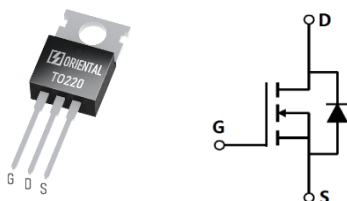
## Key Performance Parameters

| Parameter                         | Value | Unit       |
|-----------------------------------|-------|------------|
| $V_{DS}$                          | 650   | V          |
| $I_D$ , pulse                     | 45    | A          |
| $R_{DS(ON)}$ , max @ $V_{GS}=10V$ | 340   | m $\Omega$ |
| $Q_g$                             | 15.6  | nC         |
| PD                                | 104   | W          |

## Marking Information

| Product Name | Package | Marking     |
|--------------|---------|-------------|
| OSG65R340PZF | TO220   | OSG65R340PZ |

## Package & Pin Information



**Absolute Maximum Ratings** at  $T_j=25^{\circ}\text{C}$  unless otherwise noted

| Parameter                                                                 | Symbol                | Value      | Unit               |
|---------------------------------------------------------------------------|-----------------------|------------|--------------------|
| Drain-source voltage                                                      | $V_{DS}$              | 650        | V                  |
| Gate-source voltage                                                       | $V_{GS}$              | $\pm 30$   | V                  |
| Continuous drain current <sup>1)</sup> , $T_C=25^{\circ}\text{C}$         | $I_D$                 | 15         | A                  |
| Continuous drain current <sup>1)</sup> , $T_C=100^{\circ}\text{C}$        |                       | 9.5        |                    |
| Pulsed drain current <sup>2)</sup> , $T_C=25^{\circ}\text{C}$             | $I_{D, \text{pulse}}$ | 45         | A                  |
| Continuous diode forward current <sup>1)</sup> , $T_C=25^{\circ}\text{C}$ | $I_S$                 | 15         | A                  |
| Diode pulsed current <sup>2)</sup> , $T_C=25^{\circ}\text{C}$             | $I_{S, \text{pulse}}$ | 45         | A                  |
| Power dissipation <sup>3)</sup> , $T_C=25^{\circ}\text{C}$                | $P_D$                 | 104        | W                  |
| Single pulsed avalanche energy <sup>5)</sup>                              | $E_{AS}$              | 230        | mJ                 |
| MOSFET dv/dt ruggedness, $V_{DS}=0 \dots 480 \text{ V}$                   | dv/dt                 | 50         | V/ns               |
| Reverse diode dv/dt, $V_{DS}=0 \dots 480 \text{ V}$ , $I_{SD} \leq I_D$   | dv/dt                 | 50         | V/ns               |
| Operation and storage temperature                                         | $T_{stg}, T_j$        | -55 to 150 | $^{\circ}\text{C}$ |

**Thermal Characteristics**

| Parameter                                          | Symbol          | Value | Unit                 |
|----------------------------------------------------|-----------------|-------|----------------------|
| Thermal resistance, junction-case                  | $R_{\theta JC}$ | 1.2   | $^{\circ}\text{C/W}$ |
| Thermal resistance, junction-ambient <sup>4)</sup> | $R_{\theta JA}$ | 62    | $^{\circ}\text{C/W}$ |

**Electrical Characteristics** at  $T_j=25^{\circ}\text{C}$  unless otherwise specified

| Parameter                        | Symbol       | Min. | Typ. | Max. | Unit          | Test condition                                                          |
|----------------------------------|--------------|------|------|------|---------------|-------------------------------------------------------------------------|
| Drain-source breakdown voltage   | $BV_{DSS}$   | 650  |      |      | V             | $V_{GS}=0 \text{ V}$ , $I_D=250 \mu\text{A}$                            |
| Gate threshold voltage           | $V_{GS(th)}$ | 3.0  |      | 4.5  | V             | $V_{DS}=V_{GS}$ , $I_D=250 \mu\text{A}$                                 |
| Drain-source on-state resistance | $R_{DS(ON)}$ |      | 0.26 | 0.34 | $\Omega$      | $V_{GS}=10 \text{ V}$ , $I_D=7.5 \text{ A}$                             |
|                                  |              |      | 0.69 |      |               | $V_{GS}=10 \text{ V}$ , $I_D=7.5 \text{ A}$ , $T_j=150^{\circ}\text{C}$ |
| Gate-source leakage current      | $I_{GSS}$    |      |      | 100  | nA            | $V_{GS}=30 \text{ V}$                                                   |
|                                  |              |      |      | -100 |               | $V_{GS}=-30 \text{ V}$                                                  |
| Drain-source leakage current     | $I_{DSS}$    |      |      | 10   | $\mu\text{A}$ | $V_{DS}=650 \text{ V}$ , $V_{GS}=0 \text{ V}$                           |

### Dynamic Characteristics

| Parameter                    | Symbol       | Min. | Typ. | Max. | Unit | Test condition                                                                             |
|------------------------------|--------------|------|------|------|------|--------------------------------------------------------------------------------------------|
| Input capacitance            | $C_{iss}$    |      | 1066 |      | pF   | $V_{GS}=0\text{ V}$ ,<br>$V_{DS}=50\text{ V}$ ,<br>$f=100\text{ kHz}$                      |
| Output capacitance           | $C_{oss}$    |      | 73   |      | pF   |                                                                                            |
| Reverse transfer capacitance | $C_{rss}$    |      | 2.2  |      | pF   |                                                                                            |
| Turn-on delay time           | $t_{d(on)}$  |      | 25.7 |      | ns   | $V_{GS}=10\text{ V}$ ,<br>$V_{DS}=400\text{ V}$ ,<br>$R_G=2\ \Omega$ ,<br>$I_D=8\text{ A}$ |
| Rise time                    | $t_r$        |      | 6.5  |      | ns   |                                                                                            |
| Turn-off delay time          | $t_{d(off)}$ |      | 32.6 |      | ns   |                                                                                            |
| Fall time                    | $t_f$        |      | 3.9  |      | ns   |                                                                                            |

### Gate Charge Characteristics

| Parameter            | Symbol        | Min. | Typ. | Max. | Unit | Test condition                                                        |
|----------------------|---------------|------|------|------|------|-----------------------------------------------------------------------|
| Total gate charge    | $Q_g$         |      | 15.6 |      | nC   | $V_{GS}=10\text{ V}$ ,<br>$V_{DS}=400\text{ V}$ ,<br>$I_D=8\text{ A}$ |
| Gate-source charge   | $Q_{gs}$      |      | 5.6  |      | nC   |                                                                       |
| Gate-drain charge    | $Q_{gd}$      |      | 5.2  |      | nC   |                                                                       |
| Gate plateau voltage | $V_{plateau}$ |      | 6.9  |      | V    |                                                                       |

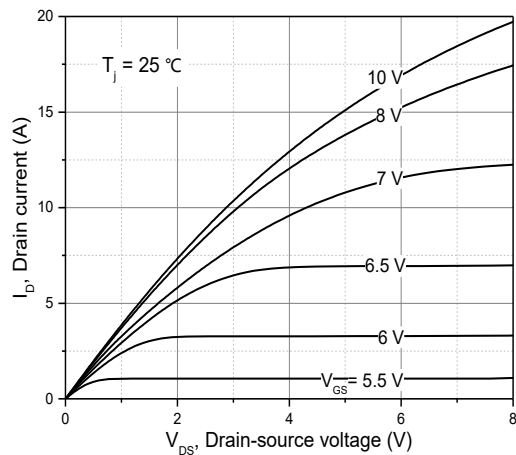
### Body Diode Characteristics

| Parameter                     | Symbol    | Min. | Typ. | Max. | Unit          | Test condition                                                                 |
|-------------------------------|-----------|------|------|------|---------------|--------------------------------------------------------------------------------|
| Diode forward voltage         | $V_{SD}$  |      |      | 1.3  | V             | $I_S=15\text{ A}$ ,<br>$V_{GS}=0\text{ V}$                                     |
| Reverse recovery time         | $t_{rr}$  |      | 103  |      | ns            | $V_R=400\text{ V}$ ,<br>$I_S=8\text{ A}$ ,<br>$di/dt=100\text{ A}/\mu\text{s}$ |
| Reverse recovery charge       | $Q_{rr}$  |      | 0.5  |      | $\mu\text{C}$ |                                                                                |
| Peak reverse recovery current | $I_{rrm}$ |      | 9.5  |      | A             |                                                                                |

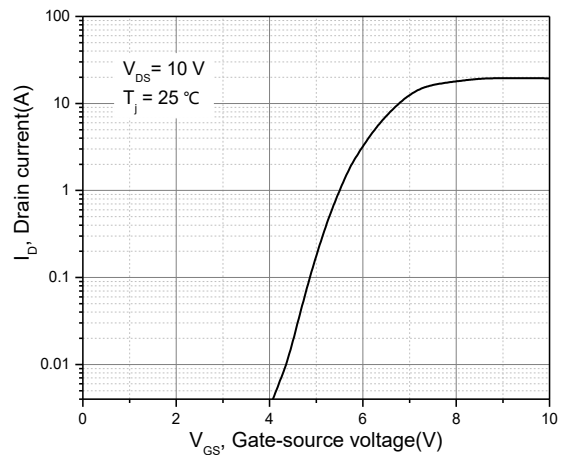
### Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3)  $P_d$  is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_a=25\text{ }^\circ\text{C}$ .
- 5)  $V_{DD}=100\text{ V}$ ,  $V_{GS}=10\text{ V}$ ,  $L=80\text{ mH}$ , starting  $T_j=25\text{ }^\circ\text{C}$ .

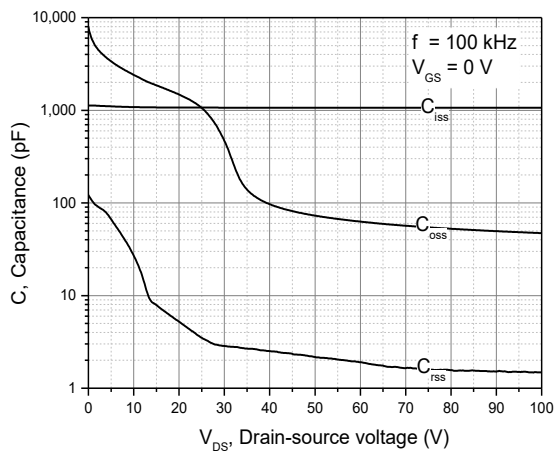
## Electrical Characteristics Diagrams



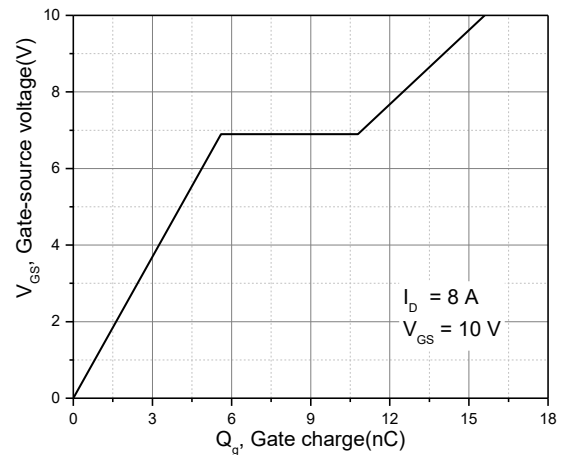
**Figure 1. Typ. output characteristics**



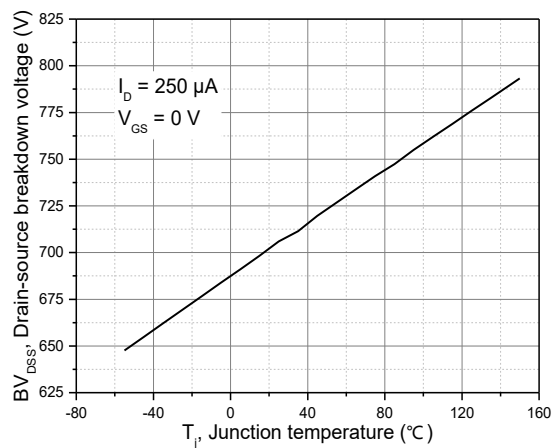
**Figure 2. Typ. transfer characteristics**



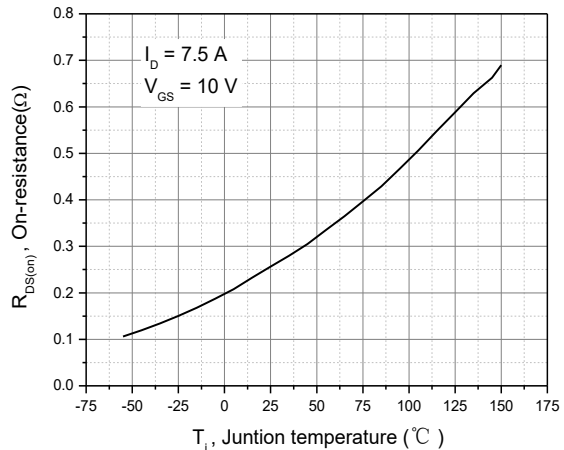
**Figure 3. Typ. capacitances**



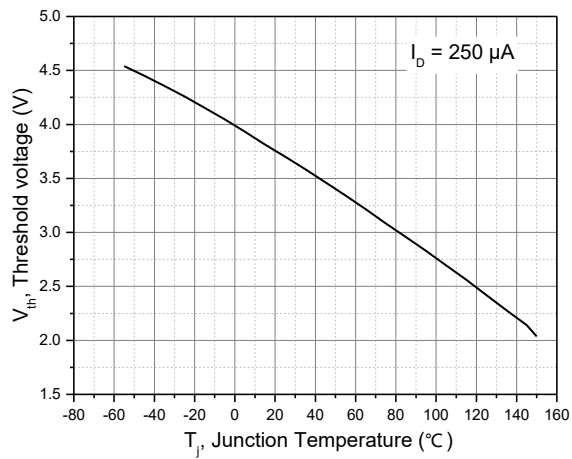
**Figure 4. Typ. gate charge**



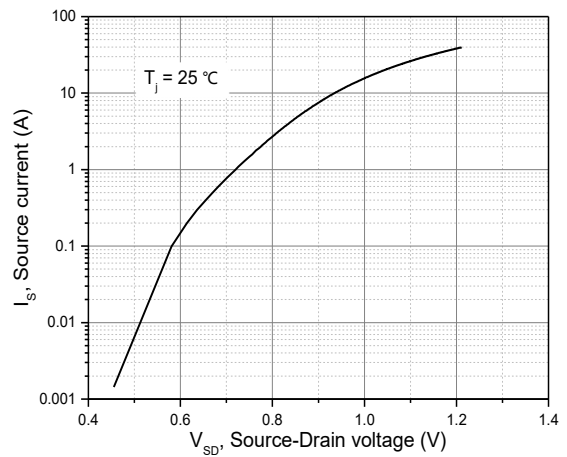
**Figure 5. Drain-source breakdown voltage**



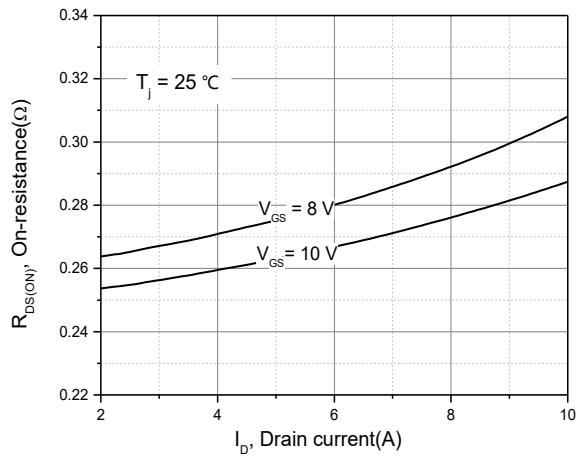
**Figure 6. Drain-source on-state resistance**



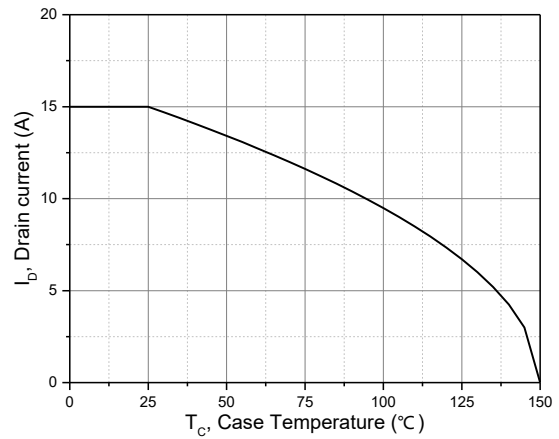
**Figure 7. Threshold voltage**



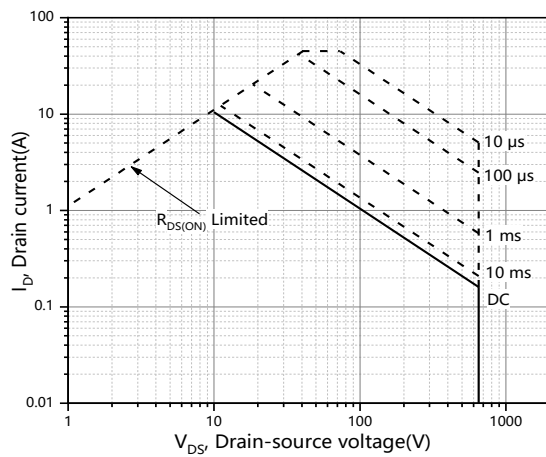
**Figure 8. Forward characteristic of body diode**



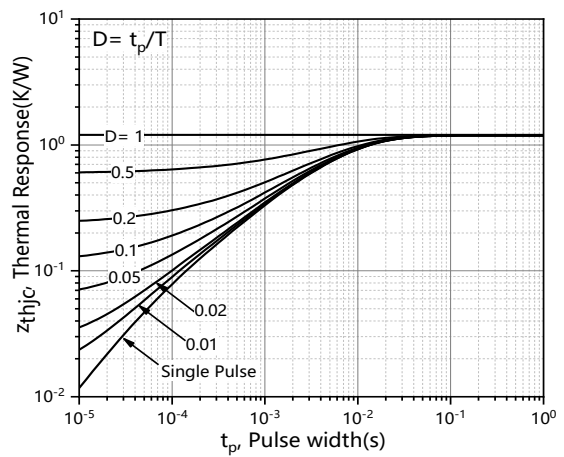
**Figure 9. Drain-source on-state resistance**



**Figure 10. Drain current**

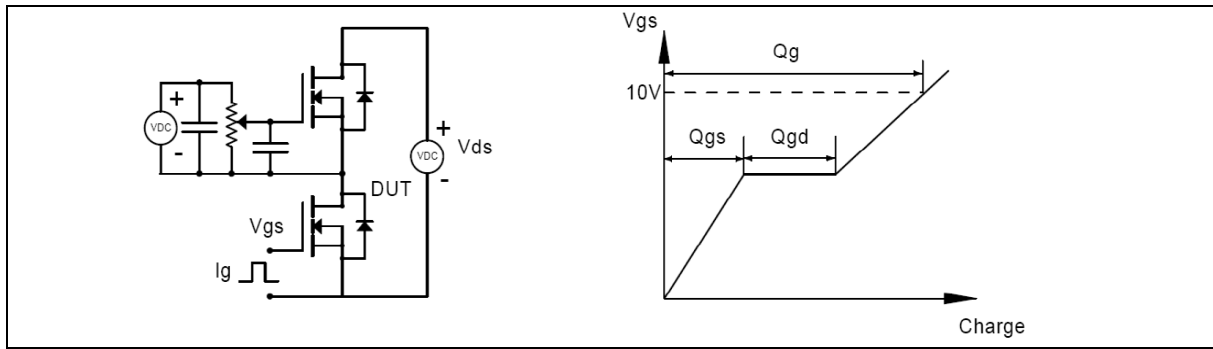


**Figure 11. Safe operation area T<sub>c</sub>=25 °C**

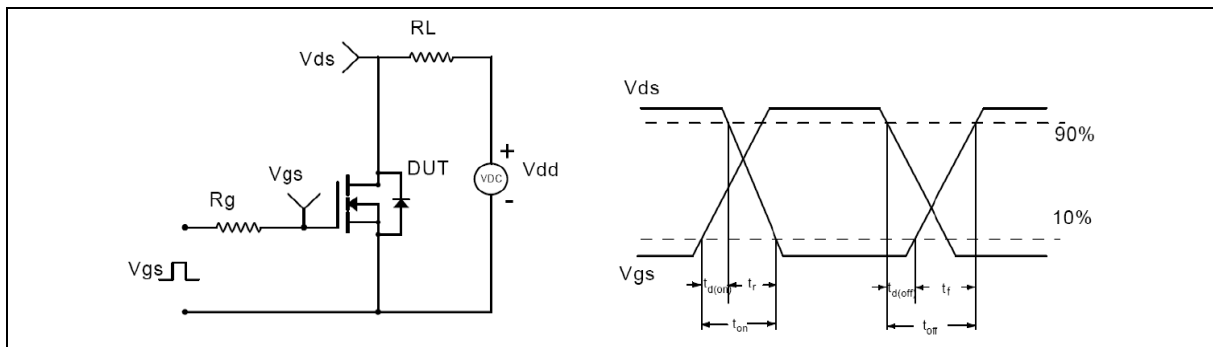


**Figure 12. Max. transient thermal impedance**

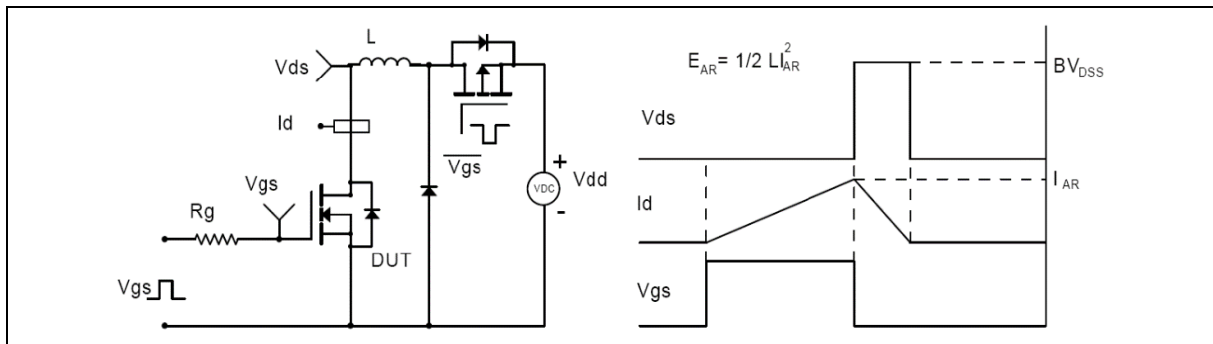
## Test circuits and waveforms



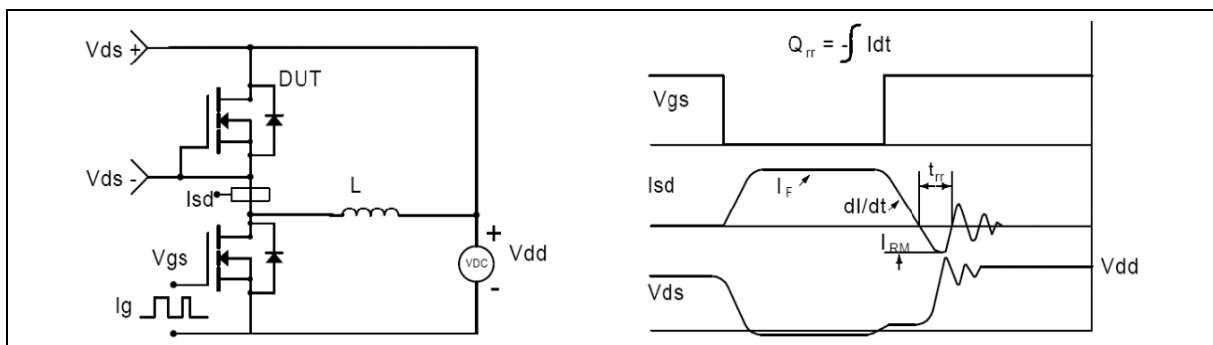
**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

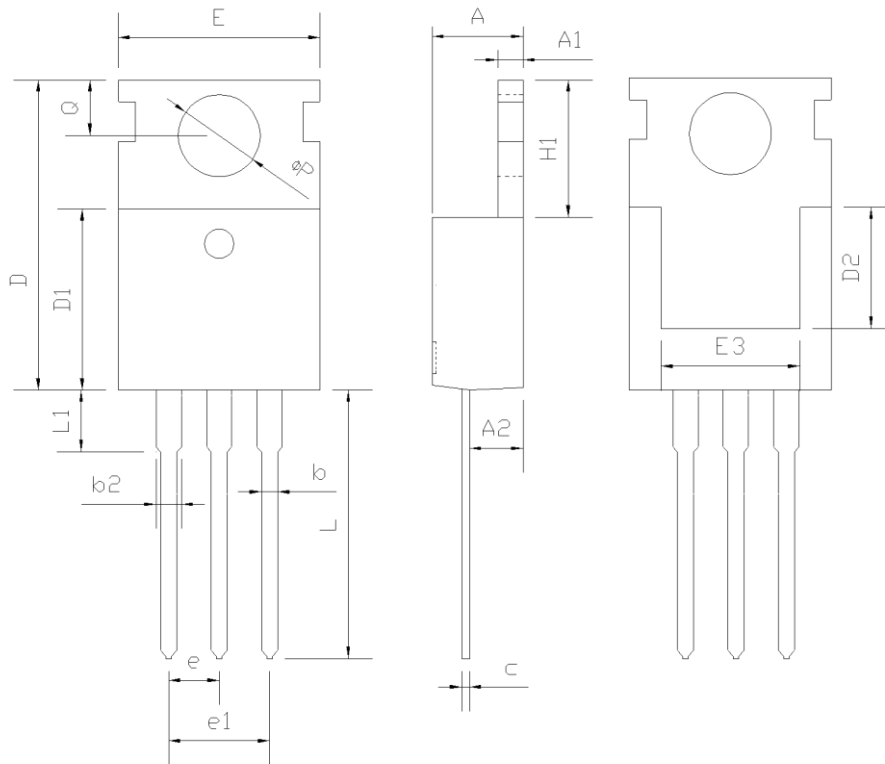


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

## Package Information



| Symbol | mm       |       |       |
|--------|----------|-------|-------|
|        | Min      | Nom   | Max   |
| A      | 4.37     | 4.57  | 4.77  |
| A1     | 1.25     | 1.30  | 1.45  |
| A2     | 2.20     | 2.40  | 2.60  |
| b      | 0.70     | 0.80  | 0.95  |
| b2     | 1.17     | 1.27  | 1.47  |
| c      | 0.40     | 0.50  | 0.65  |
| D      | 15.10    | 15.60 | 16.10 |
| D1     | 8.80     | 9.10  | 9.40  |
| D2     | 5.50     | -     | -     |
| E      | 9.70     | 10.00 | 10.30 |
| E3     | 7.00     | -     | -     |
| e      | 2.54 BSC |       |       |
| e1     | 5.08 BSC |       |       |
| H1     | 6.25     | 6.50  | 6.85  |
| L      | 12.75    | 13.50 | 13.80 |
| L1     | -        | 3.10  | 3.40  |
| ΦP     | 3.40     | 3.60  | 3.80  |
| Q      | 2.60     | 2.80  | 3.00  |

Version 1: TO220-P package outline dimension

**Ordering Information**

| Package Type | Units/ Tube | Tubes/ Inner Box | Units/ Inner Box | Inner Boxes/ Carton Box | Units/ Carton Box |
|--------------|-------------|------------------|------------------|-------------------------|-------------------|
| TO220-P      | 50          | 20               | 1000             | 6                       | 6000              |

**Product Information**

| Product      | Package | Pb Free | RoHS | Halogen Free |
|--------------|---------|---------|------|--------------|
| OSG65R340PZF | TO220   | yes     | yes  | yes          |

**Legal Disclaimer**

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Oriental Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

For further information on technology, delivery terms and conditions and prices, please contact the Oriental Semiconductor sales representatives ([www.orientalsemi.com](http://www.orientalsemi.com)).

© Oriental Semiconductor Co.,Ltd. All Rights Reserved